

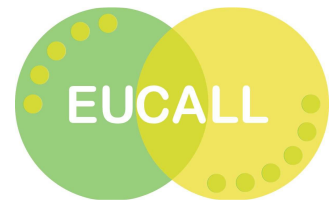
EUCALL GPU / FPGA Workshop
15-18th November 2016

Venue: E1.173, XHQ
European XFEL, Holzkoppel 4, 22869 Schenefeld, Germany

Tuesday 15 November 2016

Time	Programme
12:00	Reception
12:30	<i>Welcome note & brief introduction of XFEL – European XFEL</i>
12:50	<i>Brief introduction of workshop – European XFEL</i>
13:00	<i>GPU session – HZDR</i>
14:45	Coffee break
15:00	<i>GPU session – HZDR</i>

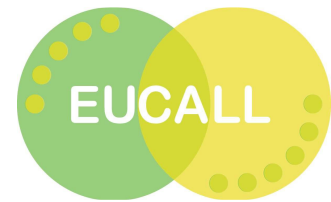




Wednesday 16 November 2016

Time	Programme
09:00	<i>GPU session</i> – HZDR
10:30	Coffee break
10:45	<i>GPU session</i> – HZDR
12:00	Lunch break
13:00	<i>GPU Q&A session</i> – HZDR
13:30	<i>FPGA session</i> – European XFEL
15:00	Coffee break
15:15	<i>FPGA session</i> – European XFEL
17:00	European XFEL facility visit

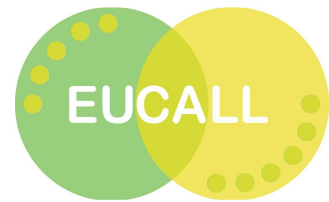




Thursday 17 November 2016

Time	Programme
09:00	<i>FPGA session – European XFEL</i>
10:30	Coffee break
10:45	<i>FPGA session – European XFEL</i>
12:00	Lunch break
13:00	<i>FPGA Q&A session – European XFEL</i>
13:30	<i>Keynote session</i>
15:00	Coffee break
15:15	<i>Keynote session</i>
18:00	<i>Exchange of information will be continued on dinner</i>





Friday 18 November 2016

Time	Programme
09:00	<i>Keynote session</i>
10:15	Coffee break
10:30	<i>Keynote session</i>
11:45	<i>Closing remarks – European XFEL</i>
12:00	End of workshop

