



# 3rd MicroTCA Workshop for Industry and Research

## Wednesday 10 December 2014

### Applications in research facilities - FLASH II Hall (16:00-18:00)

-Conveners: Vollrath Dirksen

time	[id] title	presenter
16:00	[39] Status of MicroTCA at DESY and XFEL	Mr REHLICH, Kay
16:15	[29] uTCA and MTCA.4 hardware used at KEK	Dr MICHIZONO, Shinichiro
16:30	[30] A MicroTCA Power Module Test Pad and a short status report on the MTCA evaluation project at CERN	Mr MENDEZ, Julian
16:45	[21] SLAC National Accelerator Laboratory MicroTCA.4 Collaboration	YOUNG, A
17:00	[1] Open Hardware MTCA development at Creotech Instruments SA and WUT	Dr KASPROWICZ, Grzegorz Kasprowicz
17:15	[25] MicroTCA.4-based Timing System used at XFEL and FLASH/FLASH2	STECHMANN, Christoph
17:30	[40] MicroTCA.4 Event Receiver for MRF Timing System	Mr PIETARINEN, Jukka
17:45	[5] An MTCA White Rabbit Timing Receiver for FAIR	Mr MEHLE, Marko

# Thursday 11 December 2014

## Applications in research facilities - FLASH II Hall (14:15-14:30)

-Conveners: Raymond Larsen

time	[id] title	presenter
14:15	[26] Conceptual Design of LCLS II BPM System on MicroTCA.4	Mr XU, Chengcheng

## Applications in research facilities - FLASH II Hall (16:15-17:45)

-Conveners: Raymond Larsen

time	[id] title	presenter
16:15	[35] FLASH2/XFEL machine protection system, experience	Mr JAEGER, Juergen M.
16:30	[54] DESY Board Development Stage and Licensing	Mr FENNER, Michael
16:45	[13] Precision regulation for SRF cavities using MTCA.4	Mr RUTKOWSKI, Igor
17:00	[22] MicroTCA.4-based BPM and orbit feedback systems at Sirius	Mr BRUNO, Gustavo
17:15	[10] MTCA.4 based LLRF system using direct sampling method	Mr GRZEGRZÓŁKA, Maciek
17:30	[7] Test-Stand for High-Performance FPGA Computing Module	Mr MIELCZAREK, Aleksander