



Science & Technology Facilities Council

Technology

Atlas Pixel Meeting
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Strip HR-CMOS

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Submitting test structure for Atlas Strip Tracker in TJ180nm process

Starting material (list almost final):

P epi (intrinsic for high res) on P substrate

- 5 um epi, low res (standard TJ material)

- 12 um epi, low res

- 12 um epi, high res

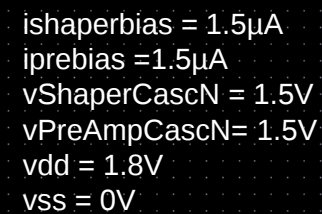
- 18 um epi, high res

- 25 um epi, high res

P epi (intrinsic for high res) on N substrate

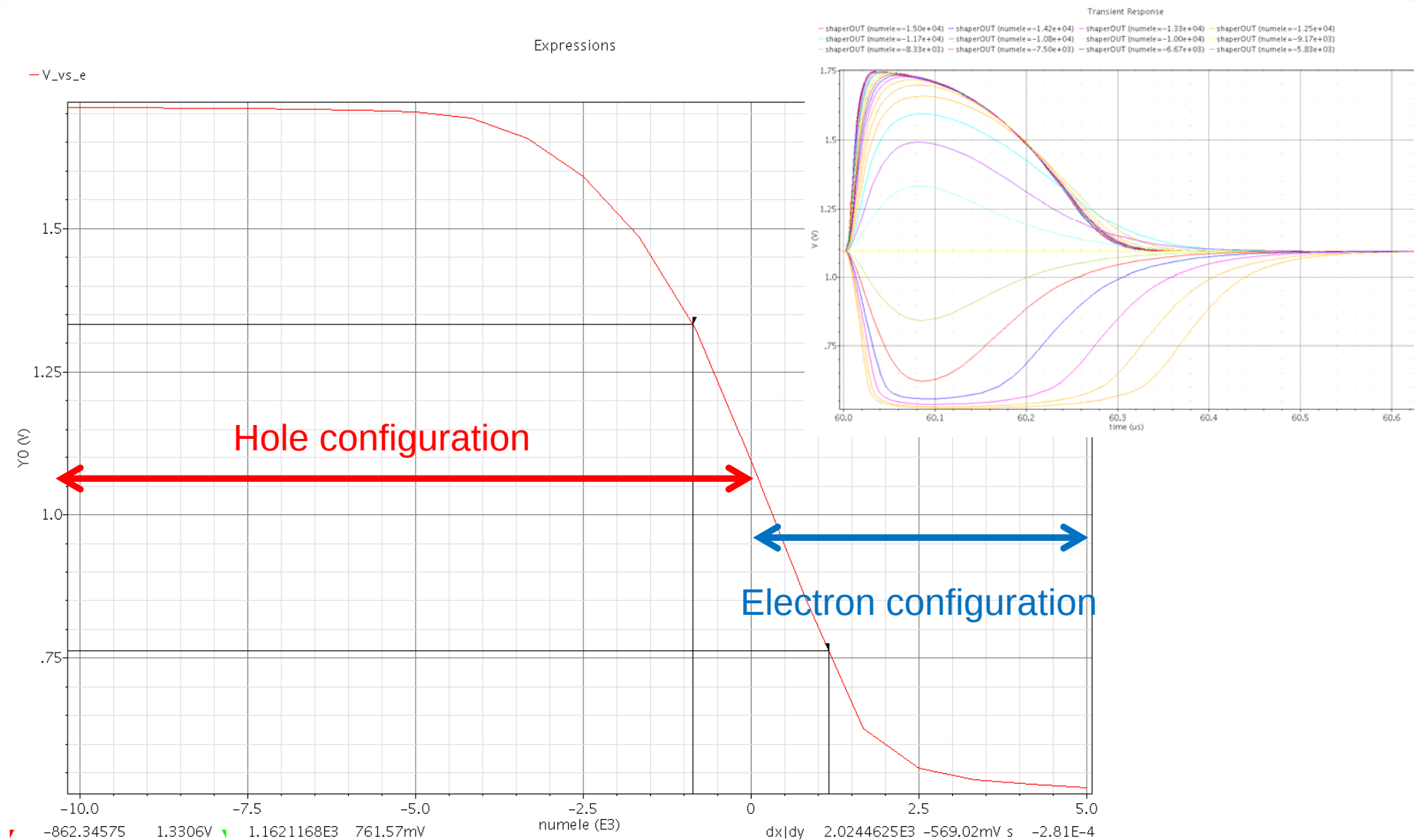
- 25 um epi, high res

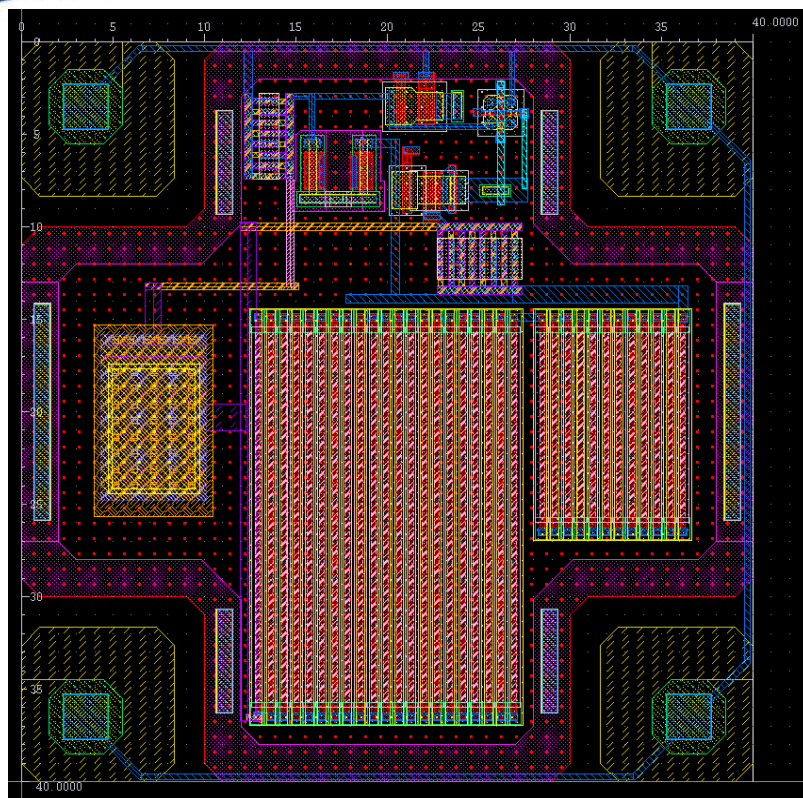
Shaper



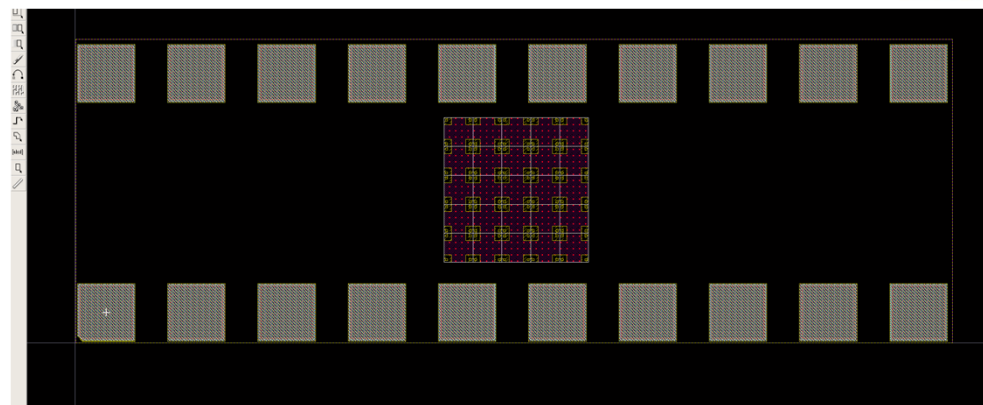


Output transfer curve

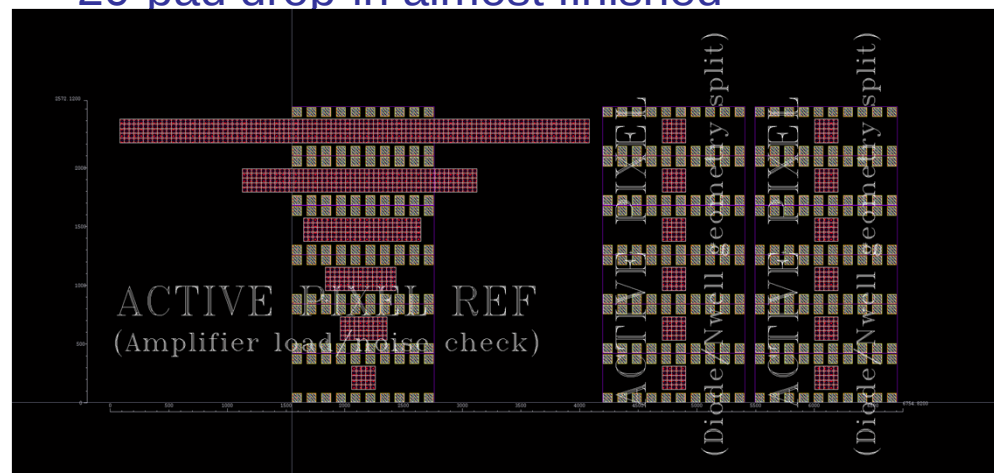




Baseline pixel ready



20-pad drop-in almost finished



Variants to be developed for P-on-N and P-on-P pixels