Minutes of Strip CMOS sensor progress report meeting

*2014-09-19*

Present: T. Hoffman, J. John, V. Fadeyev, J. Doppke, C. Buttar, R. Nickerson, M. Warren

Apologies: A. Grillo

Meeting timing and content was discussed. This was unusual time (Friday), due to CPIX workshop. Future meetings will happen on Tuesdays, as before.

The other difference of this and future meetings, compared to the past agendas, is that the overall status reports are kept to a minimum to devote the bulk of the discussions to the testing.

Alex sent a report by email. A key aspect of the architectural submission in November is data readout, where occupancies dictate the speed and bandwidth required. This was discussed in Working Group one early in the year. However, now is the time to design a concrete circuit, so the data and conclusions are being reviewed again. Jens and Richard asked to be included in the discussions.

Tower Jazz submissions: There was a chip review the day before. 3 chips are being designed. There will be another review in October.

Previously designed Tower Jazz structures are being tested at RAL. A report was given with pre-radiation test results, where threshold voltages were derived for different transistor geometries. The gamma irradiations to follow at RAL.

Motherboard for the HVstripv1 chip is being designed at Oxford. The design is very close to final. The review to be done next week. Agreed to produce 15 boards. It is desirable to load them all with components at the same place. Oxford will check their loading availability.

Firmware for the chip readout is nearly ready. Matt will need to sit down with the FPGA board and the chip to hammer out the last kinks at some point. This is anticipated to be quick.

Atlys boards for the Ivan’s chip are being ordered at some places.

Testing the CHESS-AMS chip was brought up. Jens asked for the latest specs document to be uploaded to the agenda page to start thinking about the carrier board, etc.