

# CMOS Testing

Plans & Discussion  
By  
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# HVStrip1 (Aka Ivan's Chip)

- It's working (First results today)
  - Can now proceed to “clone setups”
- 4 Motherboards “in the wild”
  - RAL, Oxford, DESY, Glasgow
- 2 More requests
  - Santa Cruz, Argonne
  - Liverpool & Ljubljana
  - Anyone else ?

# Testing CHESS

- Suggestion to make a few bare die tests
  - Who would be interested ?
- Designing a motherboard
  - To enable testing for everyone
- Questions
  - a) Who gets the whole batch of chips (and distributes them)
  - b) Motherboard design

# HVStrip1 Testing plans

- With the current set of non-irradiated sensors
  - Noise, Charge Collection Speed, Gain, digital functionality, Thresholds..
- Reordering more
  - “Aimed at irradiation campaign”