

Readout Electronics / ASICs in DTS

Monday 23 February 2015

Session 1: Current Design Activities - Sem Room IV Bldg 99 (CFEL) (13:00 - 15:50)

time	[id] title	presenter
13:00	[0] Welcome	Dr TRUNK, Ulrich
13:10	[1] Chip Design Activities and Capabilities at KIT	Mr LEYS, Richard
13:30	[2] Discussion	
13:40	[3] Chip Design Activities at DESY	Dr TRUNK, Ulrich
14:00	[4] Discussion	
14:10	[5] Chip Design Activities at GSI	Dr SCHMIDT, Christian J. Dr FLEMMING, Holger
14:30	[7] Discussion	
14:40	Coffee Break	
15:10	[6] 65nm CMOS Developments at FZJ	Dr KRUTH, Andre
15:30	[8] Discussion	