

ATLAS Strip CMOS Regular Meeting

Tuesday 20 January 2015

ATLAS Strip CMOS (17:00 - 18:55)

time	[id] title	presenter
17:00	[0] Intro	Mr STANITZKI, Marcel Prof. NICKERSON, Richard FADEYEV, Vitaliy
17:05	[6] Architectural chip design	Dr GRABAS, Herve
17:15	[1] Testing HVStripV1	Prof. BUTTAR, Craig Dr JENS, Dopke JOHN, Jaya John KANISAUSKAS, KESTUTIS VIGANI, Luigi Prof. HUFFMAN, Todd
17:30	[2] Testing CHESS1	MANDIC, Igor
17:40	[5] Testing CHESS1 Active circuitry	Dr GRABAS, Herve
17:50	[3] Status of test kit and its distribution; CHESS daughter board	Prof. TODD, Huffman JOHN, Jaya John
18:10	[4] Discussion	