

ATLAS Strip CMOS Regular Meeting

Report of Contributions

Contribution ID: 0

Type: **not specified**

Intro

Tuesday 3 February 2015 17:00 (5 minutes)

Presenters: Mr STANITZKI, Marcel (DESY); Prof. NICKERSON, Richard (Oxford University); FADEYEV, Vitaliy (SCIPP / UCSC)

Session Classification: ATLAS Strip CMOS

Contribution ID: 1

Type: **not specified**

Architectural chip design

Tuesday 3 February 2015 17:05 (10 minutes)

Presenter: Dr GRABAS, Herve (UCSC)

Session Classification: ATLAS Strip CMOS

Contribution ID: 2

Type: **not specified**

Testing HVStripV1

Tuesday 3 February 2015 17:15 (15 minutes)

Presenters: Prof. BUTTAR, Craig (University of Glasgow); Dr JENS, Dopke (RAL); JOHN, Jaya John (Oxford); KANISKAUSKAS, KESTUTIS; VIGANI, Luigi (Oxford); Prof. HUFFMAN, Todd (Oxford University)

Session Classification: ATLAS Strip CMOS

Contribution ID: 3

Type: **not specified**

Testing CHESS1

Tuesday 3 February 2015 17:30 (10 minutes)

Presenter: MANDIC, Igor

Session Classification: ATLAS Strip CMOS

Contribution ID: 4

Type: **not specified**

Fast low-noise (external) amplifier

Tuesday 3 February 2015 18:00 (10 minutes)

Presenter: NORGREN, Matt (UCSC)

Session Classification: ATLAS Strip CMOS

Contribution ID: 5

Type: **not specified**

Status of test kit and its distribution; CHESS daughter board

Tuesday 3 February 2015 18:10 (20 minutes)

Presenters: Prof. TODD, Huffman; JOHN, Jaya John (Oxford)

Session Classification: ATLAS Strip CMOS

Contribution ID: 6

Type: **not specified**

Discussion

Tuesday 3 February 2015 18:30 (20 minutes)

Session Classification: ATLAS Strip CMOS

Contribution ID: 7

Type: **not specified**

Testing CHESS1

Tuesday 3 February 2015 17:40 (20 minutes)

Presenters: LIANG, Zhijun (UCSC); Dr LIANG, Zhijun (UCSC)

Session Classification: ATLAS Strip CMOS