

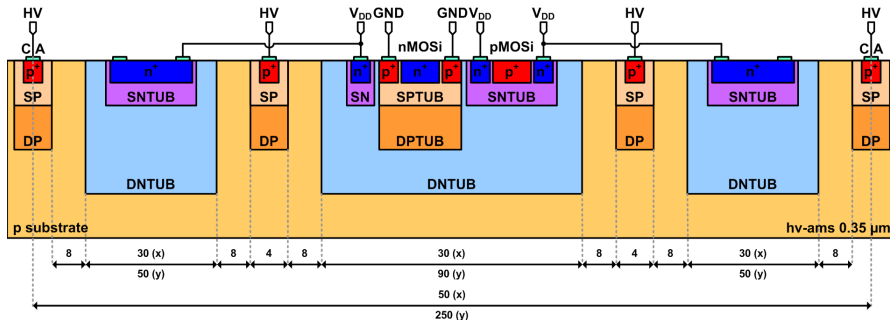
Update on TCAD Simulations of CMOS Sensors

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Vagelis Gkougkousis (LAL)
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CMOS Strip Meeting
CERN, 09 Jun. 2015



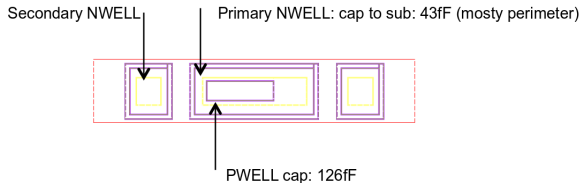
- Geometric structure
- Depletion and field properties
- Radiation effects
- Back biasing
- Transient simulation with a MIP
- Summary and outlook



Eva Vilella-Figueras

- VSS: 0.0 V
- VDD: 3.3 V
- HV: 0 – -200 V

- Resistivity: 20, 80, 200, 1000 Ωcm
- Fluence: 0, 1e14, 1e15, 1e16 n_{eq}
- Top bias without back process
- Back bias with floating top contacts



M. Tavassoli

Results of measurements with PixCap chip:

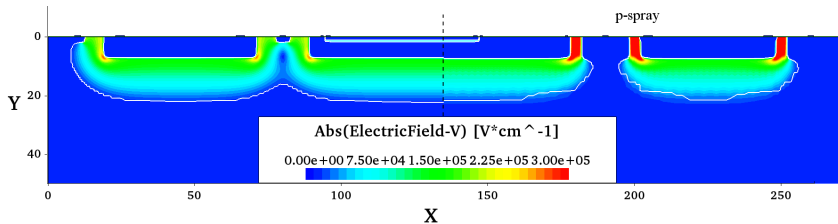
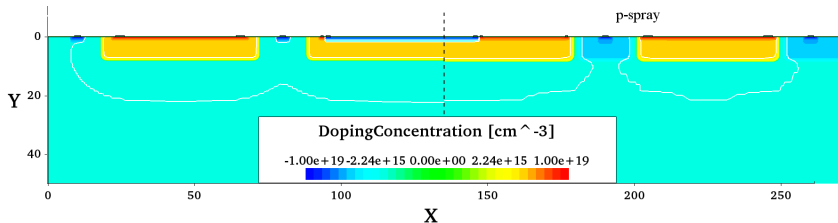
Pixel size: 50 μm x 250 μm	Planar	Diam ond	3D
[fF]	105.5 1.8 111.7 3.8	21.4 0.1	169.4 1.5

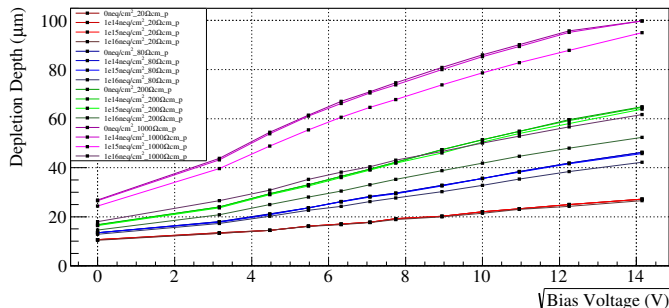
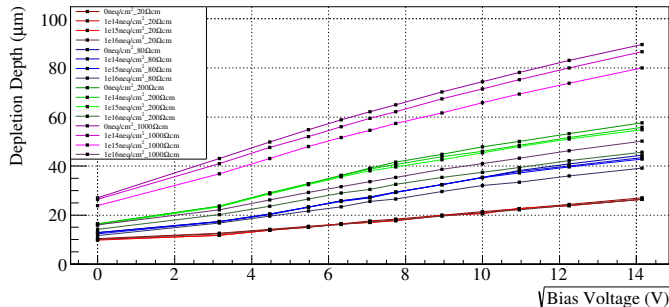
"Measurement of pixel sensor capacitances with sub-femtofarad precision", M. Havranek, F. Hügging, H. Krüger, N. Wermes – NIMA 714 (2013), 83-89

- Estimated large capacitance
- Discontinuous n-wells to reduce capacitance

Extra deep p-well (M. Benoit in comm. with AMS)

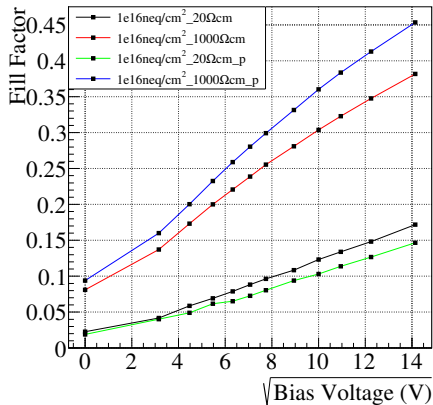
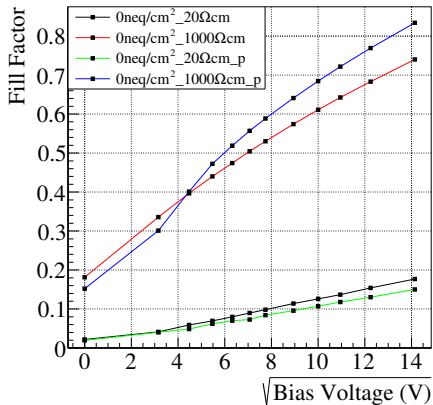
- Inverting DNTUB mask
- Same doping concentration as DNTUB

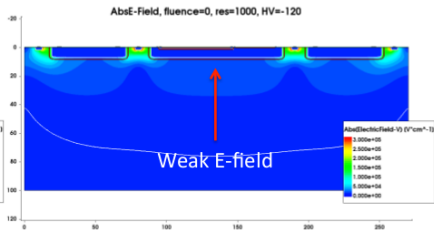
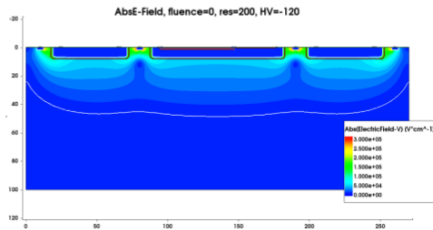
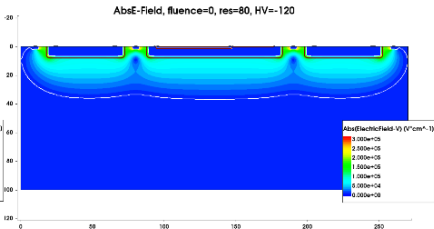
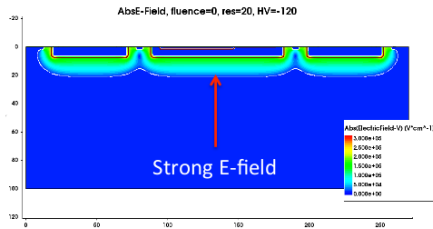




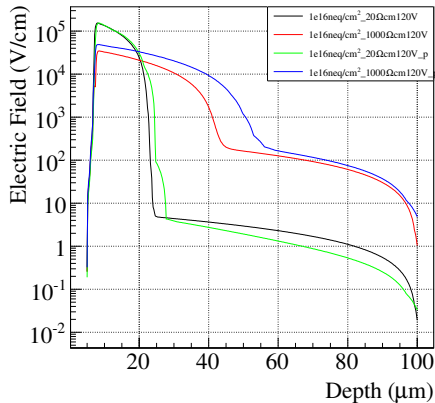
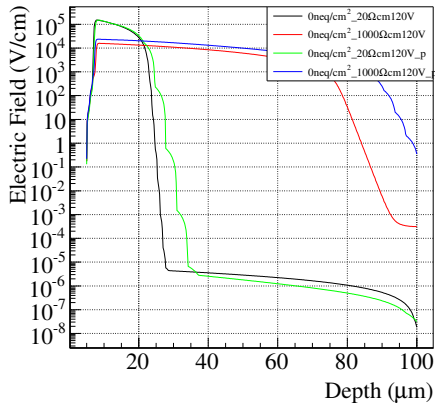
1000 Ωcm
200 Ωcm
80 Ωcm
20 Ωcm

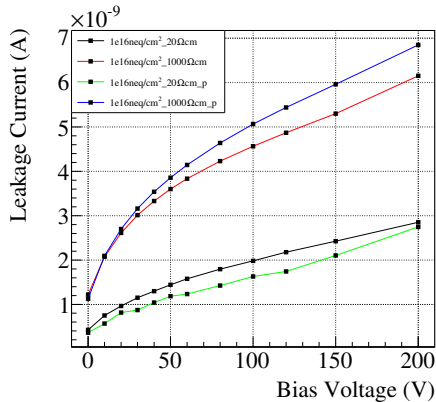
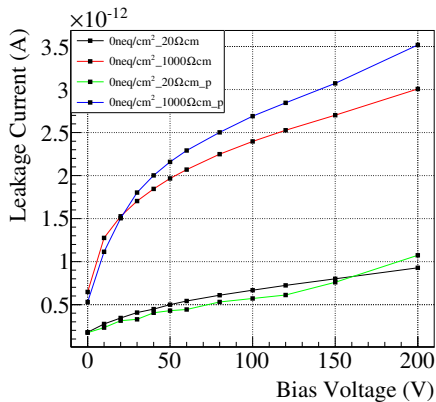
P-well:
Larger depletion
depths for higher
resistivities

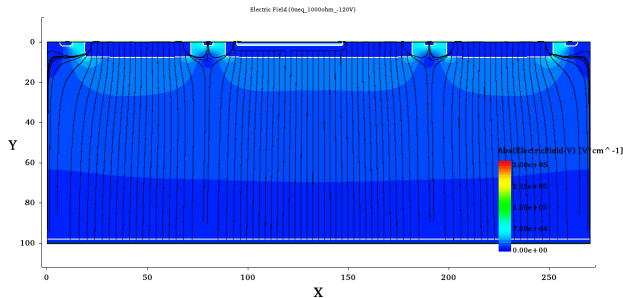
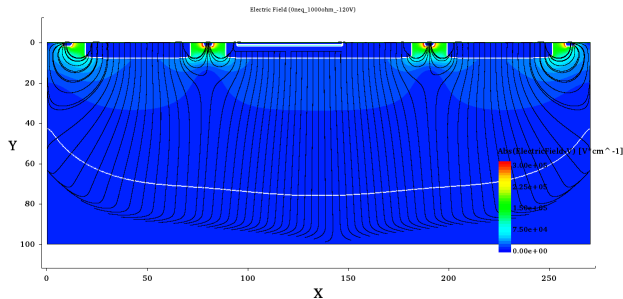




M. Buckland

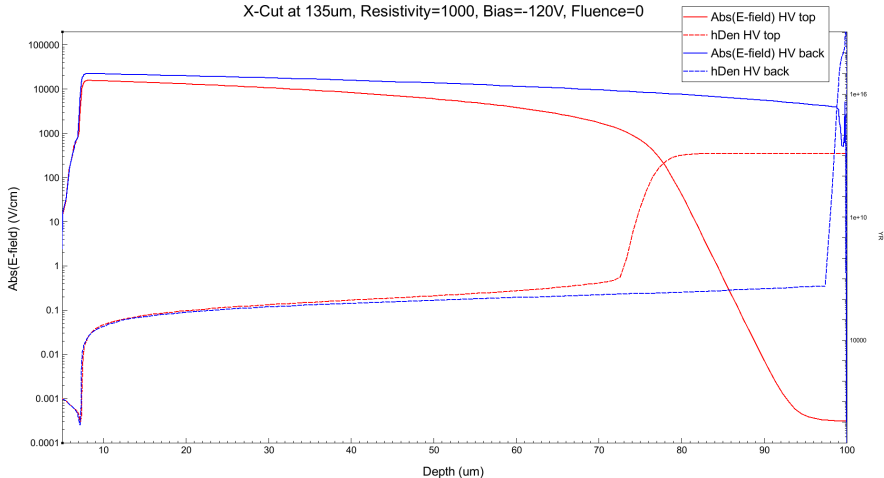






- 1000 Ωcm @ -120 V
- Top contacts set to floating
- Back processing with shallow p-well, p+ and aluminium

X-Cut at 135 μ m, Resistivity=1000, Bias=-120V, Fluence=0



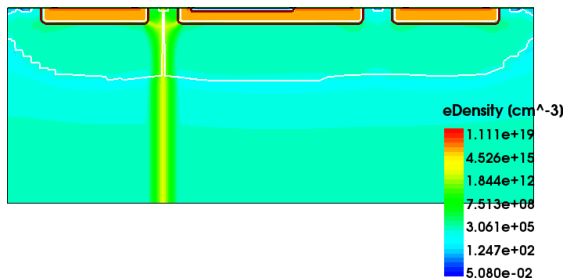
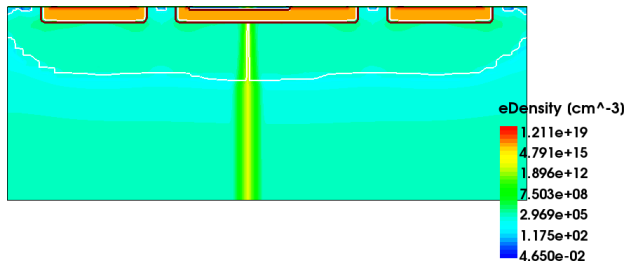
M. Buckland

Top bias:

- No electric field at the back
- Depletion depth $\sim 70\mu\text{m}$

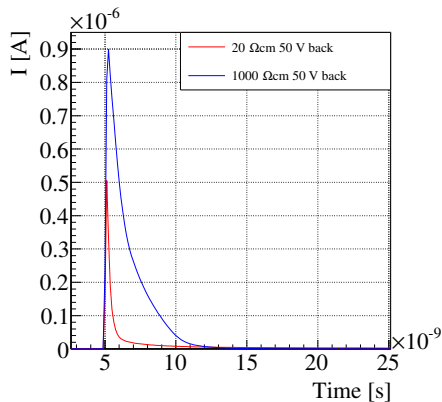
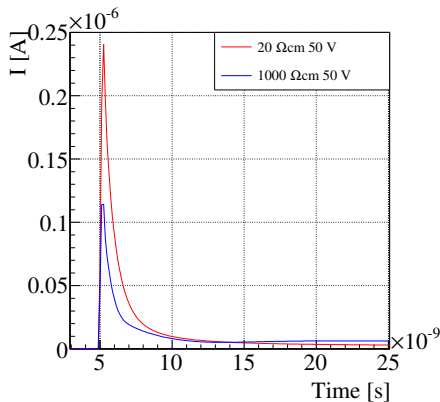
Back bias:

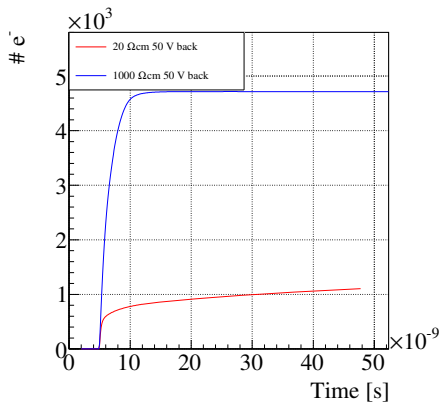
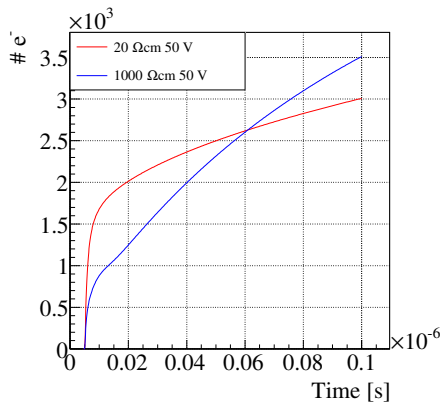
- Uniform electric field
- Fully depleted

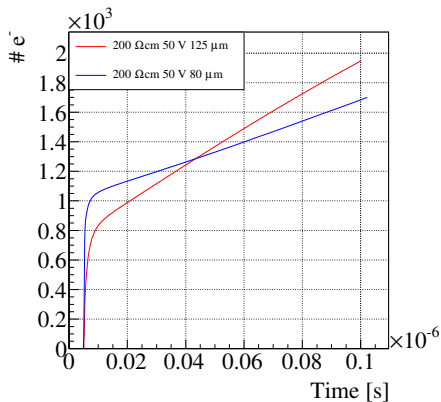
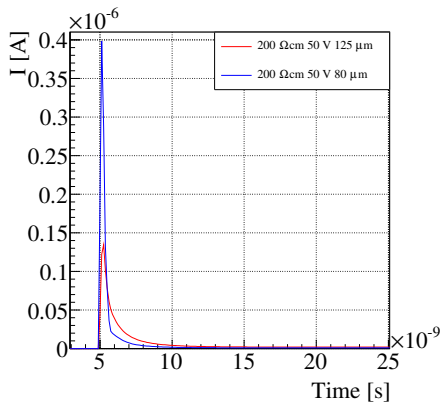


- MIP in the middle of a subpixel
- MIP between two subpixels
- Top and back bias
- 80 e-h pairs/ μm

F. Di Bello







- P-wells:
 - improved depletion depth for HR materials
 - wider electric field
 - higher leakage current
- Back bias:
 - uniform electric field
 - larger depletion
 - efficient charge collection
- Radiation effects on depletion depth of low resistivity (maybe) inconsistent with measurements
 - Check on N_{eff} vs. fluence
- Transient simulation: charge sharing and crosstalk on multipixel
- Capacity simulation
- Improve current model, e.g. by measuring the doping profile

