

3rd International Summer School on INtelligent Signal Processing for
FrontlEr Research and Industry



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VERTICAL INTEGRATION in NEW DEVICES

Wednesday 16 September 2015 13:30 (1h 30m)

This is the first lecture dedicated to the VDSM and 3D vertical interconnects, the second one dedicated specifically to the VDSM technology will be given on September 23, by G. Deptuch (FNAL)

Presenter: Prof. JONES, Matthew (University of Purdue, USA)

Session Classification: Introduction session on Advanced Very Deep Sub Micron CMOS technologies, 3D technologies and Industry trends