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**NEW FRONT-END ASIC DESIGN(s) for next  
generation of pixel based devices : How to handle  
with High granularity, high rate & high  
performances in the HEP case (HL-LHC)**

*Saturday 19 September 2015 10:50 (1h 15m)*

**Presenter:** Prof. GARCIA-SCIVERES, Maurice (LBL, USA)

**Session Classification:** Intelligent Silicon Trackers: Pixels and large area Si Trackers