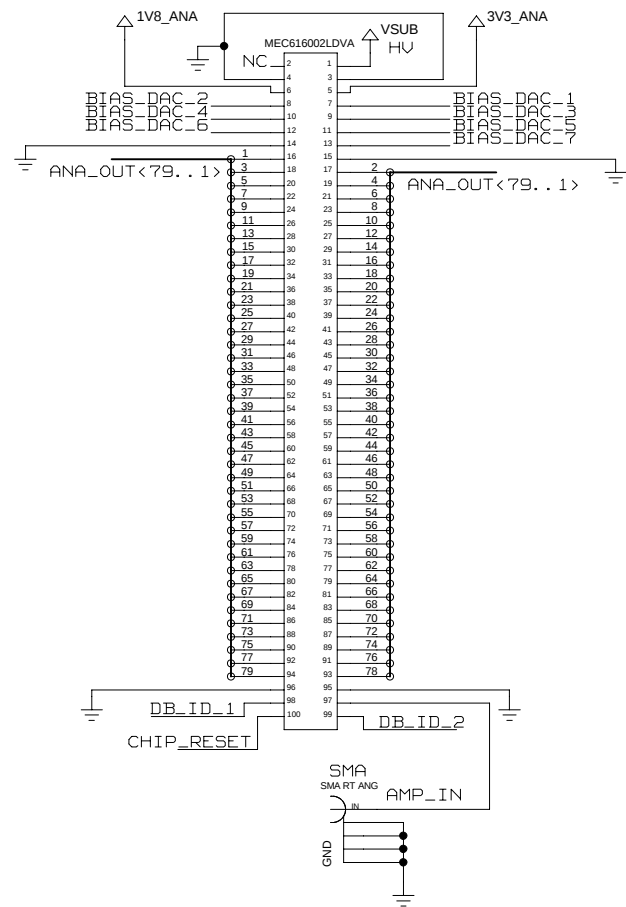
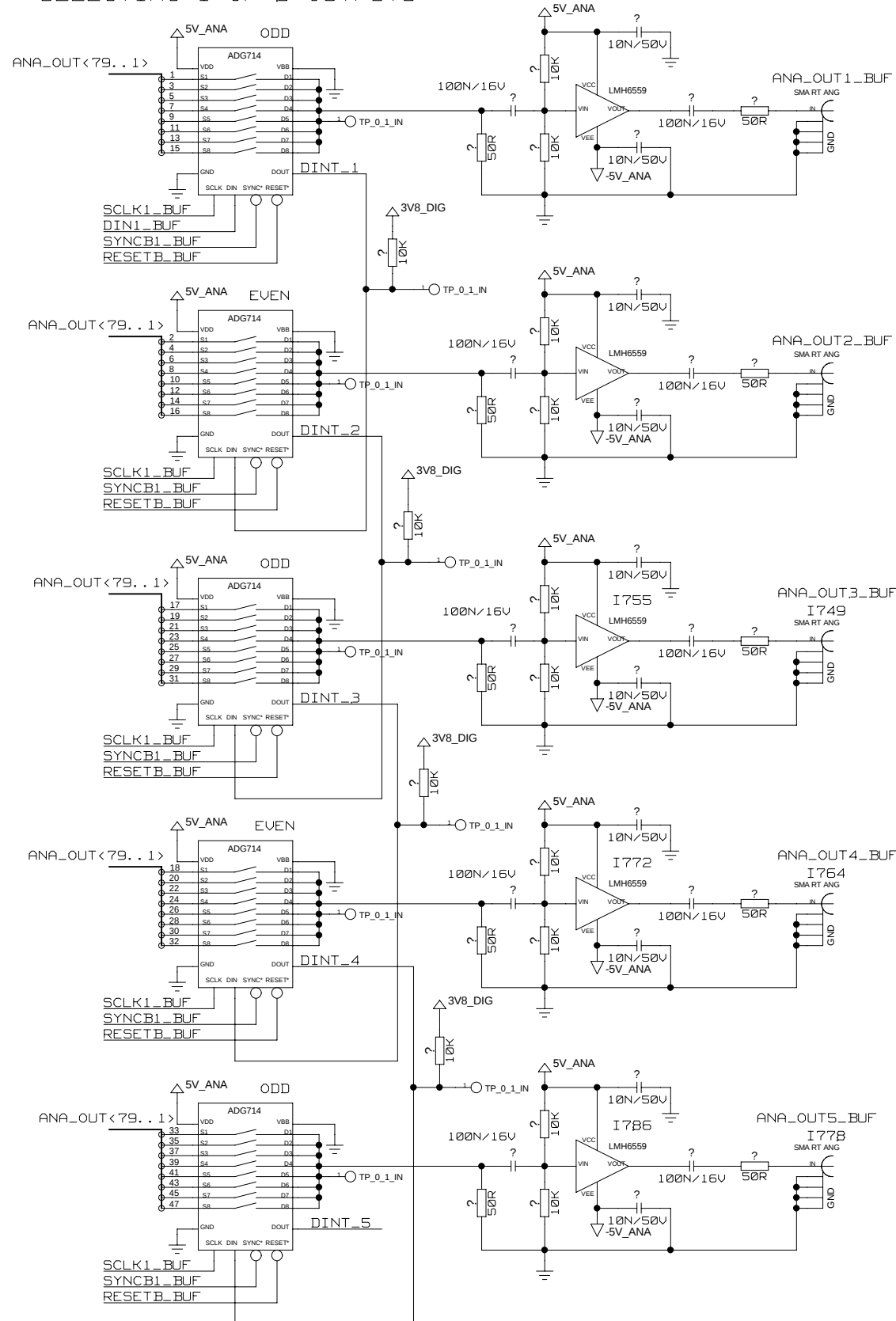


ANALOGUE SIGNALS

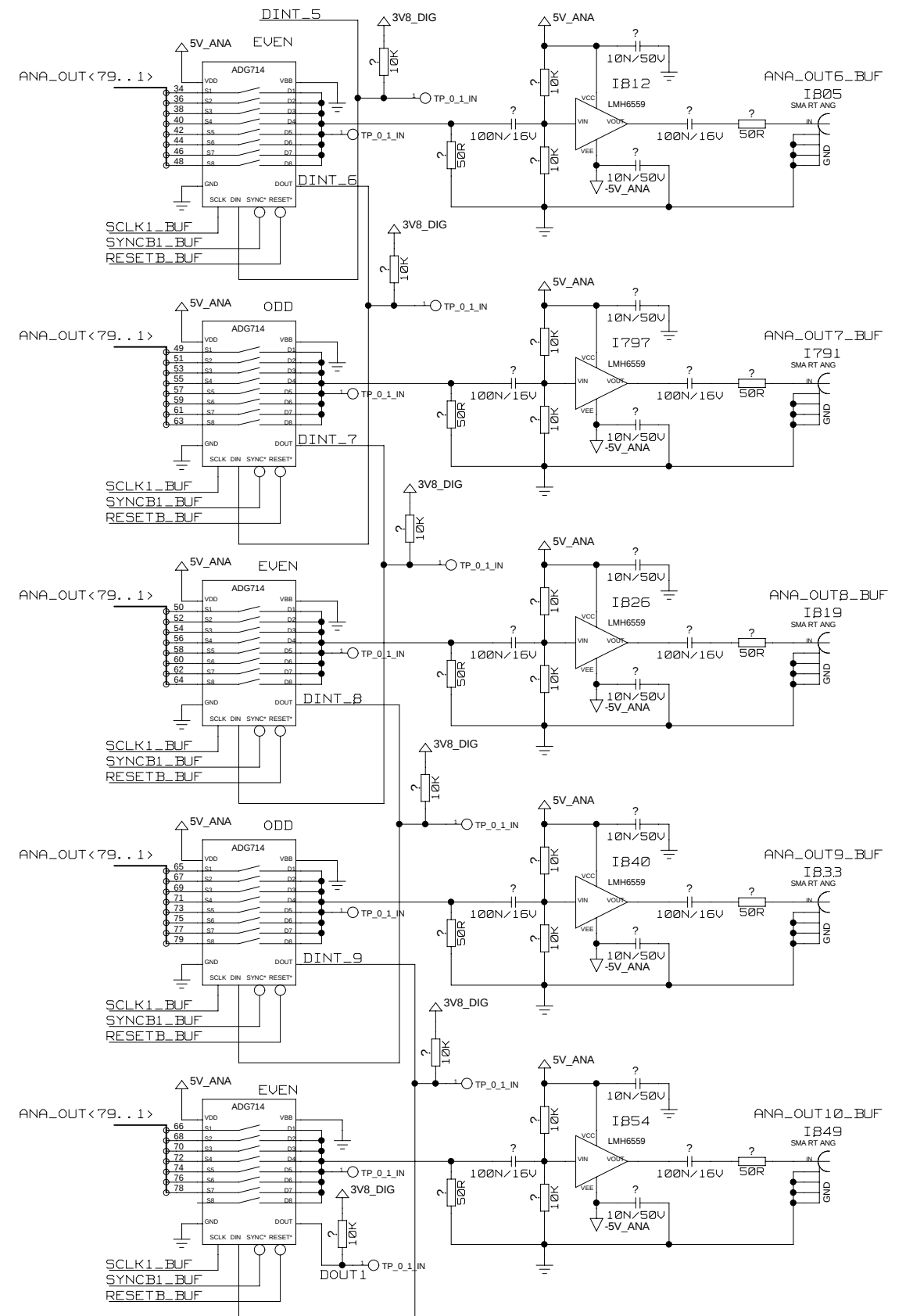
EDGE CONNECTOR



ANALOGUE SWITCHES
SELECTING 1 OF 8 OUTPUTS

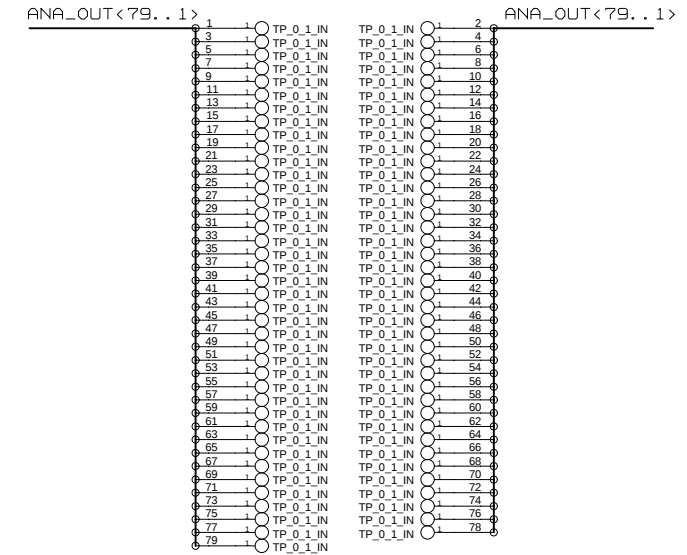


ANALOGUE BUFFERS



ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
USED ON					© OXFORD UNIV	
NUCLEAR & ASTROPHYSICS LABORATORY, KEBLE RD, OXFORD, OXON, OX1 3RH.						
TITLE	STRIP CMOS COLLABORATION CHESS-1 MOTHERBOARD					
A	2					

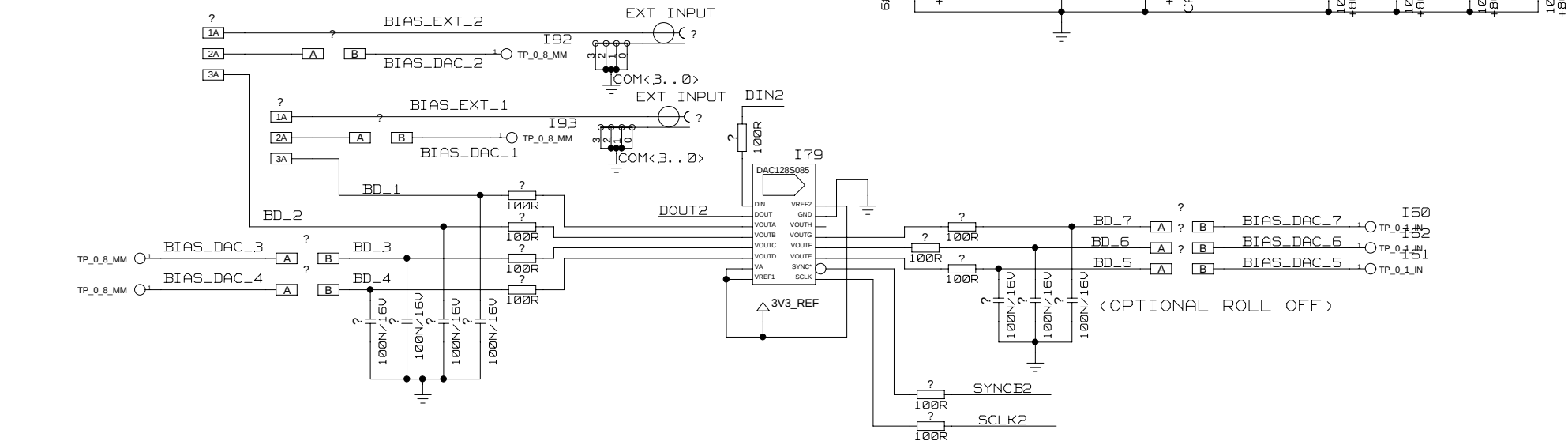
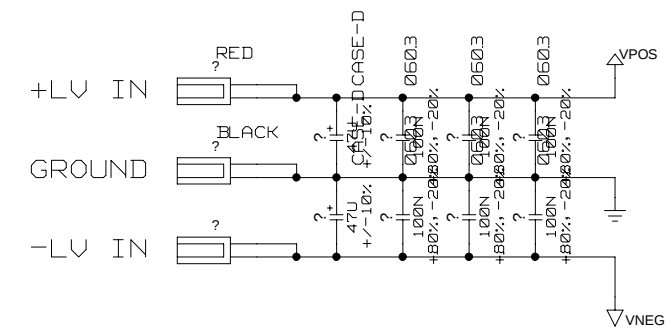
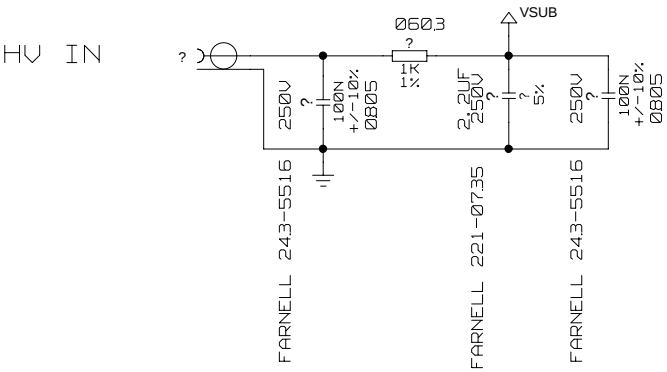
TEST POINTS ON ANALOGUE OUTPUTS



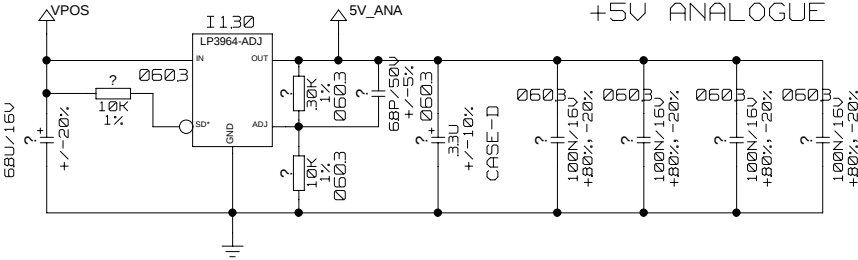
DAUGHTERBOARD ID BUFFERING - TO BE ADDED

A	2
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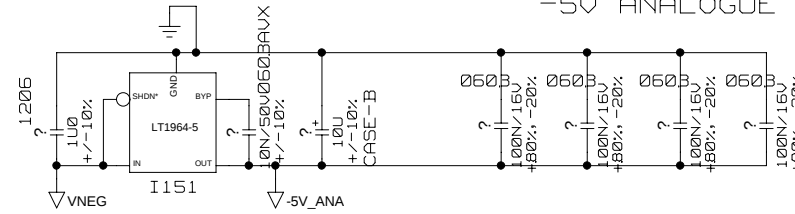
EXTERNAL POWER CONNECTORS



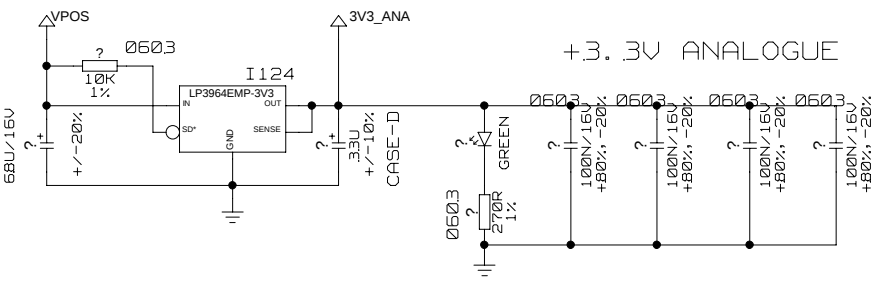
+5V ANALOGUE



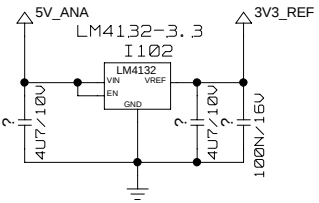
-5V ANALOGUE



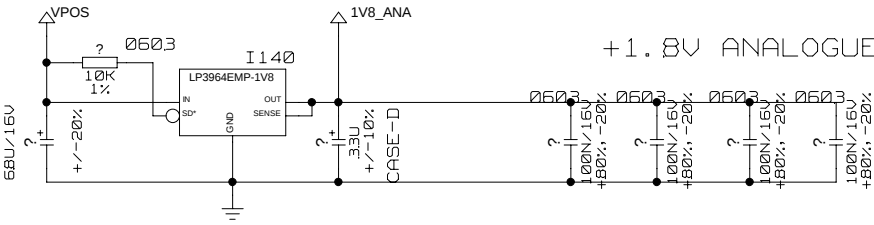
4.3. .3V ANALOGUE



+3.3V REFERENCE
FOR DAC ONLY



+1.8V ANALOGUE



ISSUE	DATE	MOD NO.	DRN BY.	CHKD.	APPD.	STATUS
USED ON					© OXFORD UNIV	
NUCLEAR & ASTROPHYSICS LABORATORY, KEBLE RD, OXFORD, OXON, OX1 3RH.						
TITLE	STRIP CMOS COLLABORATION CHESS-1 MOTHERBOARD					
A	2					