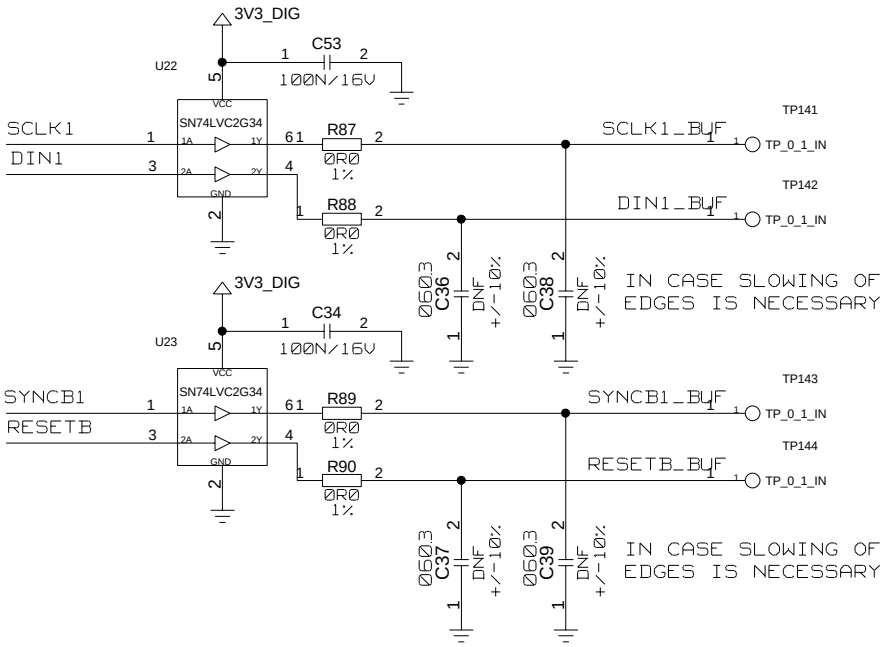
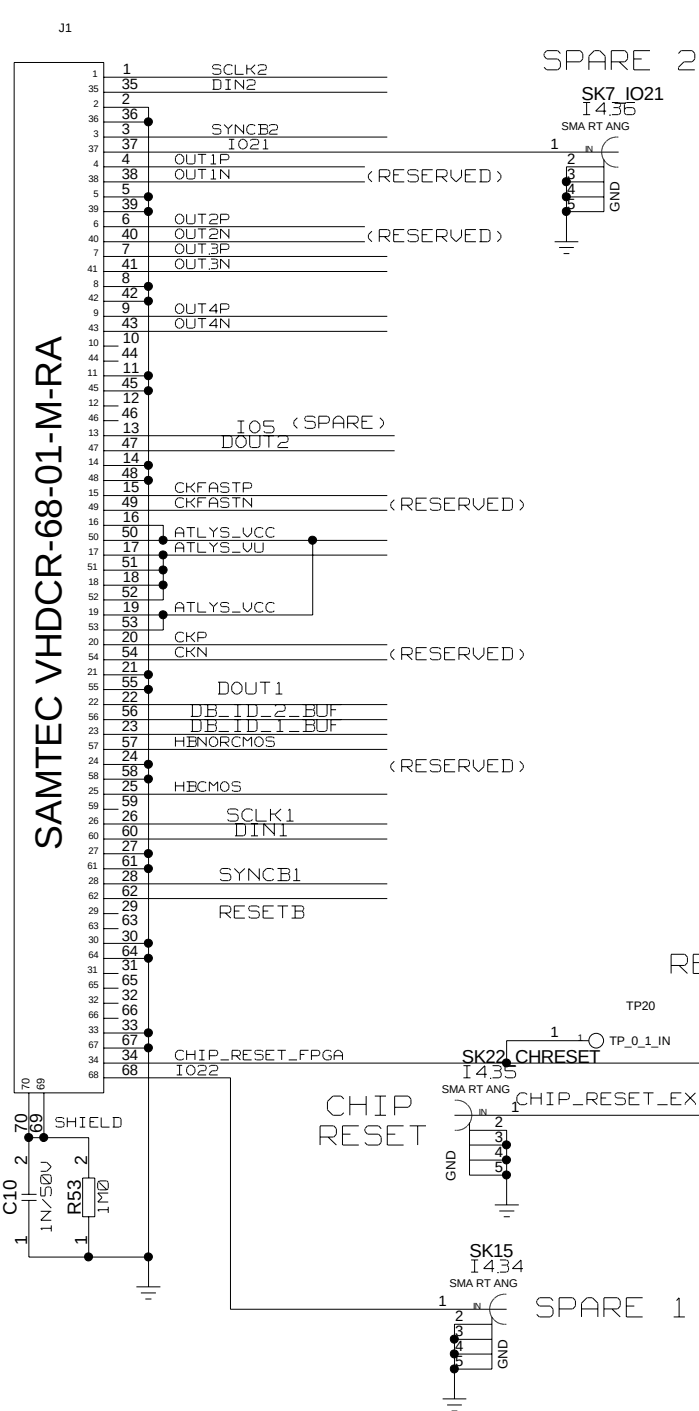


EDGE CONNECTOR
SAMTEC MEC6-150-02-L-DV-A
OR
SAMTEC MEC6-150-02-L-D-RA1

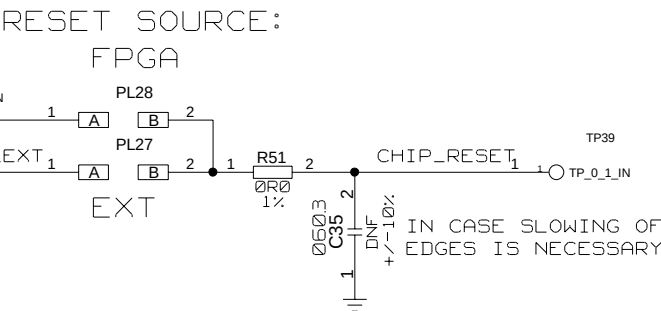
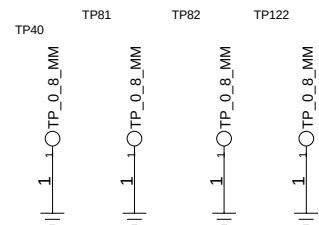


DAQ CONNECTOR
VHDCI - TO/FROM ATLYS

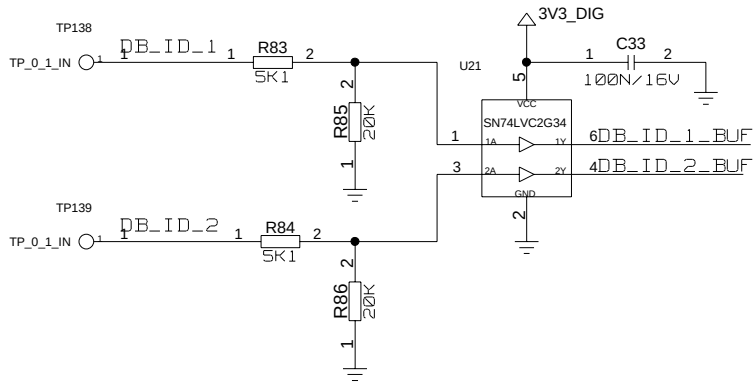
DIGITAL BUFFERS



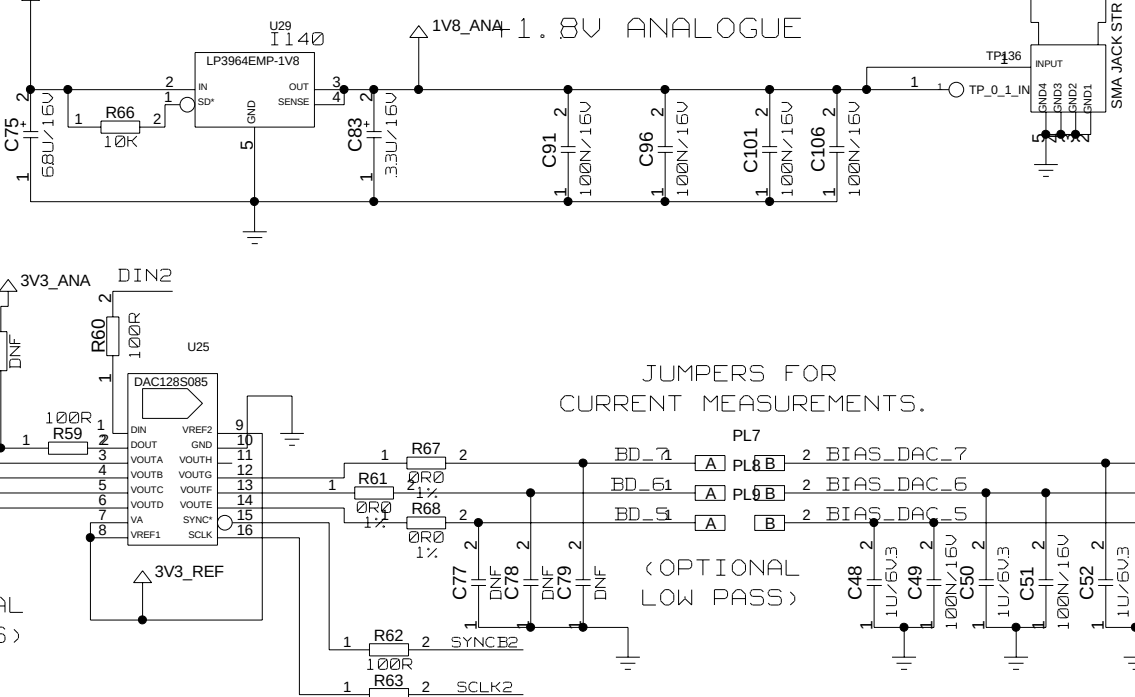
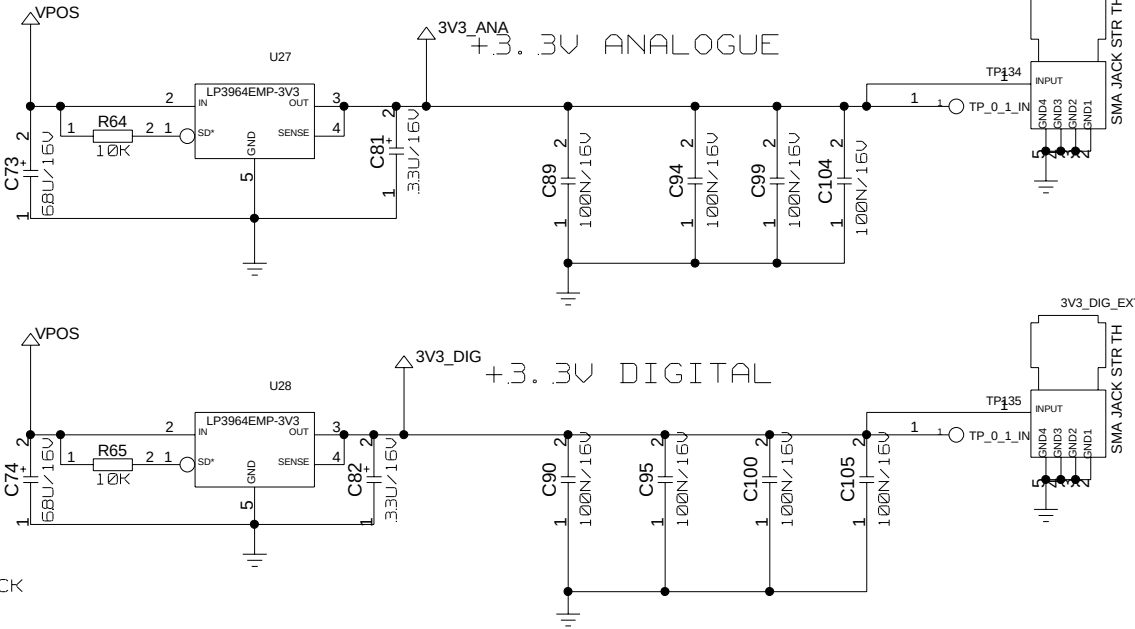
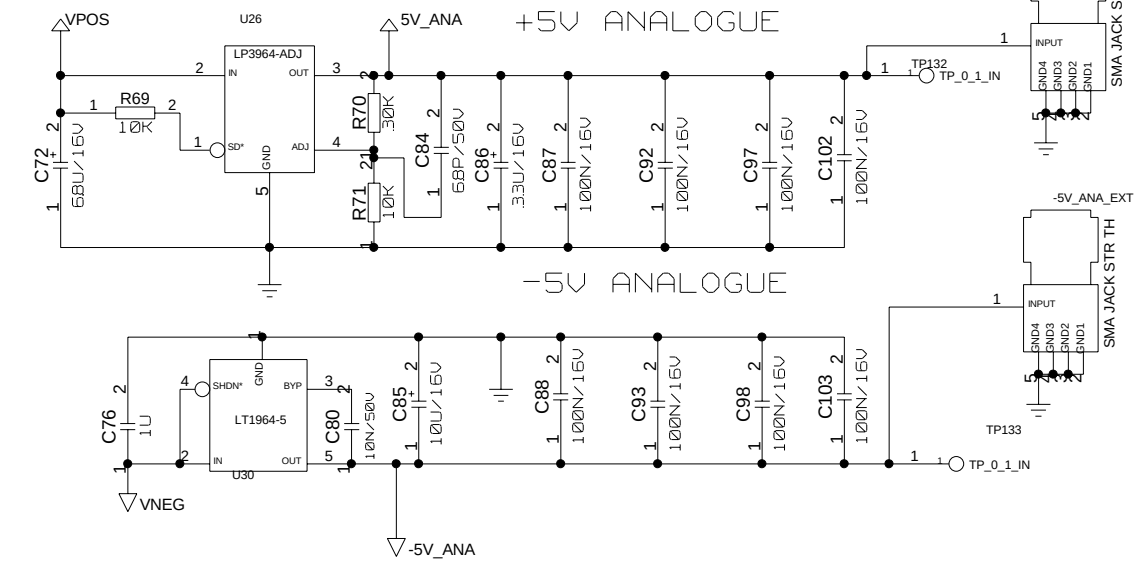
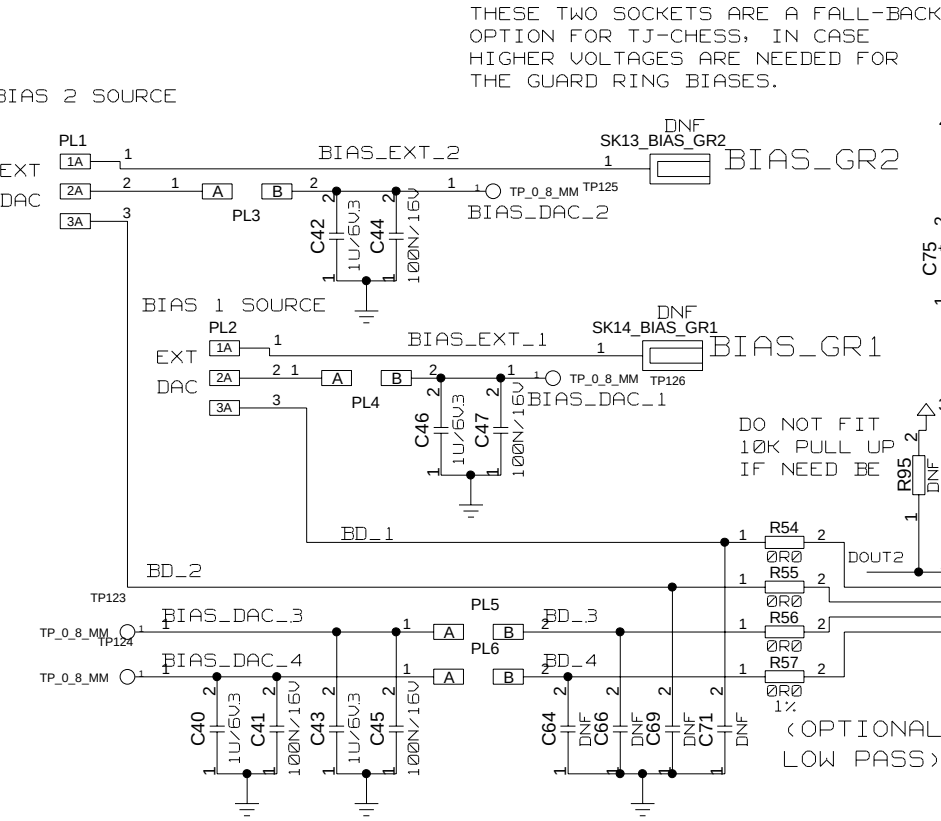
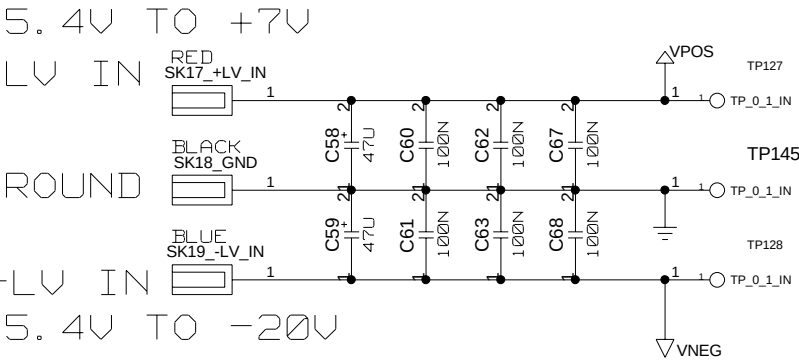
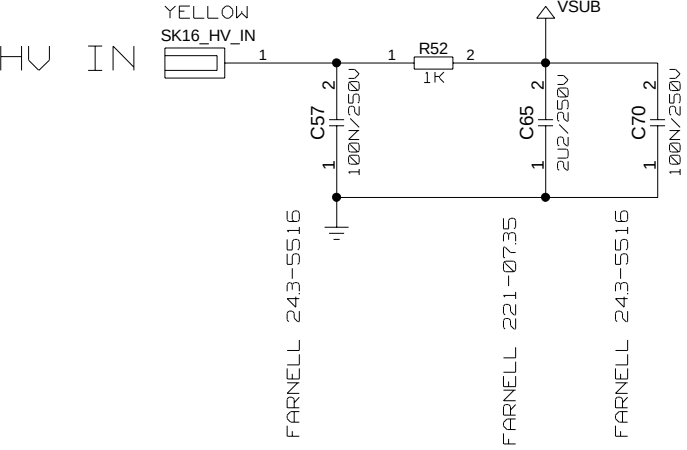
GROUND POINTS
IN CORNERS OF BOARD



DAUGHTERBOARD ID BUFFERING



POWER AND BIAS DAC



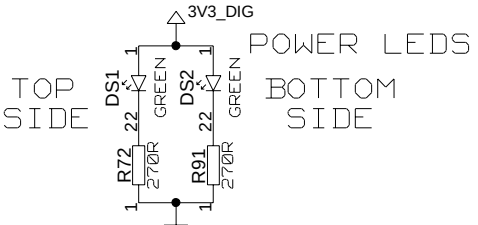
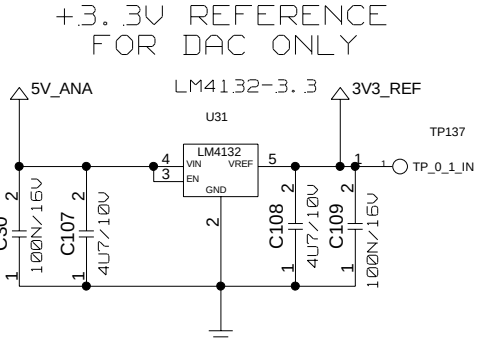
5V ANA BACK-UP
(DO NOT FIT)

-5V ANA BACK-UP
(DO NOT FIT)

3.3V ANA BACK-UP
(DO NOT FIT)

3.3V DIG BACK-UP
(DO NOT FIT)

1.8V ANA BACK-UP
(DO NOT FIT)



CENTRAL ELECTRONICS GROUP
UNIVERSITY OF OXFORD
DEPARTMENT OF PHYSICS
DENYS WILKINSON BUILDING
KEBLE ROAD, OXFORD, OX1 3RH

PROJECT:STRIP CMOS COLLABORATION
MODULE : top_blue
CHES-1 MOTHERBOARD
LAST CHANGE : Tue Aug 25 11:25:33 2015
SHEET 3 OF 3