

- Restructuring of work package started
- Second work package responsible at the stage to be installed
- Hardware work ongoing
- Turbulences of the FAIR / PANDA project also effects one of our milestones → Hopefully solved in near future

Years	Activity / Milestone	Participants
2017	Realisation of a multi-channel, silicon based electro-optical modulator	GSI, KIT
2017	Realisation of small scale DAQ prototypes for photon detection and user-driven imaging	DESY, FZJ, HZDR, KIT
2017	Development of the common technology platform based on Helmholtz AMC for DAQ, throughput-maximized data transfer and distributed data analysis	DESY, GSI, HZDR, FZJ, KIT
2018	First DAQ and event filter demonstrator based on the common technology platform using streaming data input for FAIR experiments	DESY, FZJ, GSI, KIT
2018	First DAQ and track finding demonstrator based on the common technology platform with content-addressable memories and TCA technology for CMS	DESY, KIT
2019	Prototype of an integrated electro-optical modulator and electronic driver chip	DESY, GSI, KIT
2019	Proving scalability of the DAQ demonstrators using CPU/GPU clusters	DESY, FZJ, HZDR, KIT