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CERN plans to evaluate MicroTCA for the SPS LLRF upgrade

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The LLRF of the CERN SPS will go through a complete renovation during the Long Shutdown 2019-2020. The upgrade is driven by the required performances as injector for the LHC:

For protons, the bunch intensity must be doubled (2.2E11 p/bunch at transfer to LHC), requiring major modifications to the accelerating system. On the LLRF side, the active compensation of transient beam loading must be improved (Feedforward, One Turn Delay feedback, Longitudinal damper).

For LHC ions, the required 50 ns bunch spacing calls for new LLRF gymnastics, namely slip stacking in the SPS, before transfer to the LHC.

The new LLRF will be based on the distribution of a fixed frequency reference clock, plus data (such as the instantaneous Frequency Tuning Word and Phase) using the deterministic White-Rabbit link. Studies are ongoing to minimize the phase noise resulting from the reconstruction of the reference clock from the serial-data stream, to be used as sampling clock (ADC).

A cavity-controller (RF feedback) prototype is being designed based on MicroTCA, for evaluation as VME alternative. CERN considers the MicroTCA as a candidate for the replacement of the current VME systems in operation.

Plans for the upgrade include the SPS beam-control (beam based loops) in MicroTCA.

The new philosophy will make new features possible, such as the coupled-feedback on cavities of different lengths. Use of synchronized deterministic links is also very attractive for future machines with RF stations at distant locations, such as for the CERN future collider FCC-ee under study.

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