

# Mechanism to speed-up development of FPGAs in MicroTCA

*Wednesday 6 December 2017 16:30 (15 minutes)*

This presentation shows some solutions and ideas, how to speed up the development with FPGAs and FMCs (FPGA Mezzanine Card) in MicroTCA systems.

Questions answered:

How to debug multiple FPGAs in a MicroTCA.4 crate remotely at the same time by using only an Ethernet Cable?

How to detect the FMC version and functions and how to load the right FPGA image at boot-up or hot-plug?

How to program an FMC with an empty EEPROM (no management and module information)?

## Summary

The easy to learn NAT-MCH user interfaces make the management and configuration of complex technologies e.g. PCIe-express-Clustering, complex system configuration, power management and collection of complete system information a task of seconds.

The same approach is followed for the FPGA and dynamically change FMC-IO.

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