

6th MicroTCA Workshop for Industry and Research

Wednesday, 6 December 2017

Session 4 - CFEL (16:00 - 17:30)

-Conveners: Anders Johannsen

time	[id] title	presenter
16:00	[35] Roadmap for Teledyne SP Devices high speed MTCA.4 digitizers	Mr WEBER, Laurent
16:15	[5] MTCA established in quality assurance as an image processing system	Mr FIX, Friedrich
16:30	[16] Mechanism to speed-up development of FPGAs in MicroTCA	Mr DIRKSEN, Vollrath
16:45	[7] Complete MRF Timing System on MicroTCA.4 Platform	Mr PIETARINEN, Jukka
17:00	[18] Cooling Strategies – MicroTCA crates installed in electronics cabinets	Mr GANNINGER, Christian
17:15	[45] MicroTCA Technology Lab at DESY: Status and Outlook	Dr WALTER, Thomas