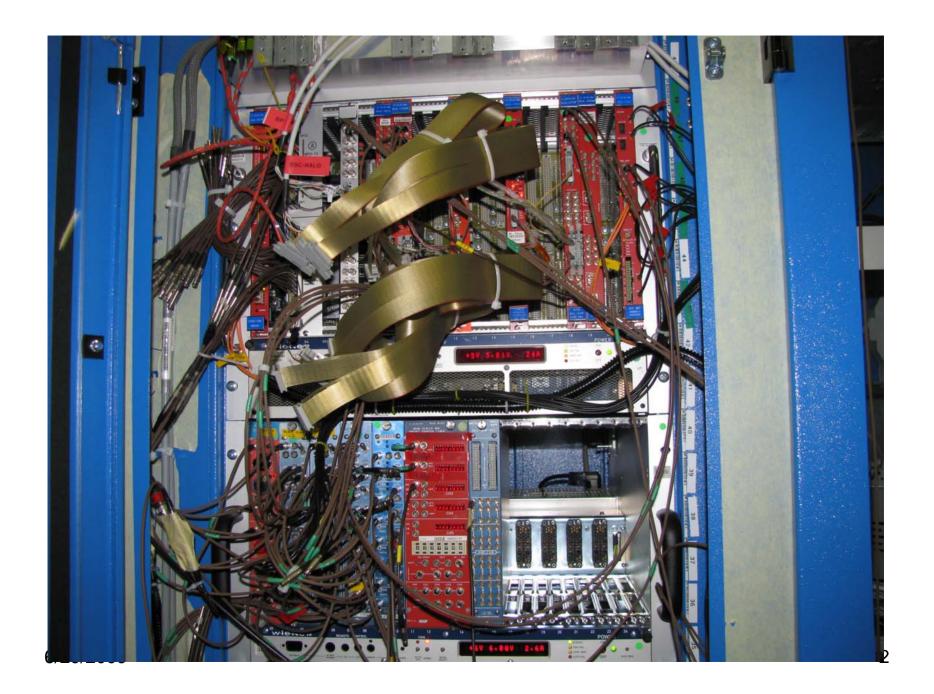
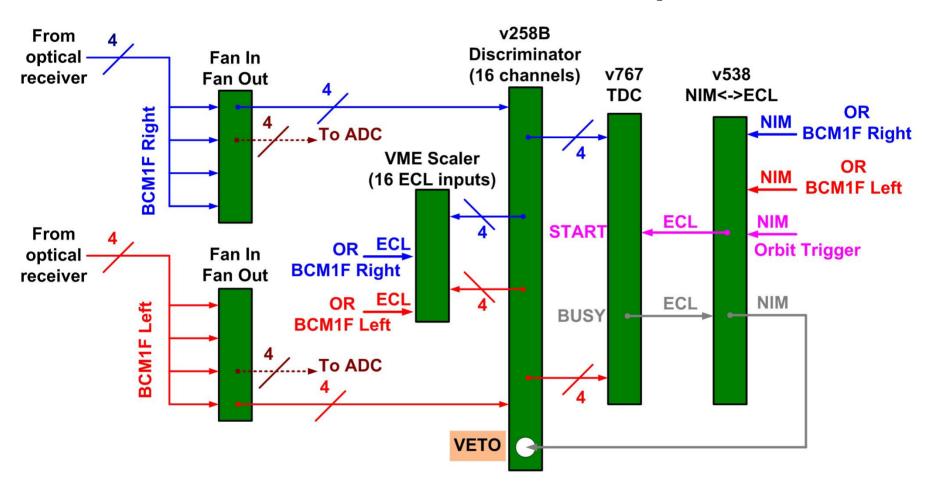
BCM1F installation (June 2009)

CMS Group-DESY Zeuthen 29 June 2009

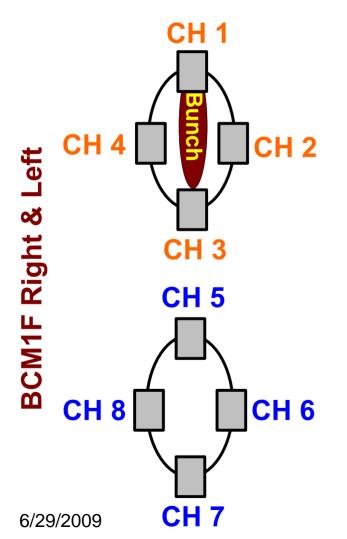


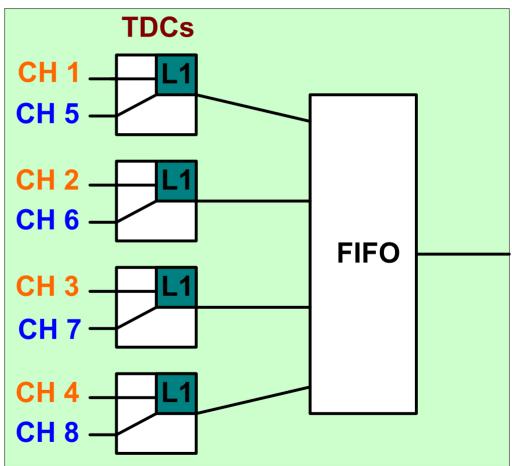
Hardware setup



6/29/2009

Hardware setup: connection of detectors to TDCs





Software Status

- Discriminator software ok
- Scalers counting Hits in the 8 diamonds and the OR of the + and - modules
- TDCs registering Hits with respect to the orbit Trigger in continuous storage mode and Continuous FIFO readout when the Almost Full Level is asserted. The BUSY (FIFO full) signal from TDC board is used to VETO the TDC inputs and avoid memory overflows. Trigger is not VETOed.

Still to do

Hardware level:

Clean up the mess of cables: shorten TDC cables

Software level:

- Run DAQ with Block Transfer Mode
- Find a fast way to store data
- Make it available for data publishing