

Some CMS Computing Items

Christoph Wissing

CMS Hamburg Meeting

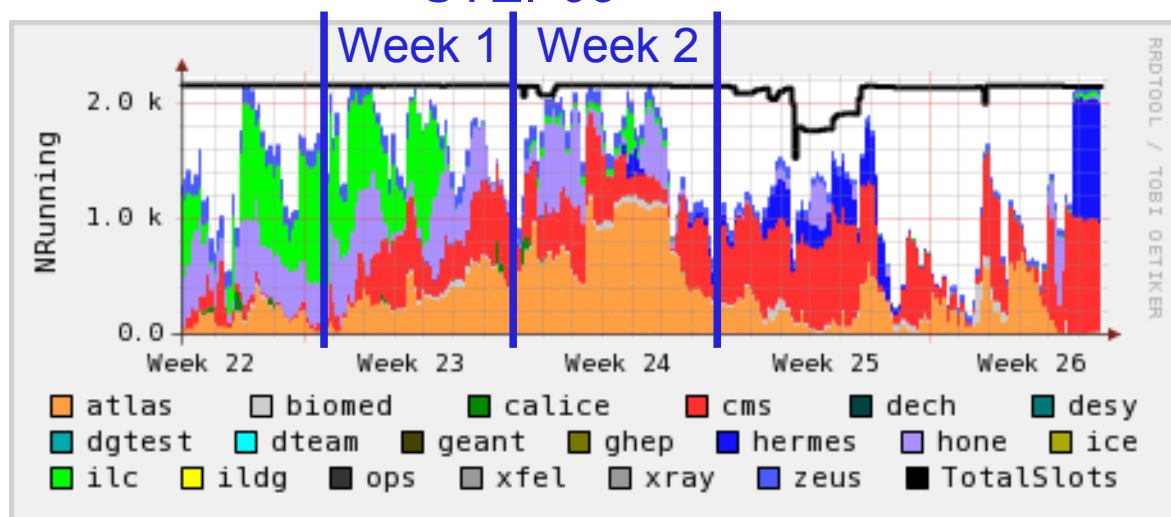
June 29th, 2009



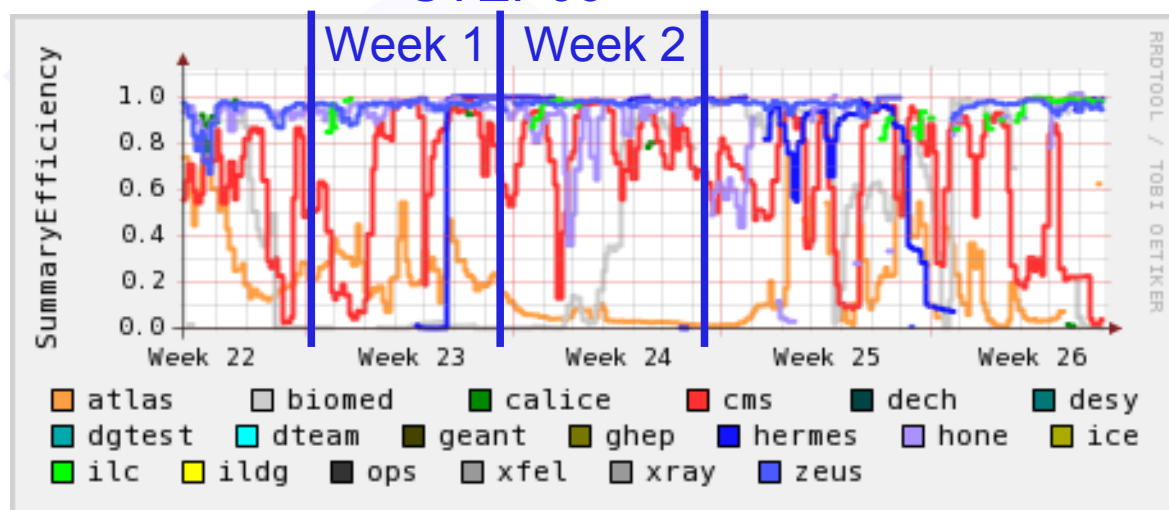
- ◆ **Scale Test of the Experimental Program**
 - Not an integrated challenge
 - Dedicated tests with overlap among VOs
- ◆ **Goals**
 - Tier0: Multi-VO writing to tape
 - Tier1: Focus on tape access
 - pre-staging for processing
 - transfer of datasets not being on disk
 - Tier2: Fill all sites with at least pledged CPU slots for analysis
- ◆ **All tests running in the first two weeks of June**
- ◆ **More detailed presentations during CMS week**
 - STEP session: <http://indico.cern.ch/conferenceDisplay.py?confId=62300#2009-06-23>
 - Computing plenary: <http://indico.cern.ch/conferenceDisplay.py?confId=58153#2009-06-26>
- ◆ **PostMortem workshop at CERN July 9-10th**



STEP09



STEP09



- ◆ Really a multi-VO event
 - Not only LHC VOs
- ◆ Rather o.k. for CMS
 - Pledged CPUs delivered
 - Good CPU efficiency
- ◆ Less successful for ATLAS
 - Difficulties with storage servers
 - Rather bad overall performance :-)
- ◆ Quite some gain in experience

- ◆ **Tier2 representatives met PAG representatives**
- ◆ **Questionnaire about Tier2 activities**
 - Interaction T2 – PAG
 - Resource distribution and usage
- ◆ **Most discussed**
 - **Prioritization of special users at Tier2s**
 - Special VOMS role (group?) proposed
 - **Tier2 to Tier2 data transfers**
 - See extra slide
 - **SL5 migration**
 - See extra slide
- ◆ **Further details:**
 - <http://indico.cern.ch/conferenceDisplay.py?confId=62300&view=standard&showDate=23-June-2009>
 - <https://twiki.cern.ch/twiki/bin/view/CMS/T2Roundtable090623>

- ◆ **Computing model (TDR)**
 - Only Tier1 – Tier2 links
 - MC upload from T2 to T1
 - Download of datasets to T2s only from T1s
- ◆ **T2 to T2 transfer useful addon**
 - Ship around group skims
 - Already established for some national T2s (e.g. DESY ↔ RWTH)
- ◆ **Try to connect T2s of each PAG (working group)**
 - Started with Top group already during CMS week:

From Node ↓ To Node →	T2_BE_IIHE	T2_DE_DESY	T2_ES_IFCA	T2_FR_IPHC	T2_US_Nebraska	T2_US_UCSD
T2_BE_IIHE						
T2_DE_DESY						
T2_ES_IFCA						
T2_FR_IPHC						
T2_US_Nebraska						
T2_US_UCSD						

- ◆ All LHC experiments want to migrate to SL5 64bit before beam
- ◆ CMS asks all Grid sites to migrate CPU resources by **September 2009**
- ◆ CMSSW issues
 - CMSSW is (and remains for some time) a 32 bit SL4 build
 - Recent 2_2_x releases and coming 3_1_x releases run on SL5
 - **There might be problems with 2_0_x and 1_6_x!!!**
 - Native SL5 builds expected by the end of 2009
- ◆ SL5 resources
 - WGS at DESY: desy-cms006.desy.de
 - NAF WGS: tcx035.naf.desy.de + some batch nodes (s. NAF docu)
 - CERN lxplus: lx64slc5.cern.ch + some lxbatch nodes
 - Two Grid CEs at CERN
 - One Grid CE at DESY by mid of July in addition to existing CEs
- ◆ **Start testing your applications..**