Contribution ID: **27**   Type: **not specified**

Modernization of MicroTCA.4 FPGA Firmware Framework

Thursday 5 December 2019 12:00 (15 minutes)

MSK group in DESY has been working on a common framework to develop FPGA firmware for MicroTCA standard for over 15 years. This highly modular, abstracted framework has been used to deploy many accelerator facilities inside and outside of DESY. With increasing complexity, maintenance and continuous integration becomes critical in order to ensure minimal downtime for FPGA firmware related failures. This talk gives overview on the framework as well as future upgrades on interfaces, revision control and continuous integration.

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Session Classification: Session 6: FPGA