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Overview of DMCS Projects and MicroTCA.4 Developments

Thursday 5 December 2019 09:15 (15 minutes)

The Lodz University of Technology, Department of Microelectronics and Computer Science is involved in the development of MicroTCA.4 and MicroTCA.4.1 standards from 2007 onwards. Since that time, we developed various MicroTCA.4 components including Intelligent Platform Management, Advanced Mezzanine Cards (AMCs), Rear Transition Modules (RTMs) for data acquisition and processing systems used in numerous accelerators and fusion projects.

The presentation will discuss a selected projects ongoing at our department based on MTCA.4 technology.

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