

MTCA.4 based BPMs at DESY

BUTTON AND STRIP-LINE BPM ELECTRONICS

Hans-Thomas Duhme

Thanks for Material, Discussions and Inspiration

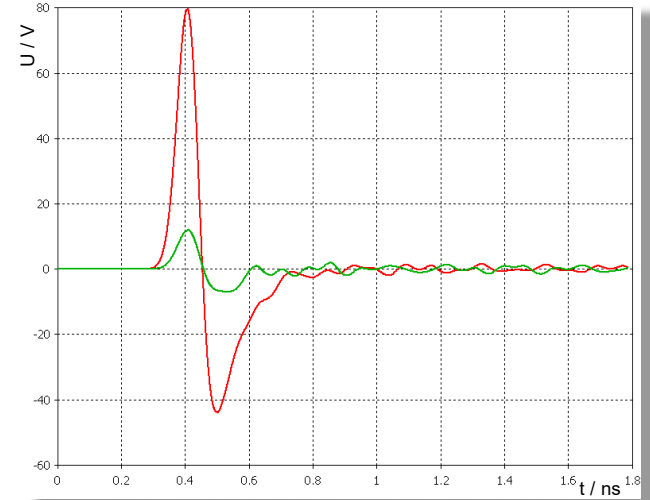
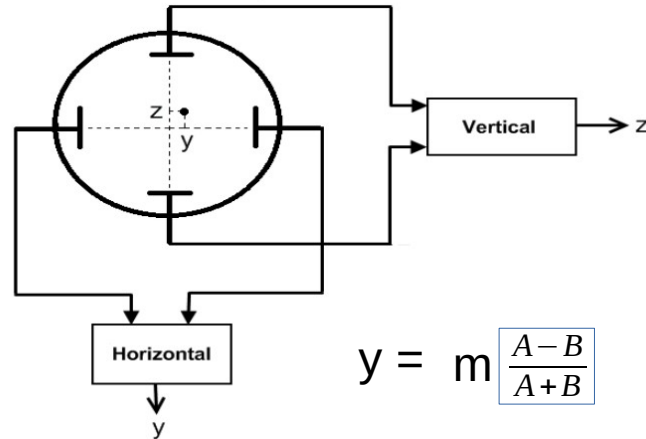
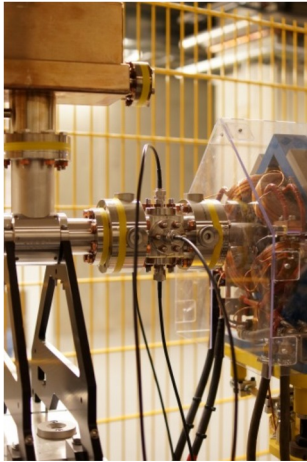
Bastian Lorbeer, Dirk Lipka, Nicoleta Baboi, Frank Schmidt-Foehre,
Gero Kube, Mathias Werner, DESY

M. Bruno, O. Tavares, LNLS

MTCA.4 based BPMs at DESY

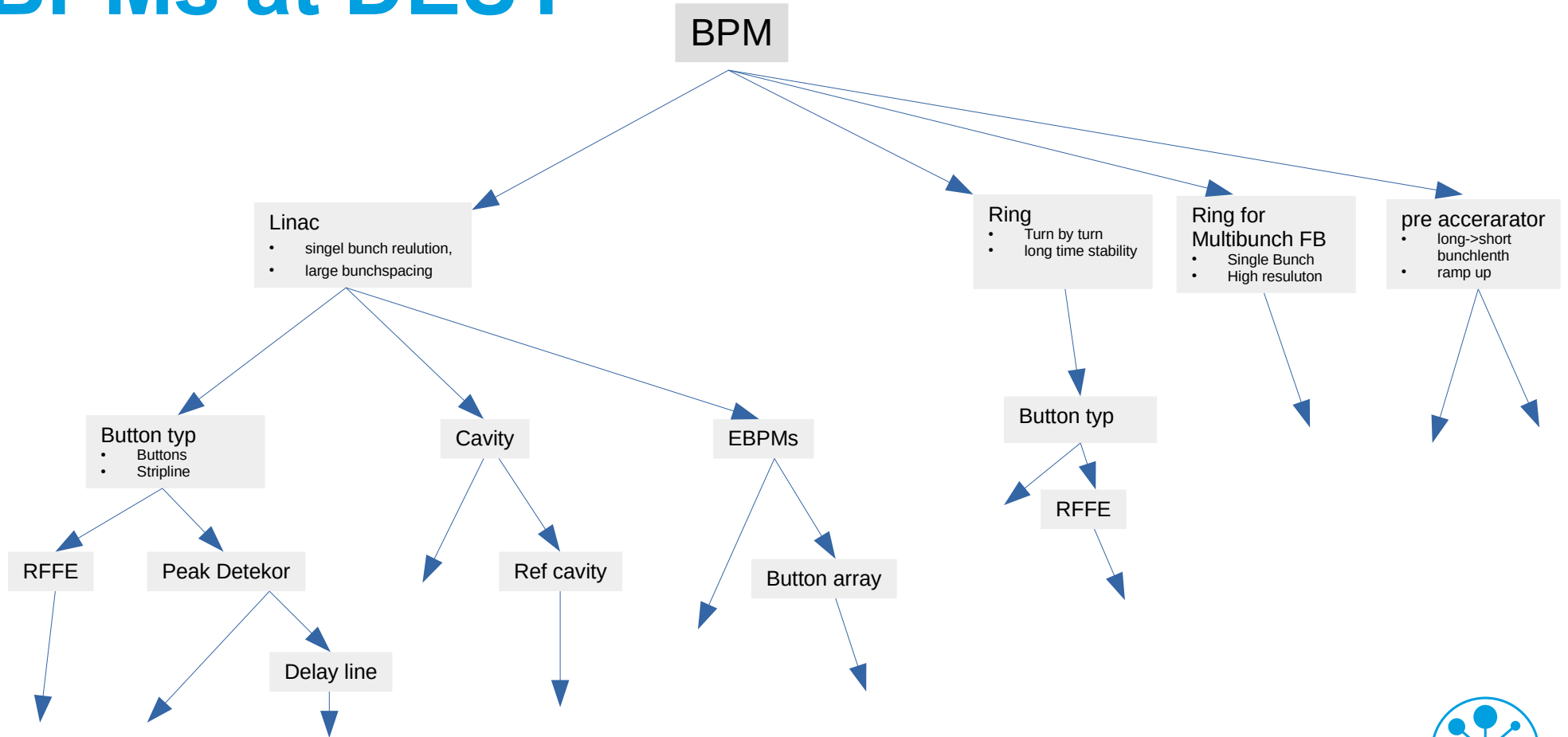
What is a BPM ? = Beam Position Monitor

18FL2EXTR



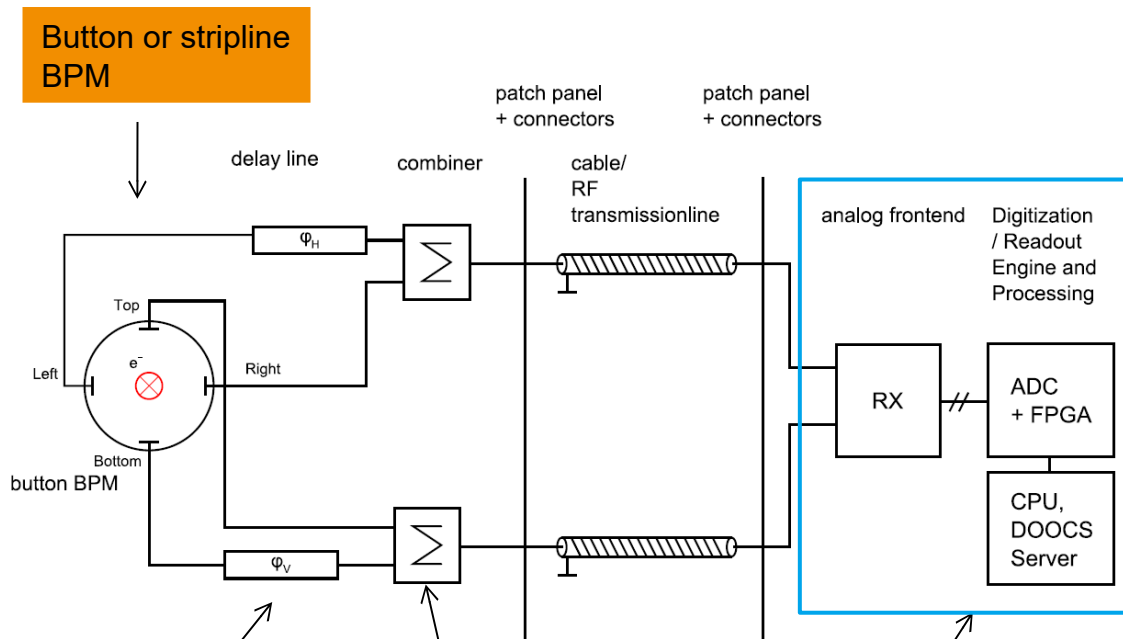
(Quelle :Dirk Lipka MDI, internal talk 2019)

BPMs at DESY



Peakdetektor with delayline

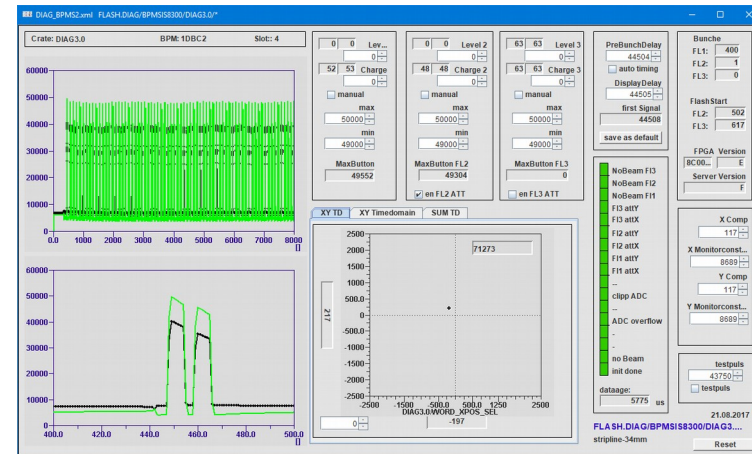
for linacs with large bunchspacing



Delay = 100ns
RF cable 1/2" ~26m

Combiner type:
broadband

RTM low charge
Peak detector
electronics



Peakdetektor



4 Channel Input RTM



Struck SIS8300L2



Crate with electronic 6 Struck ADCs for 12 BPMs
possible 12 AMC for 24 BPMs



Delayline = 100ns
RF cable 1/2" ~26m

EBPMS for Energie measurement

Beam position measurement with 500mm range

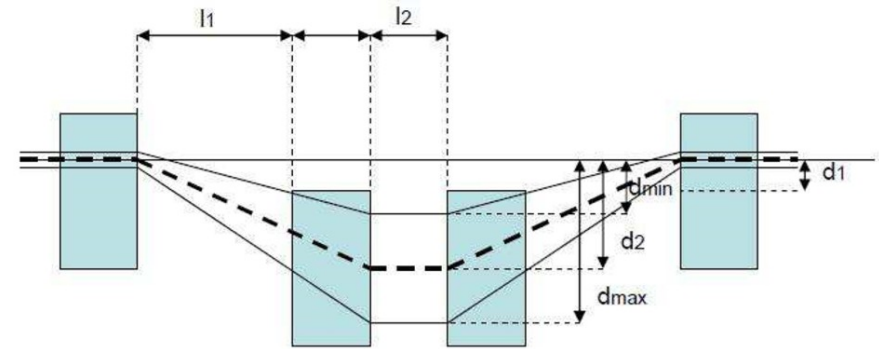


Figure 2: Sketch of XFEL BC chicane layout.

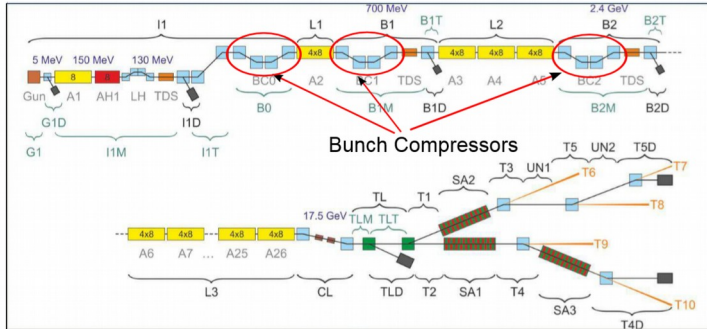
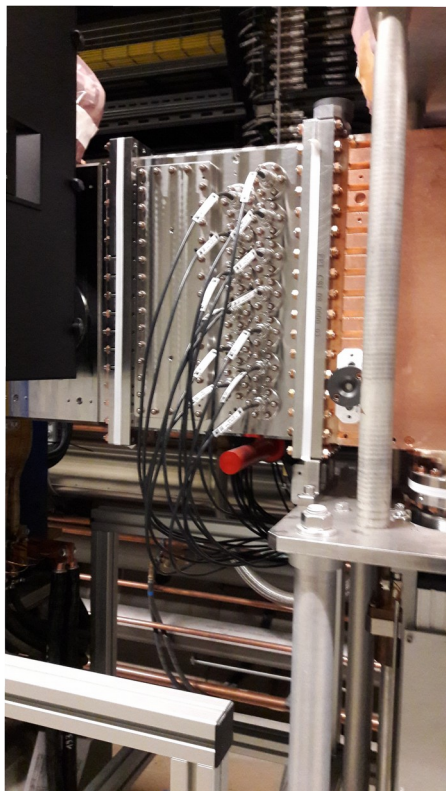


Figure 1: European XFEL Section Overview with bunch compressors.

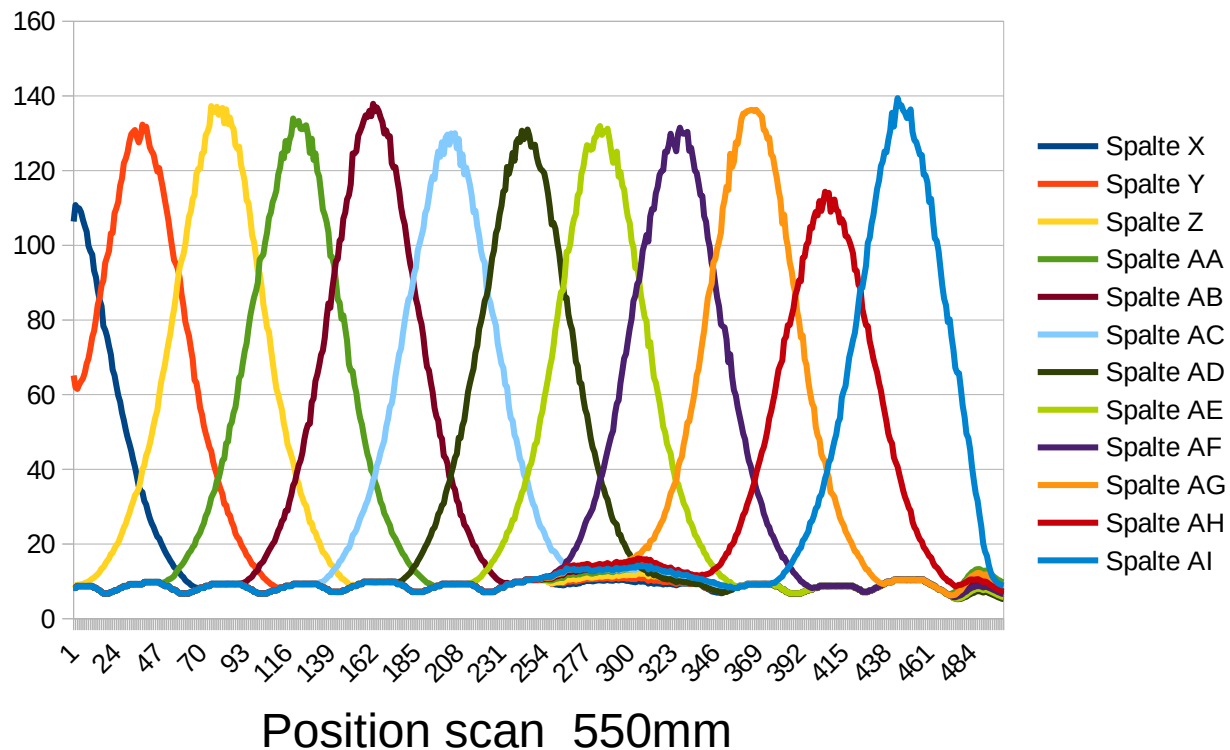
- Mtca crate installed in tunnel near to Bunchcompressor
- Radiation Problems at BC2
- SSD and FPGA lachtup problems
- need SSD replace (typ. 2 time/year)
- reboot FPGA (typ. 2 time/year)

Button Overlap

button array



13 x Button array



Ideas for new Petra BPM

Motivation:


- Looking for replacement for outdated libera BPM
- new requirement for Petra 4
- mtca.4 at PETRA

Libera Spark

Compact beam position monitor electronics for electron & hadron linear machines

Libera Brilliance+

High performance beam position monitor electronics for electron circular machines



LNLS Open Hardware Designs

Open hardware designs and open source gateway and software codes are available at:

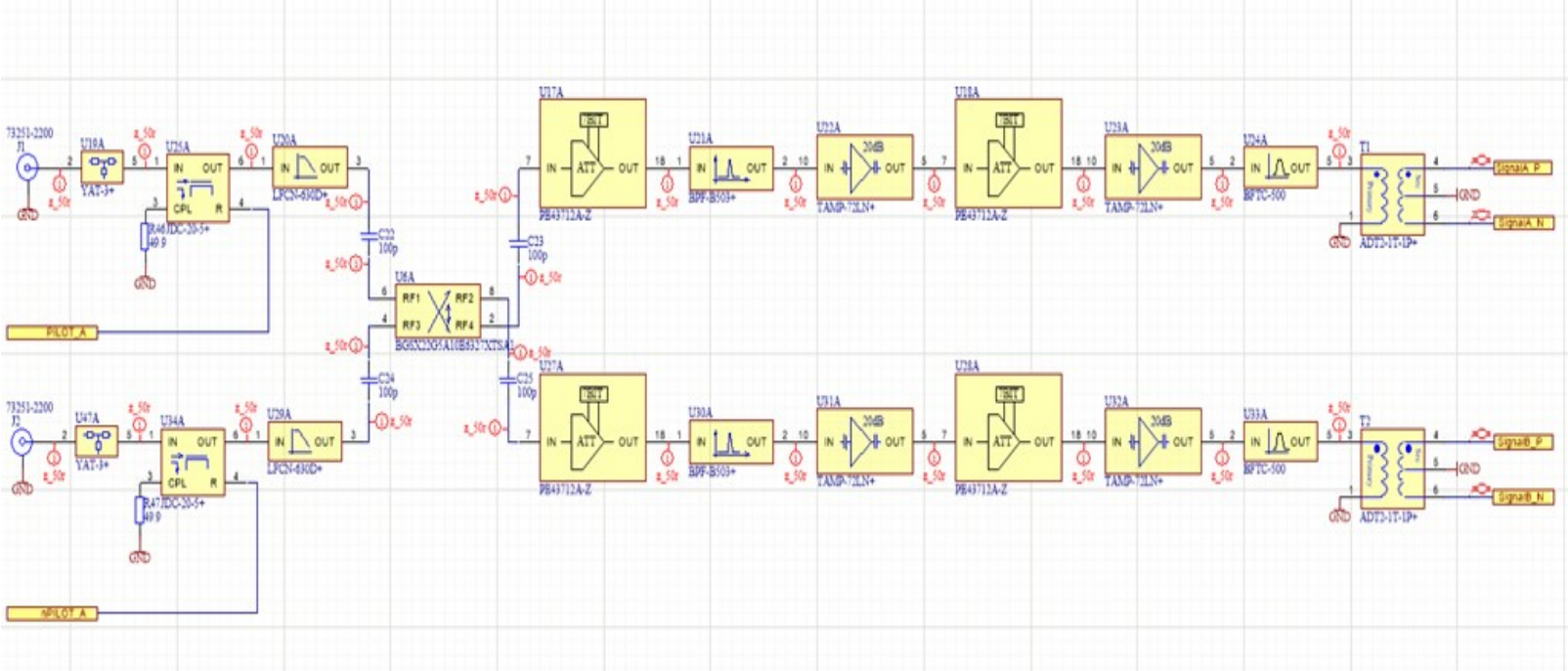
<http://github.com/lnls-dig>
<https://www.ohwr.org/projects/afc>

Sirius mTCA based BPMs

(Quelle: RF BPM ELECTRONICS M. Bruno, O. Tavares, LNLS, Campinas, SP, Brazi)

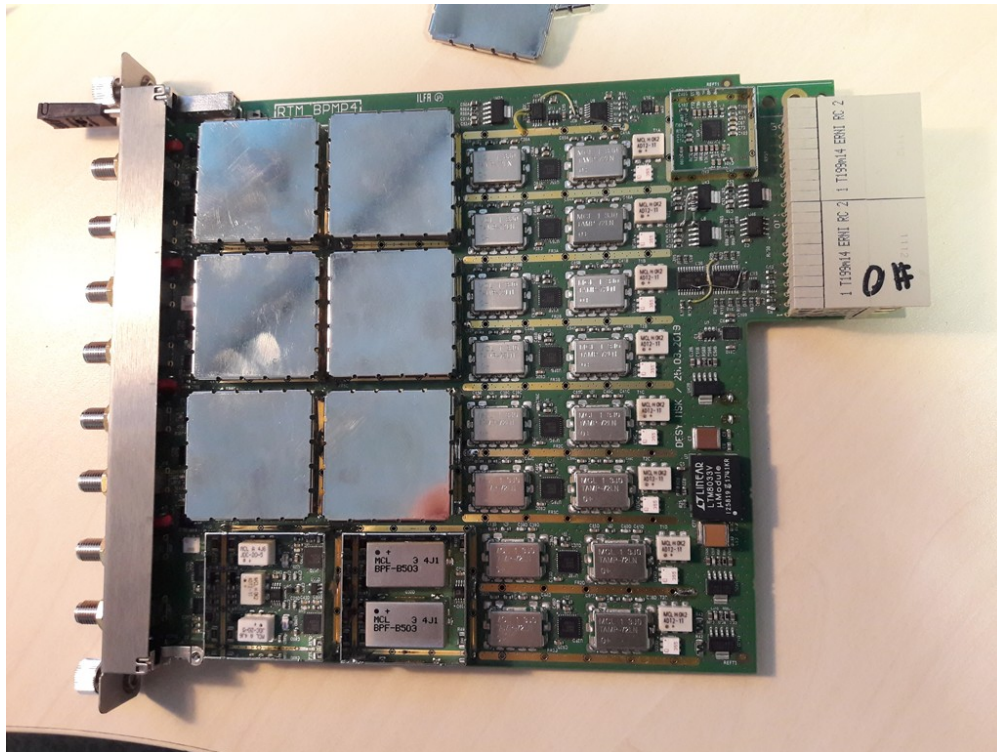
Detector with Pilot and Switching

RTM Testsetup



RTM BPM Development

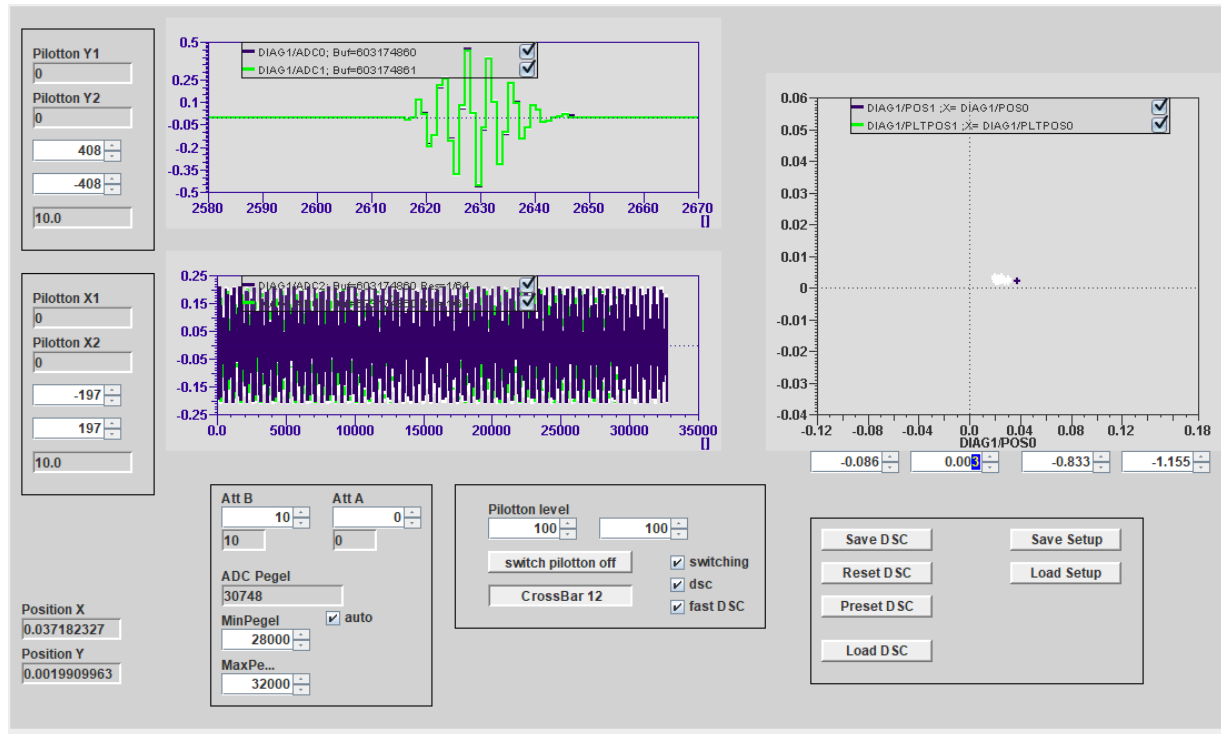
Prototype



- 8 Channel for 2 BPMs
- SIS8325 250MSPS ADC
- Pilot ton
- 2 Channel Switching
- PLL
- temperature stabilized
- Mtca.4 RTM
- Based on SIRIUS open Hardware design

RTM BPM Development

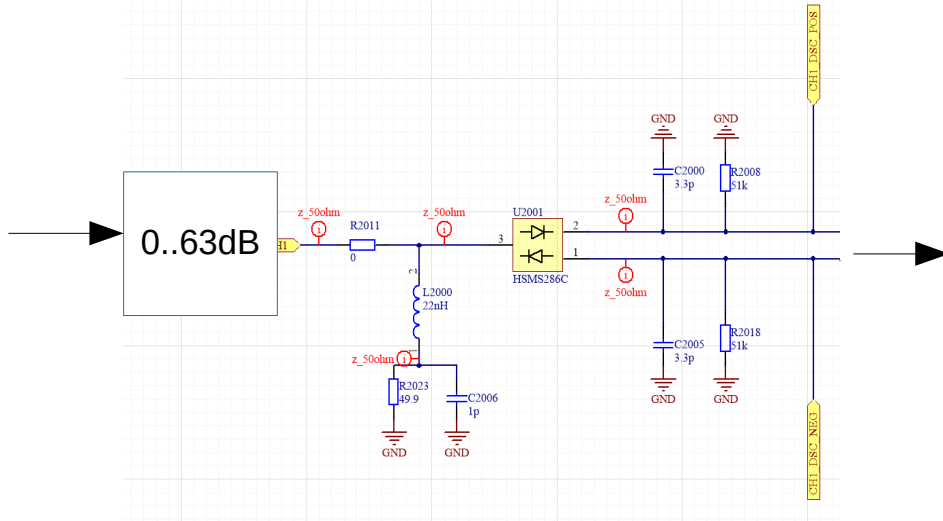
Promising first results



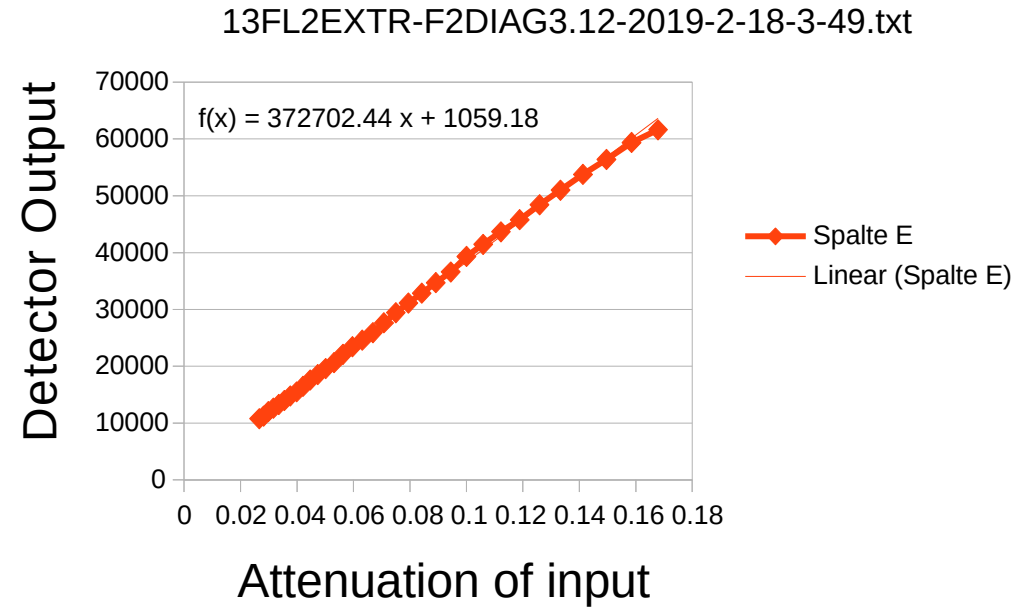
Thank you for your attention!

Peakdetektor Calibrating

appendix



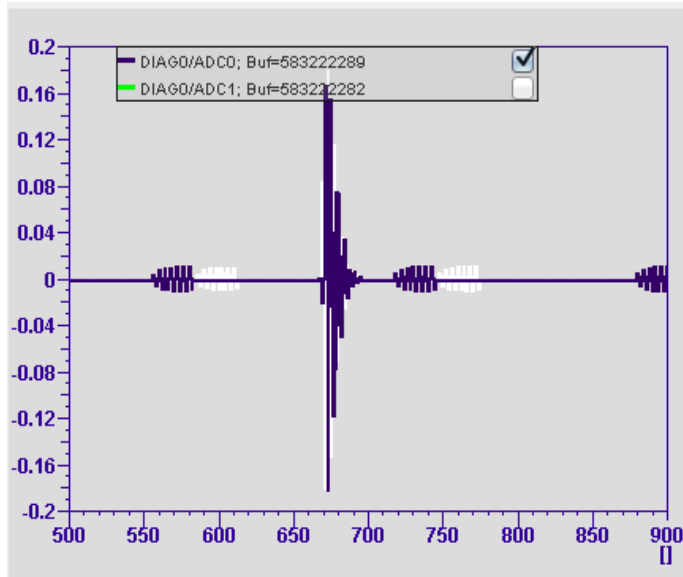
(Quelle: B. Lorbeer, 2017)



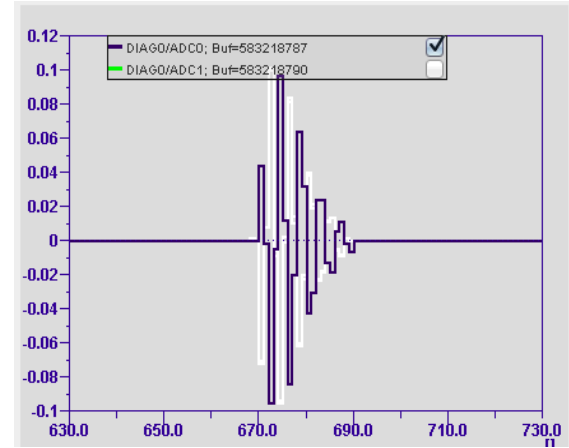
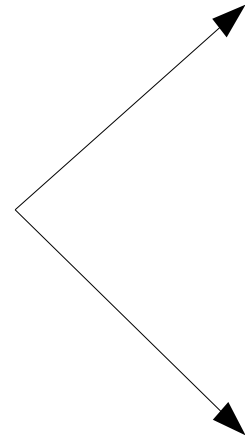
Lock-in-filter for pilot-tone separation

appendix

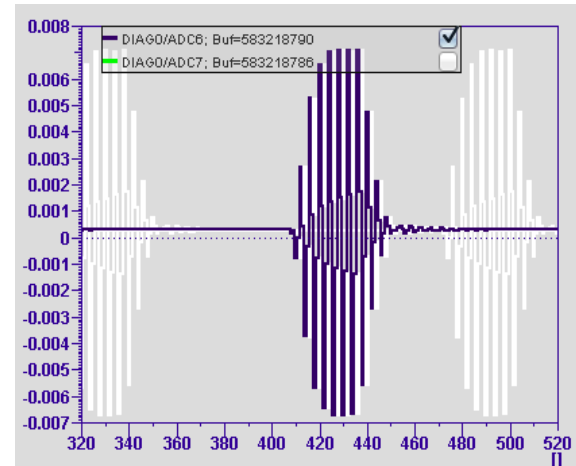
preliminary, under
labor conditions



Signal and Pilot-ton



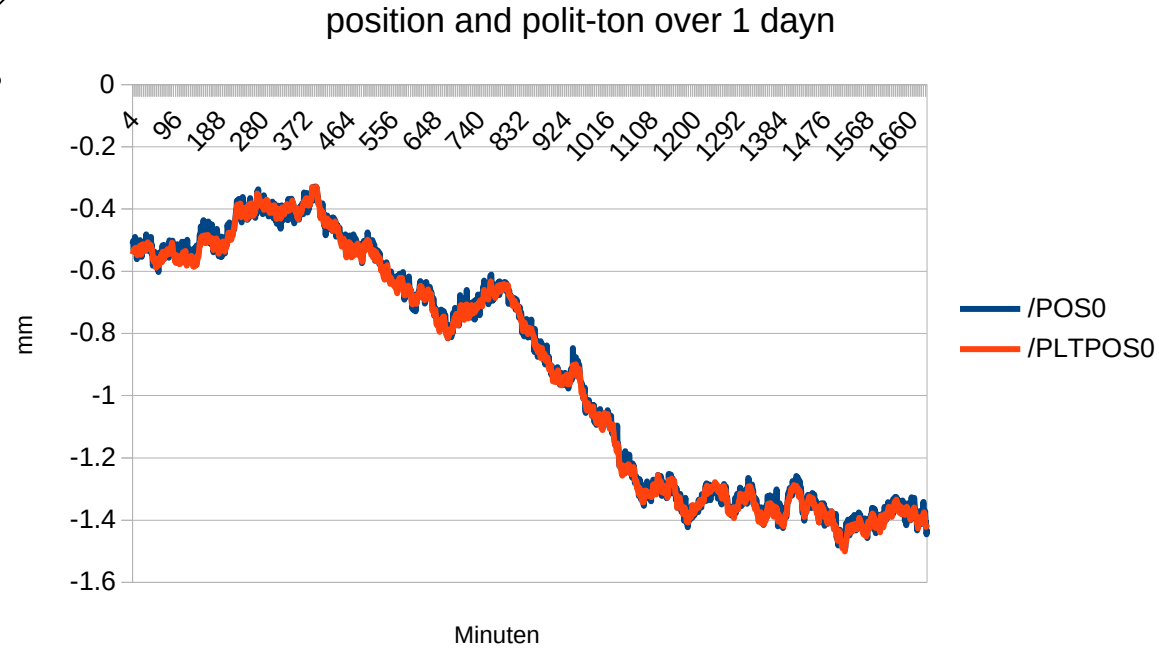
signal



Drifts : signal and pilot-tone

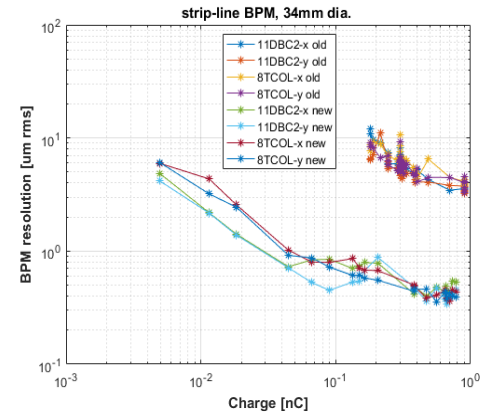
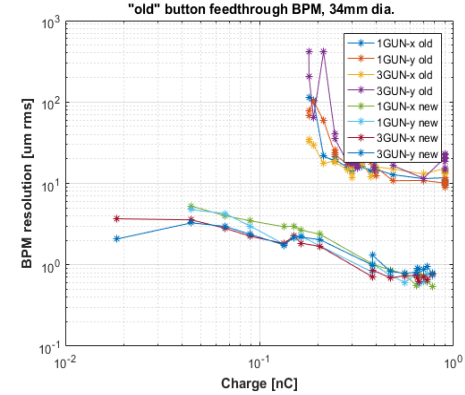
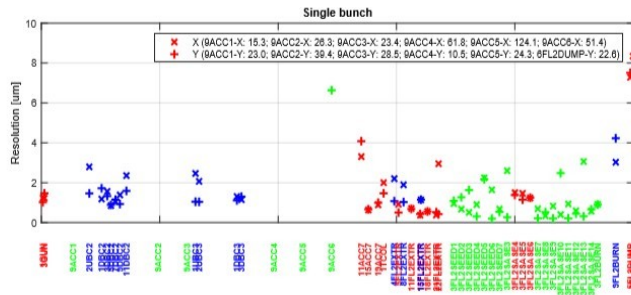
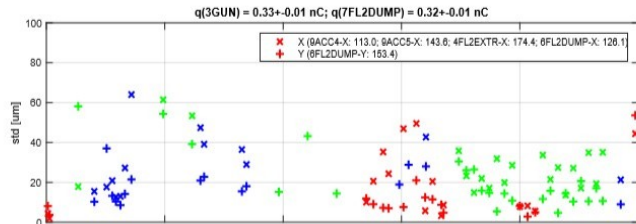
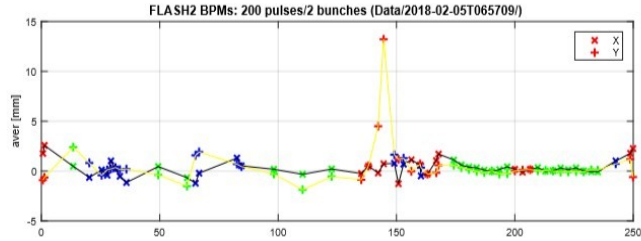
appendix

preliminary, under
labor conditions



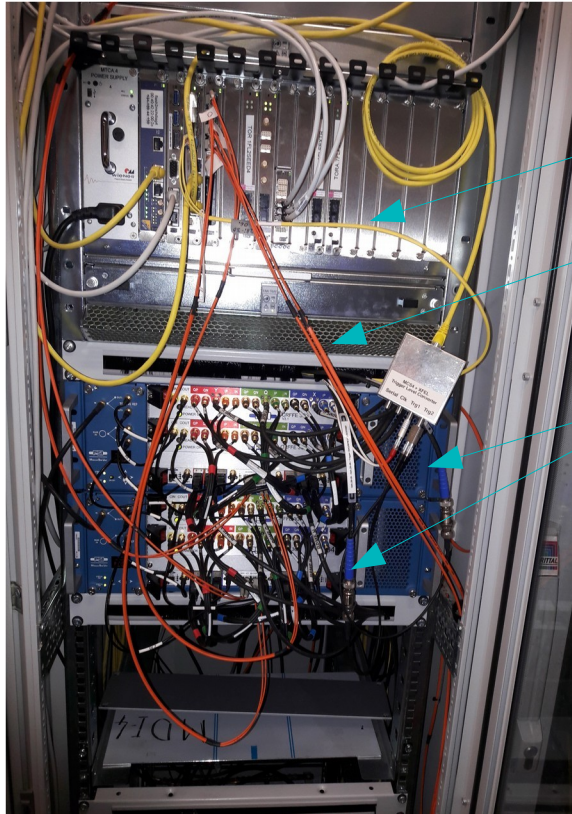
Resolution of BPMs at Flash

appendix



PSI type BPMs at XFEL and FLASH

appendix



mtca.4 crate

optical link

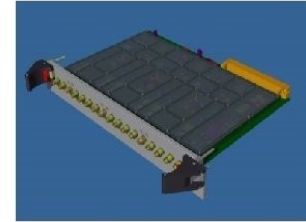
external BPM

elektronik

E-XFEL BPMs

31

Undulator Cavity BPM RFFE



- 2nd version (2011): Active temperature stabilization, solid shielding, ADC clock synthesis, more gain ranges, ...
- Beam tests: 1-2 μ m RMS noise @ FLASH without adjusting gain, delays, LO, ... (1 shift, lack of time ...) \rightarrow expect < 1 μ m if adjusted.
- Lab & beam test with properly adjusted/calibrated RFFE ongoing.