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Firmware and software synchronization for MicroTCA roll-out in the LLRF at CERN

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During the long shutdown from 2019 to 2020, CERN's Super Proton Synchrotron (SPS) is undergoing a comprehensive upgrade of its Low Level RF system, using MicroTCA as a hardware platform.

The new system, currently under development, attempts to maximise firmware reuse by implementing its functionality as independent IP cores, each of which must be supported by corresponding software components which interface it to the accelerator control system. Any changes in the HDL need to be followed consistently by changes in the software layers. This talk will present an overview of the software stack and tools which have been developed to synchronize the firmware and software during the development process.

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