Contribution ID: 19 Type: not specified

Certification and improvements of MicroTCA Technology Lab's GigE Vision Stack

Thursday 5 December 2019 11:45 (15 minutes)

The DIPC-7050 GigE Vision Stack is a system solution for running GigE Vision cameras in a FPGA/SoC-based environment. It is usable in wide array of industrial and scientific applications. By integrating the component in their system users are able to create and run their own high performance image processing solutions without taking care of any camera interfacing; especially without taking care of the GigE Vision standard.

This talk outlines the latest developments and improvements of the DIPC-7050 GigE Vision Stack.

We present the results of the products standard certification at AIA plug-fest, where our implementation had to interface with cameras from several vendors. Our implementation is now officially certified as a GigE Vision compliant product.

For covering the increasing demand of higher data throughput, the GigE Vision standard offers a 10Gigabit Ethernet variant. In the last couple of month we upgraded our solution for operating with devices via 10 Gigabit Ethernet. We present the improved version and discuss the performance.

Primary author: STUBBE, Sven (DESY)

Co-authors: Mr GORNOTT, Aaron (DESY); Mr MARJANOVIC, Jan (DESY)

Presenter: STUBBE, Sven (DESY)

Session Classification: Session 6: FPGA