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Update on GigE Vision implementation in FPGA

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Presented here are the latest updates from the development of the GigE Vision in MicroTCA.

First we present the results of the certification process at AIA plug-fest, where our implementation had to interface with cameras from several vendors.

A new hardware platform (Struck SIS8160 - an Advanced Mezzanine Card for MicroTCA platform) was also added to the list of supported hardware. With two 64-bit wide DDR4 memories this board offers a good platform for data intensive application, and with the support for White Rabbit is also allows timestamping of the captured frames.

Lastly we present the steps needed to extend the support for 10 GigE Vision, based on 10 Gigabit Ethernet.

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