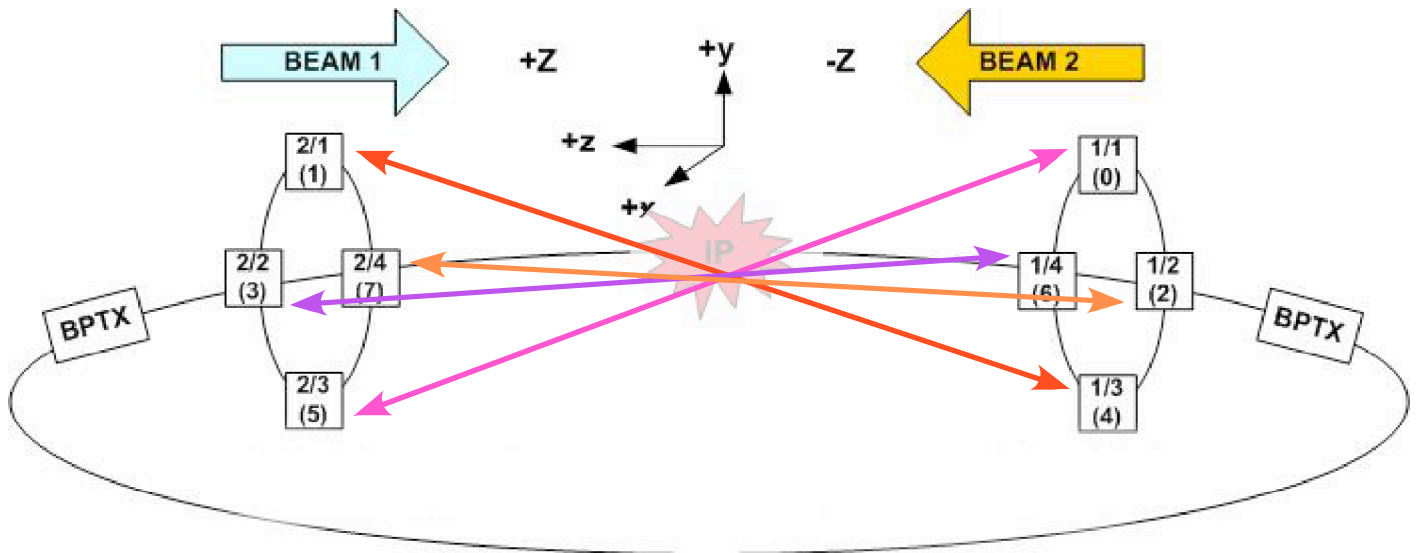


Implementation of a BCM1F time slot coincidence check

using a programmable look up table (LUT) inside a CAEN versatile I/O register

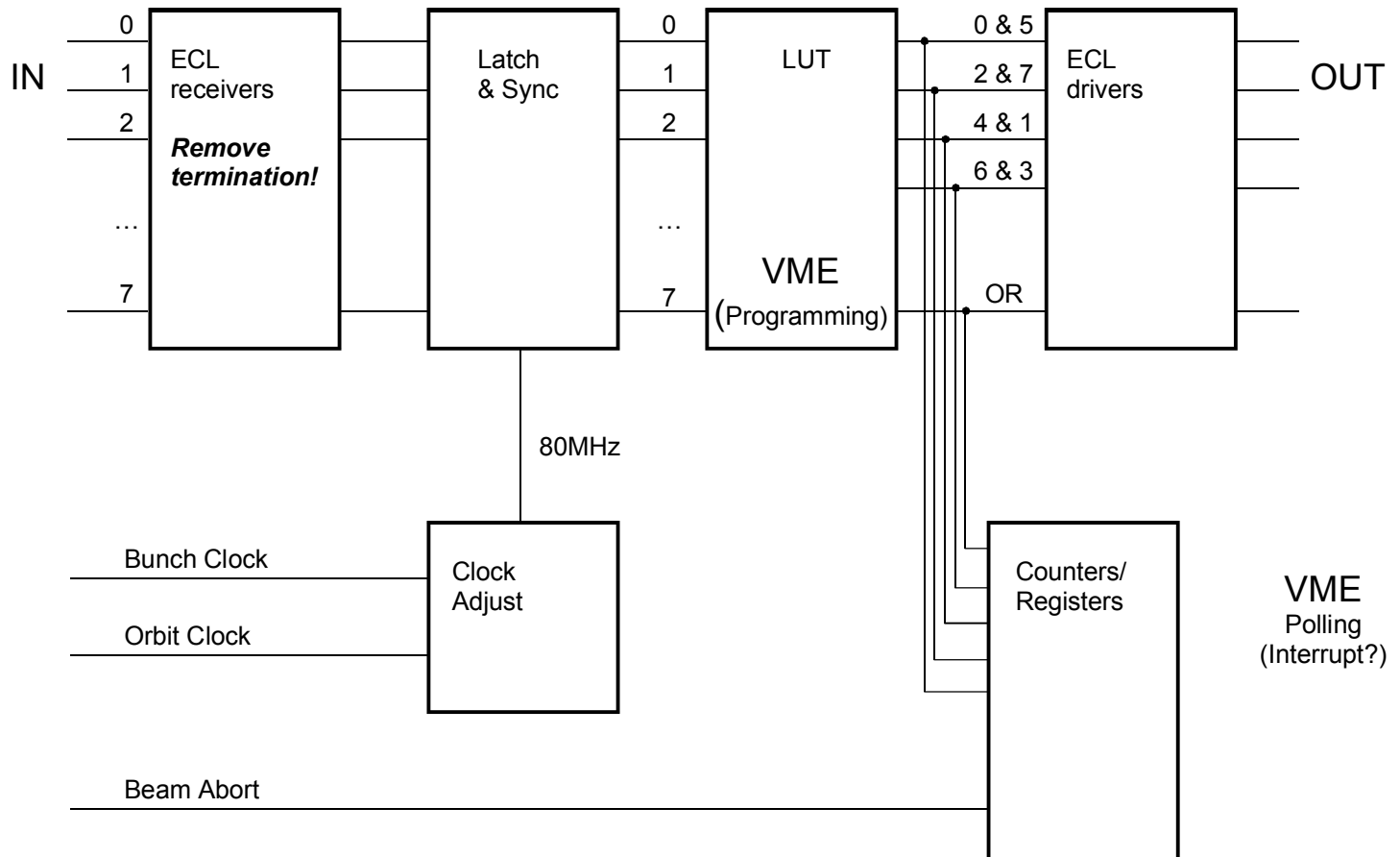


- build 4 coincidences between 3D-opposite detectors
- time of flight from IP is ~6ns, pulse width is ??ns
- outrule fake coincidences with beam halo!
- confine coincidence window to ~12ns

Hans Henschel
10-Jan-2010

Implementation of a BCM1F time slot coincidence check

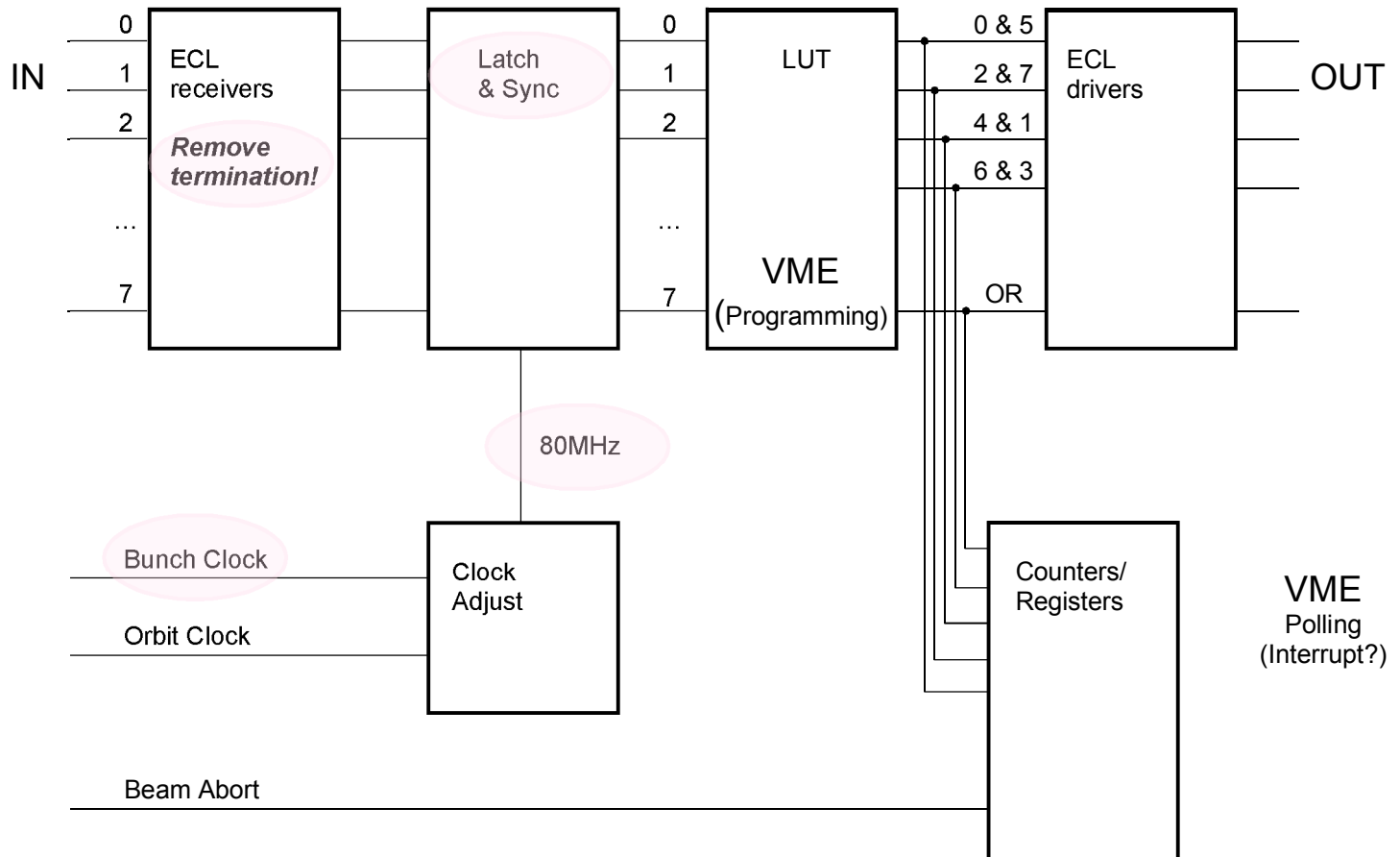
using a programmable look up table (LUT) inside a CAEN versatile I/O register



Hans Henschel
10-Jan-2010

Implementation of a BCM1F time slot coincidence check

using a programmable look up table (LUT) inside a CAEN versatile I/O register



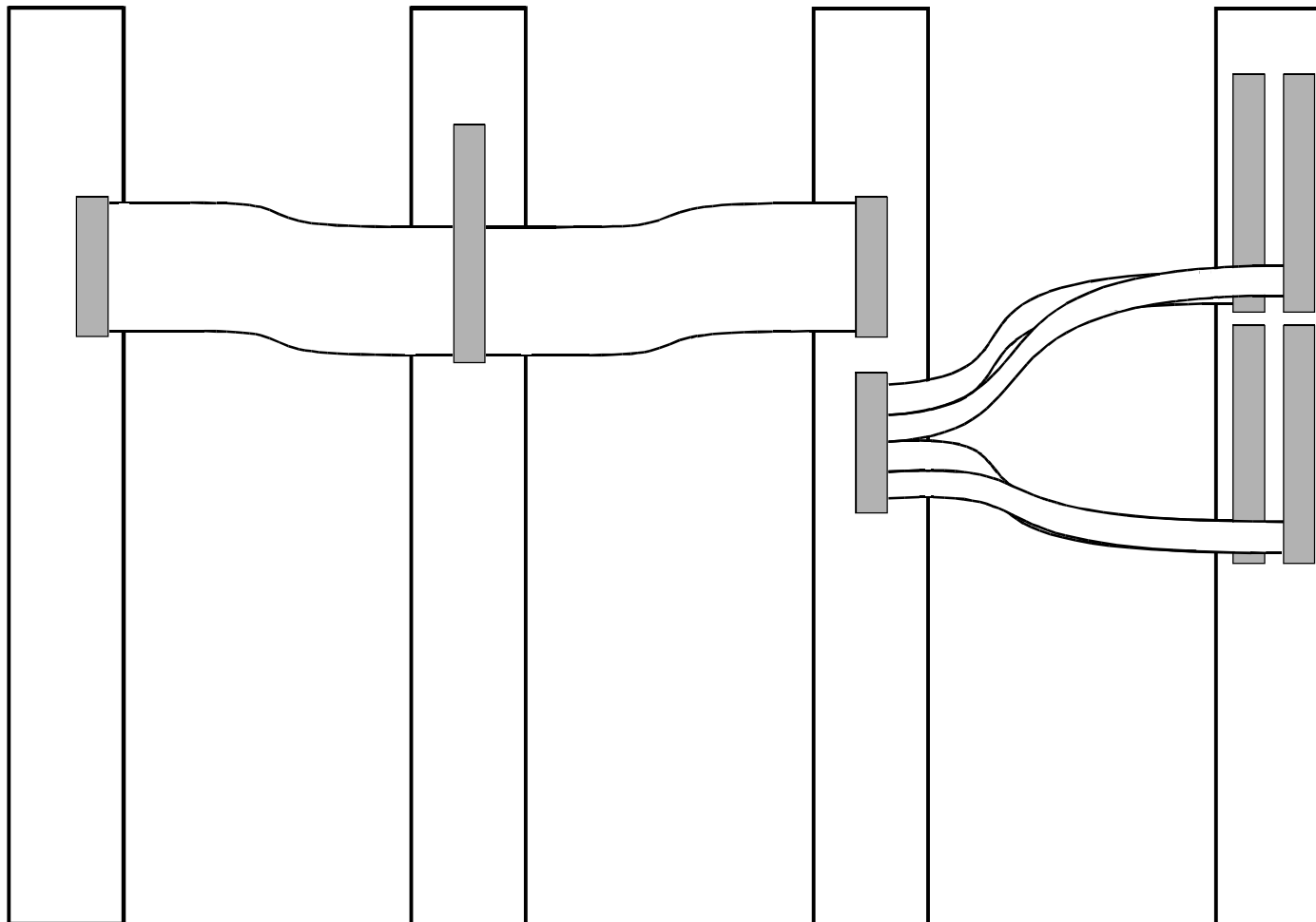
Hans Henschel
10-Jan-2010

Discriminator
1x 16pin DIN

**V1495 versatile Register
(LUT)**
1x 64pin Robinson Nugent

ECL VETO
2x 16pin DIN

TDC
4x 64pin Robinson Nugent



TDC Wiring with LUT
Henschel/Lange
08jan10