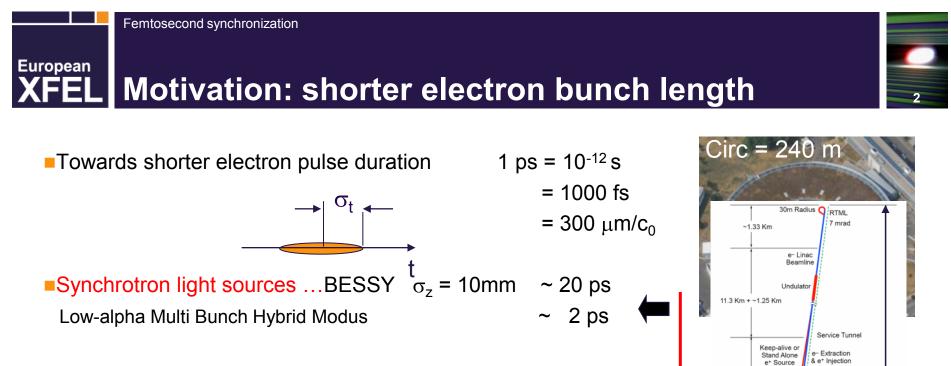


Holger Schlarb for the optical synchronization team

- Motivation
- Synchronizationsystem
- Komponenten des optischen Synchronisationssystem bei FLASH

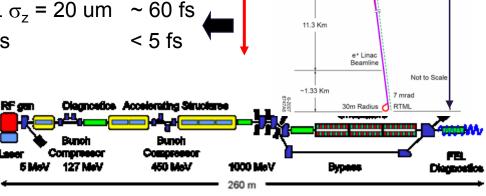




Linac driven colliders ...

lersSLC $\sigma_z = 2 \text{ mm}$ ~ 6 psILC=0.3 mm~ 1 ps

Free Electron Lasers ... European XFEL σ_z = 20 um ~ 60 fs Short pulse operation modes: FEL pulses < 5 fs



10-3



31km

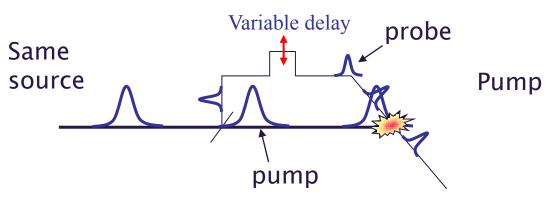
e⁺ Extraction

& e- Injection Service Tunnel

e- Source

XFEL Motivation: Pump-probe experiments

Classical setup:



Probe = flash



Shot pulses fs q ps

Pump pulse initiate reaction, probe pulse records current state.
 Atomic / Molecular Physics/ Solid state dynamics

FELs: two different pulse sources: FEL beam and optical laser

Knowledge of time delay is crucial!

Comments:1) measured at experiment => post analysis2) for small event rates=> not possible



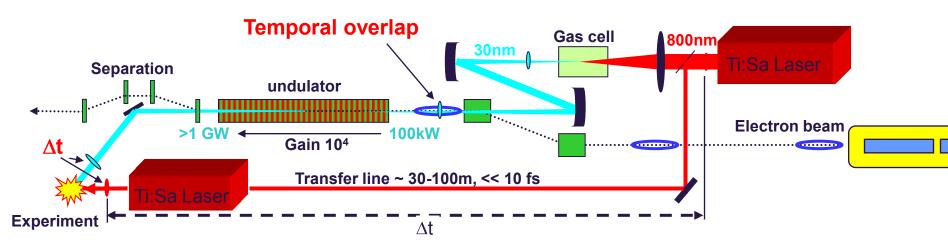
European





_ Motivation: seeding with higher harmonics

Generic layout of seeding a single pass FEL



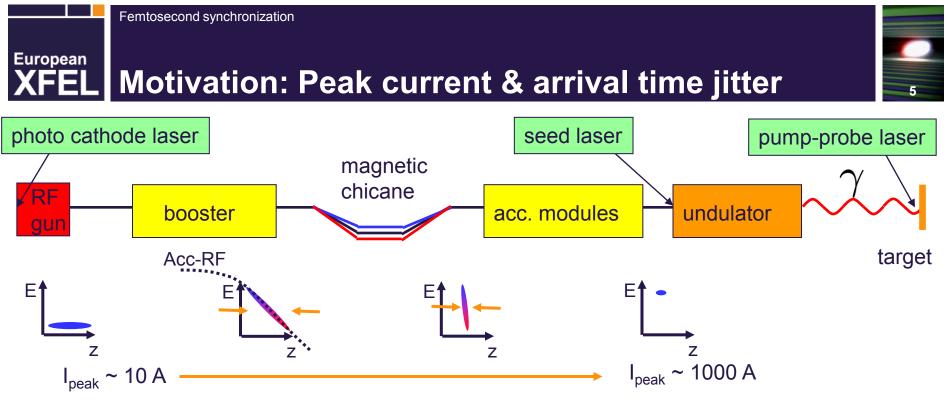
New class of experiments:

- Electron beam is seeded or manipulated using external laser
- Timing of FEL pulse defined by now by seed (laser)

Requirements:

- Temporal overlap between electron bunch & seed pulse essential ~ 10-50 fs
- Particular high demands on synchronization to pump-probe laser \sim 1-5 fs





Compression process & FEL resonance condition

 stability of peak current 	ΔΙ/Ι	<	1-10 %
 control of arrival 	Δt	<	10-50 fs
 FEL wavelength fluctuations 	∆E/E	<	10-4

⇒ Increased acceleration RF stability requirements

Phase stability (rel.)	∼ 0.05°0.005°	(~ µH … ~ 1 MHz)
Amplitude stability	~ 0.05%0.005%	(~µH ~ 1 MHz)

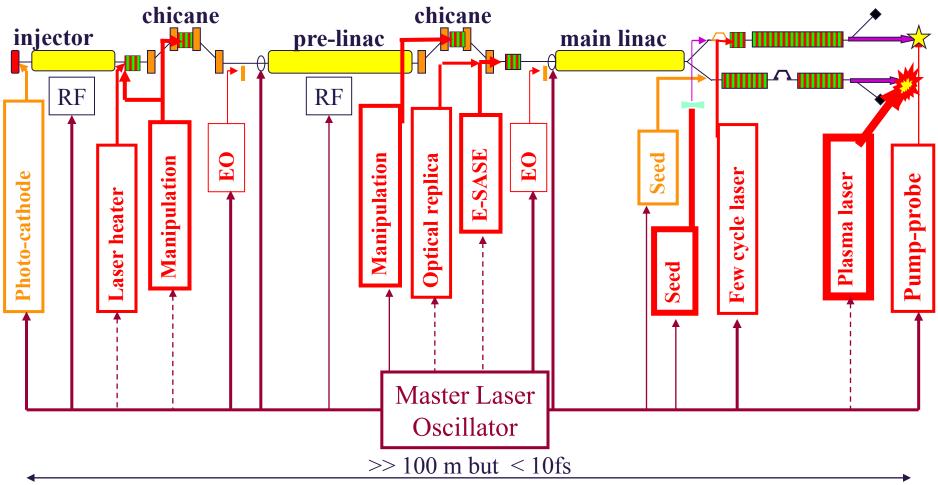




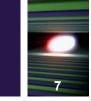




Generic layout of single pass FELs



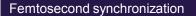




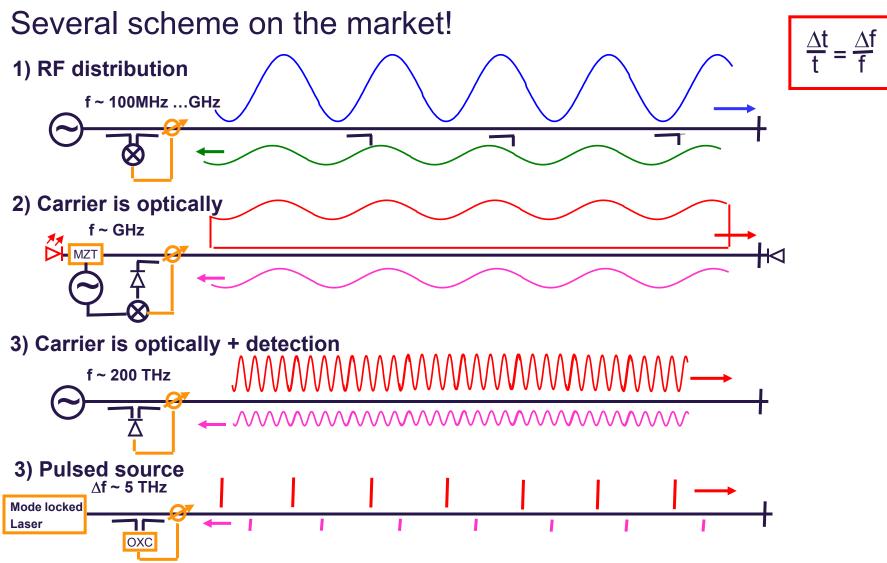
- Accelerator technology advanced from picosecond bunch duration → few 10 fs bunches
- Facility length range from ~100 m up to 3.5 km (European XFEL)
- Stability of FELs puts stringent demands on RF requirements
- Optical laser systems become an integral part of FEL facilities
 - for the beam generation,
 - beam diagnostics,
 - beam conditioning/seeding and
 - for user experiments

 \Rightarrow Changed the requirements on synchronization from ps to fs precision





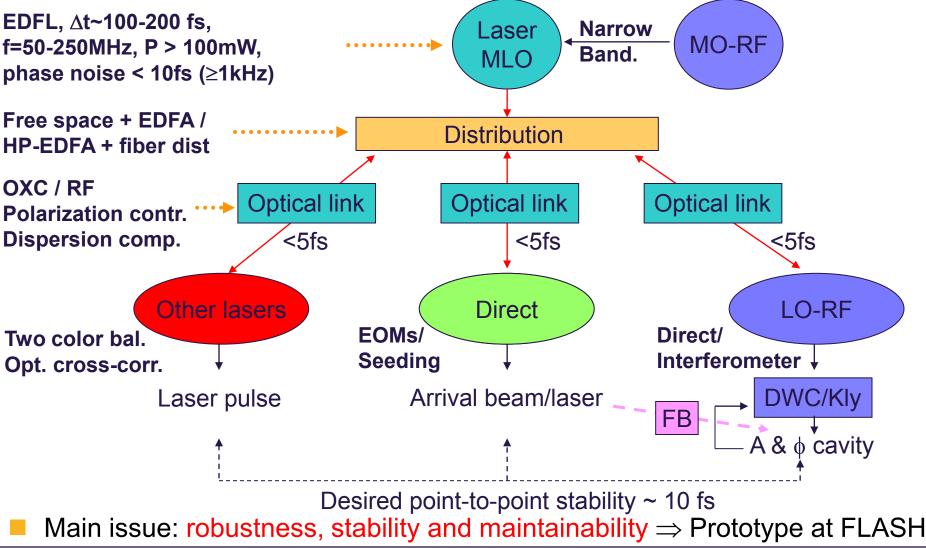
XFEL Synchronization







XFEL Optical synchronization system

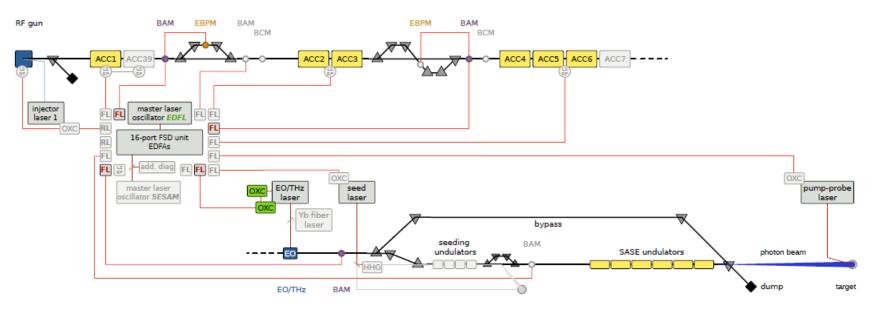








FLASH accelerator and optical end-stations for synchronization:



Master laser oscillator (EDFA) with 4 stabilized optical links to

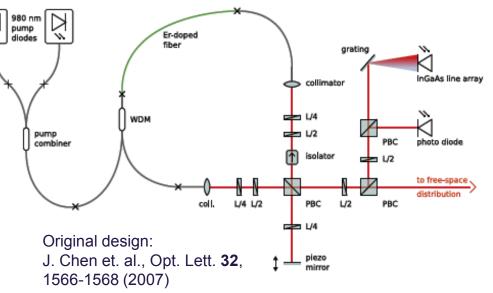
3 Beam arrival monitors (BAM)
1 cross-correlator for locking diagnostic TiSa laser system (OXC)
and large aperture BPM for energy measurements (EBPM)



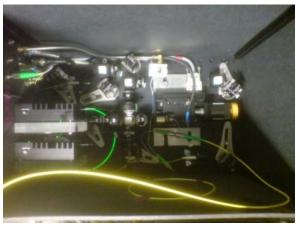
Courtesy: S. Schulz

XFEL Master laser oscillator (MLO)

- Specification/Requirements
- Mode-locked erbium-doped fiber laser
- Repetition rate of 216.66MHz
- Average power > 100mW
- Pulse duration < 100 fs (FWHM)
- Integrated timing jitter < 10 fs [1kHz,40MHz]
- Amplitude noise $< 2 \cdot 10^{-4}$ [10Hz,40MHz]
- Mechanical robust/easy to maintain







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2nd generation MLO

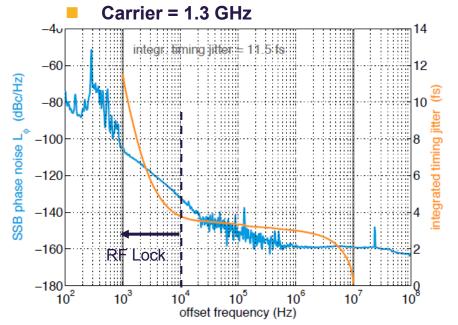


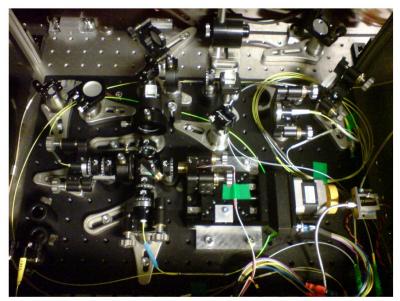












Courtesy: S. Schulz

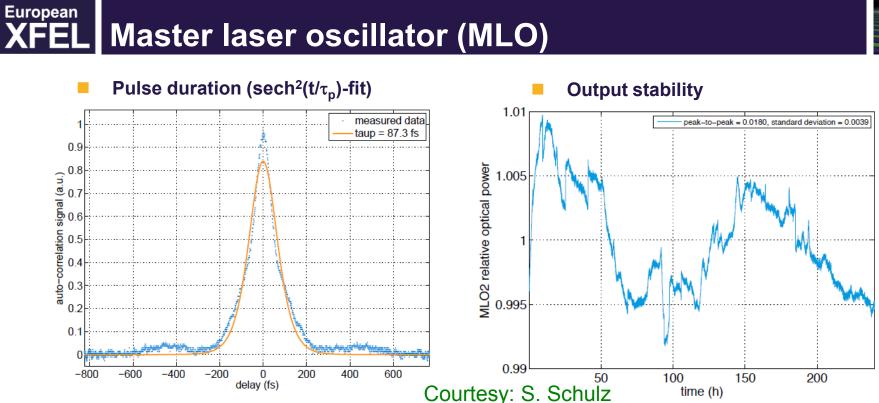
Performance:

- Integrated timing jitter 11.5 fs @ [1kHz, 10MHz] / 4fs @ [10kHz,10MHz]
- Very short pulses < 90 fs</p>
- Power stability: 2% pkpk and 0.4% rms over 240 hours! (no FB stabilization)

Problems

- Output power < 70mW otherwise double pulses</p>
- Reproducibility and maintance





Performance:

- Integrated timing jitter 11.5 fs @ [1kHz, 10MHz] / 4fs @ [10kHz,10MHz]
- Very short pulses < 90 fs</p>

Femtosecond synchronization

Power stability: 2% pkpk and 0.4% rms over 240 hours! (no FB stabilization)

Problems

- Output power < 70mW otherwise double pulses</p>
- Reproducibility (spectrum/pulse duration/phase noise) & maintenance
- Requires special diagnostics and complex exception handling



XFEL Master laser oscillator (MLO)



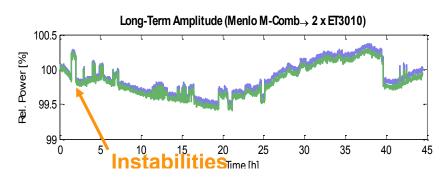
Commercial lasers

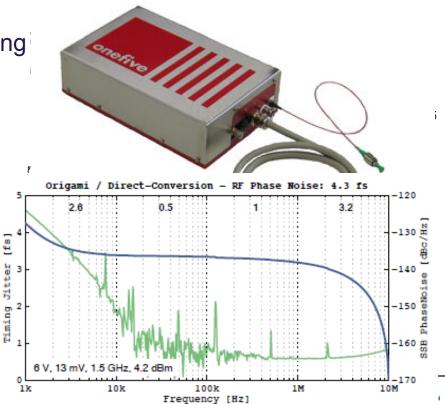
- Based on same technology
- \Rightarrow After some trouble shooting ok
- \Rightarrow High output power > 100 mW
- ⇒ Similar problems in terms of reproducibility
- ⇒ Long term measurements: observed instabilities



Type: saturable absorber laser (SESAM) Output power > 100mW Pulse duration τ_p < 150 fs Robust mechanics, high reproducibility Integrated timing jitter **4.3 fs** [1kHz,10MHz]

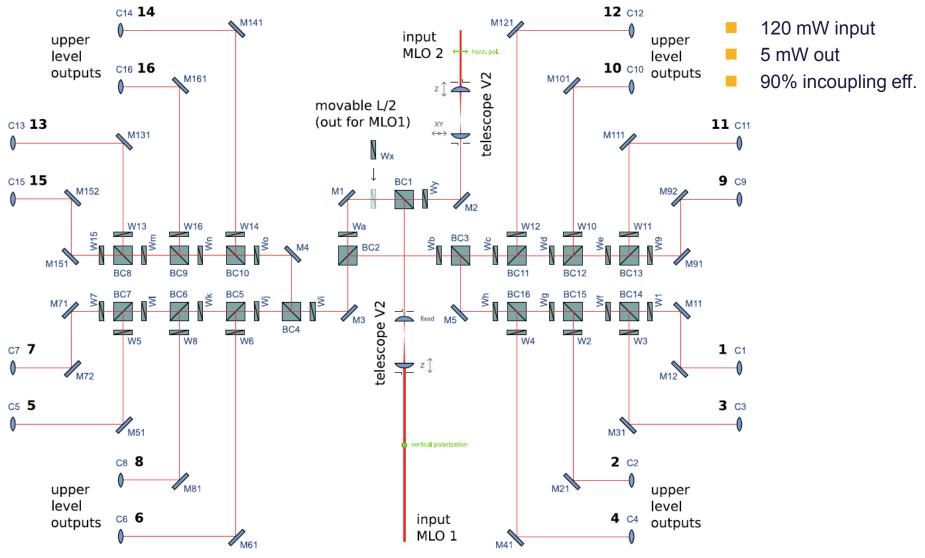
Inspected at tested at PSI Changed specification to our purpose ⇒ Delivery April 2010 Problem: long term live time







Distribution system



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Courtesy: S. Schulz

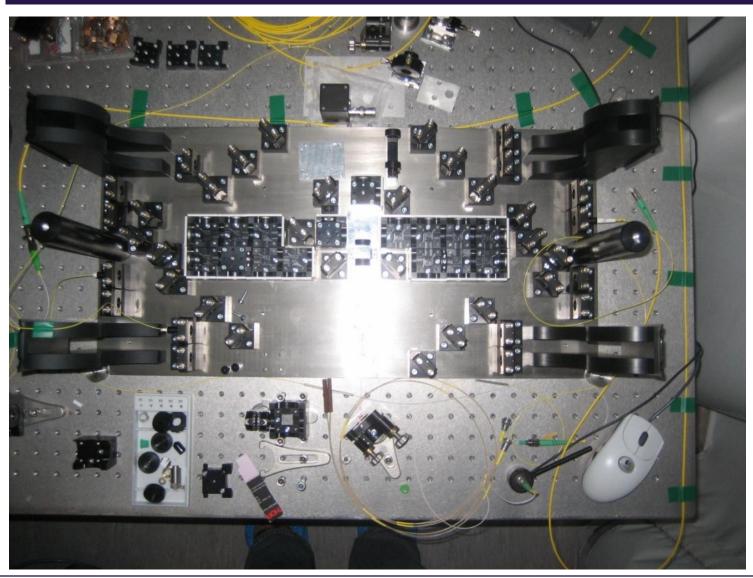






Distribution system



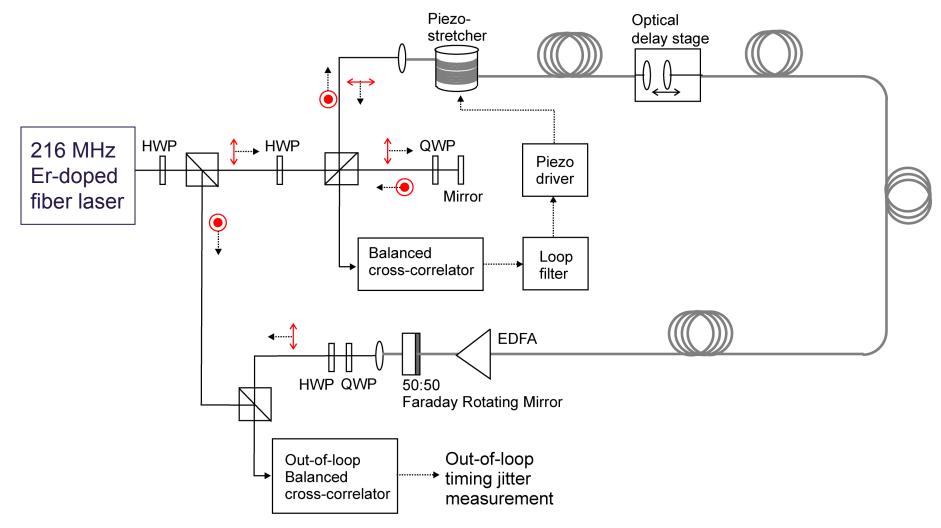


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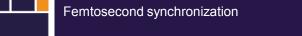
XFEL Fiber Link Stabilization



J. Kim et al., Opt. Lett. 32, 1044-1046 (2007)

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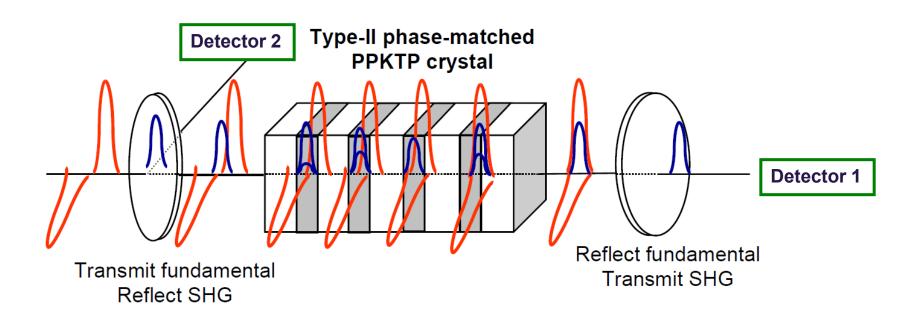




XFEL Fiber Link Stabilization

18



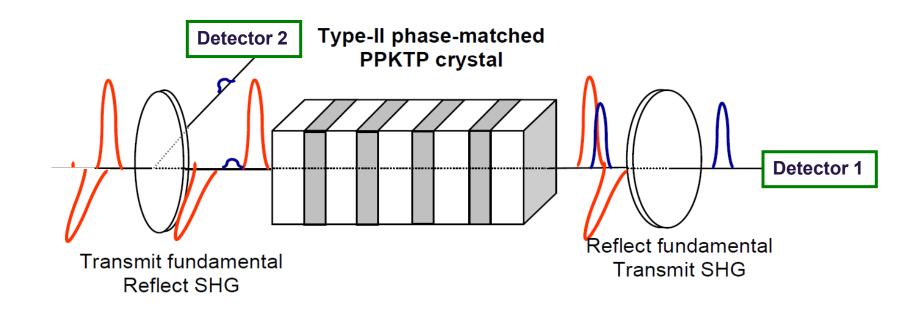






19

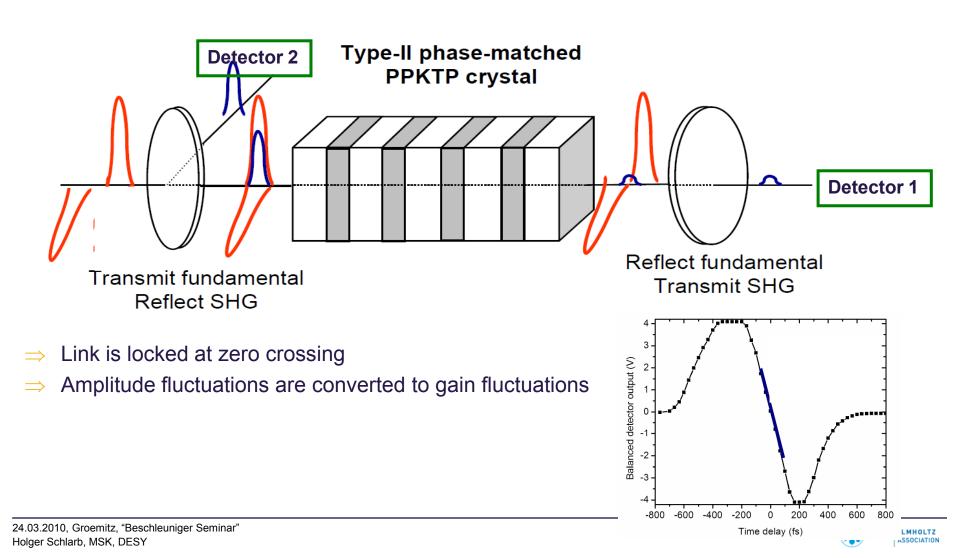
Single crystal background free optical cross-correlator



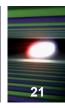




Single crystal background free optical cross-correlator



20



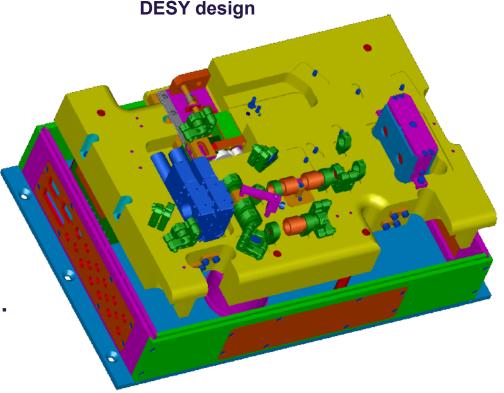
XFEL Fiber Link Stabilization: engineered version

- Three layer design:
 - -Free space optics with OXC (top)-Fiber layer(middle)-Electronic layer(bottom)
- 5 links completely assembled
- mechanically very robust
- several problems identified
 - -Yawing/pitching of delay line
 - -Some optics holders
 - -Easy to assembly
- 2nd prototype design ready for prod.

Drawback link stabilization:

- requires dispersion compensation
- precise overlap of laser pulses
- to cost intensive to supply many end-stations

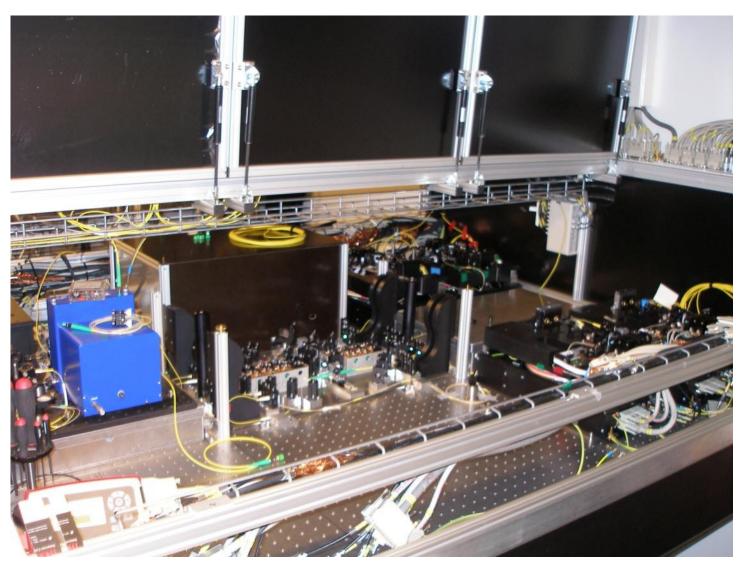
⇒Room for further improvements (packaging/costs)





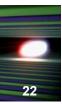


XFEL Fiber Link Stabilization: engineered version



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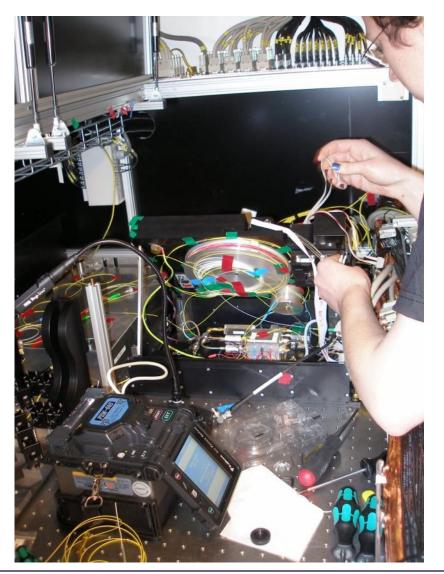






XFEL Fiber Link Stabilization: engineered version



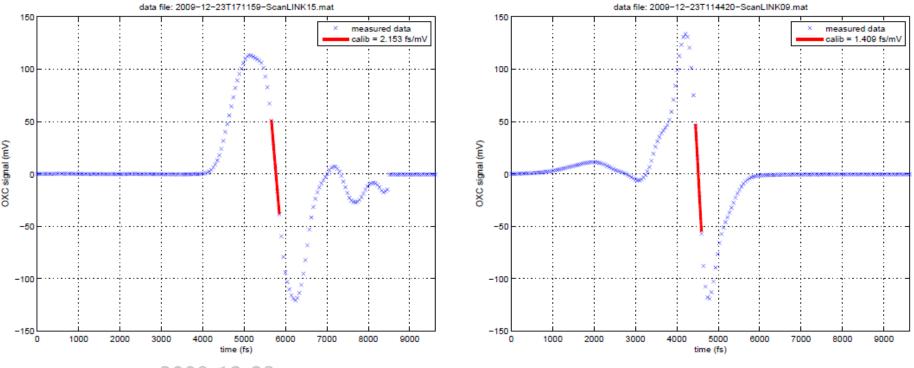






XFEL Fiber Link Stabilization: cross-correlator

- Calibration of detector signal
 - Still varies from link to link (learning curve ...)



measurements: 2009-12-23

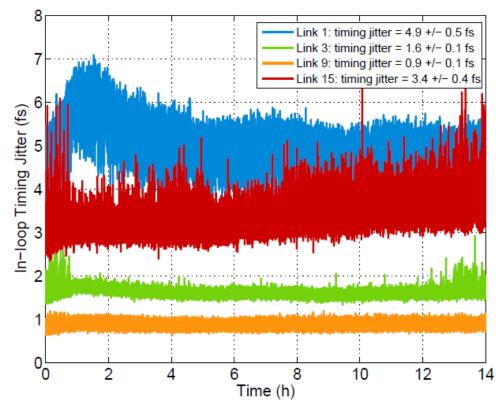
- Varies likely due to dispersion compensation & optical power achieved in link
- Calibration is used to determine remaining in-loop error



European XFEL F

Fiber Link Stabilization: cross-correlator

- Calibration of detector signal
 - Still varies from link to link (learning curve ...)



Out-of-loop jitter cannot be determined easily
 LINK15 uses different link end configuration





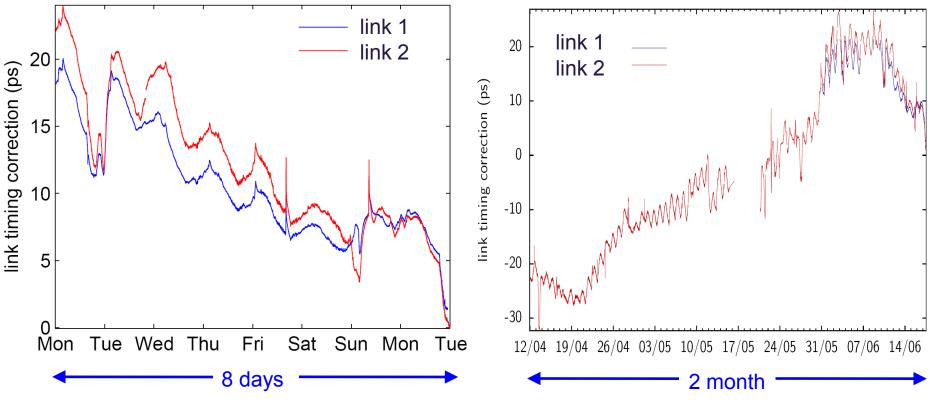
European



Fiber Link Stabilization: long term behavior

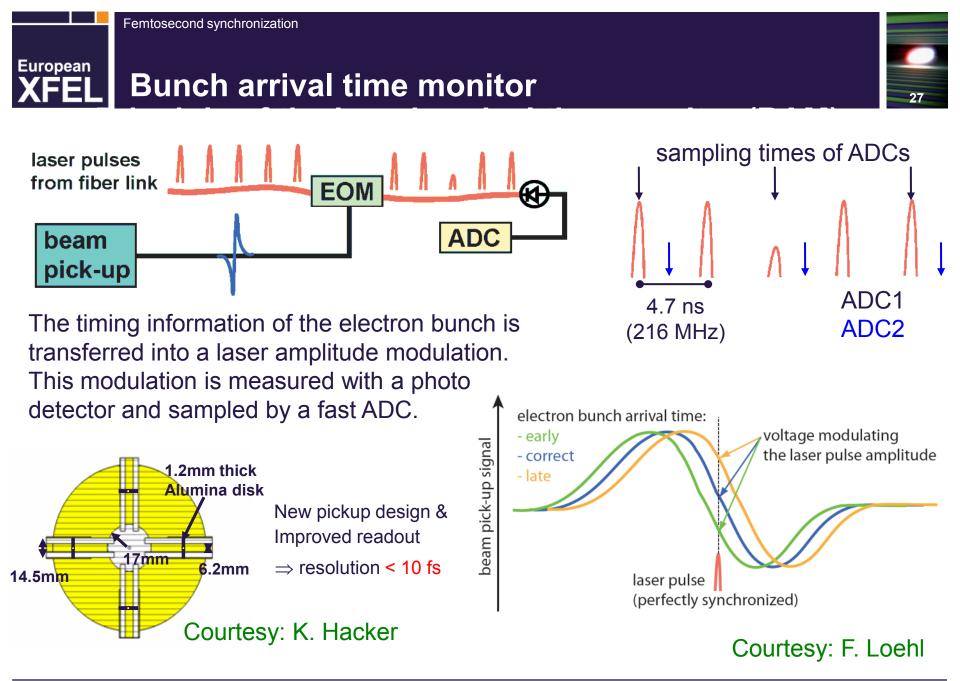
Installation of two fiber links at FLASH

- Much smoother correction
- For 6 month in operation
- Day/night periods & spring -> summer observable
- Smooth operation (interruptions understood)



Courtesy: F. Löhl





See: EPAC'06, Loehl et al., p.2781



XFEL Bunch arrival time monitor



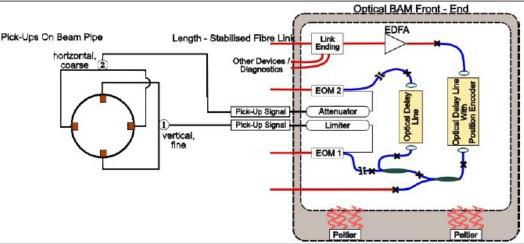
Major Changes of the Latest BAM compared to the BAMs Installed Further Downstream

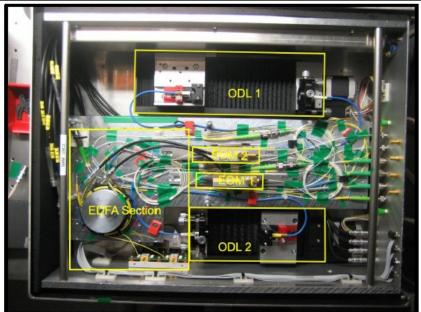
Bunch arrival time monitor (BAM)

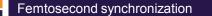
- Coarse and fine measurements
- 10GHz Mach-Zehnder interferometer
- Precision encoder + high duty cycle delay stage
- Controls: Bias, Temp. readout & regulation
- New engineered design (2nd iteration)

Performance & findings:

- Typical resolution: 6-10 fs, (down to 4 fs) @ 1n(
- Stable operation after establishing FB on coarse
- Assembly + slicing ~ 4 weeks
- Problematic fiber management (too long fibers)
- hardware + firmware + automation software

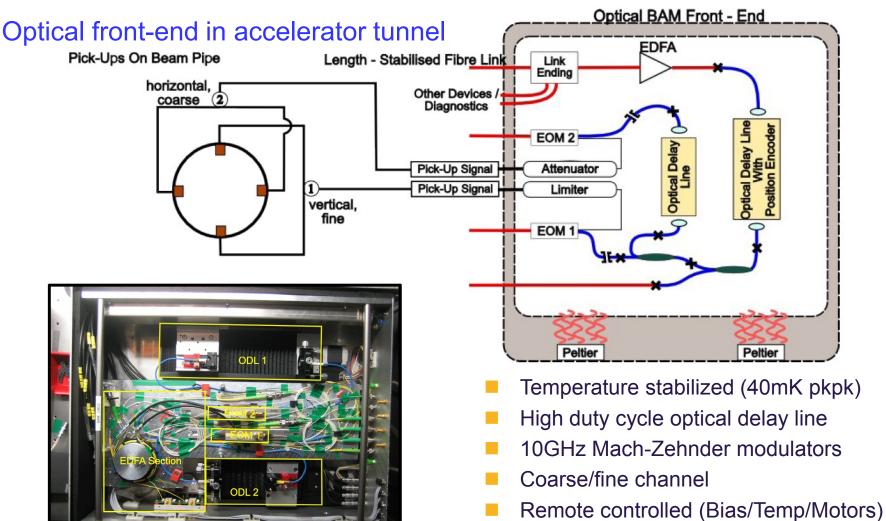






XFEL Bunch arrival time monitor



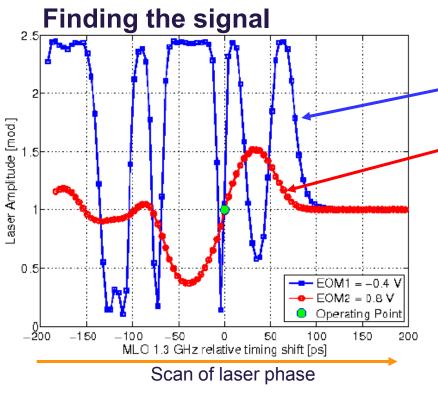


Heidnhain encoder

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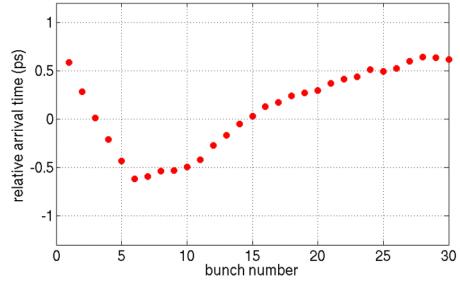
XFEL Bunch arrival time monitor



Fine channel, dynamic range \sim 4 ps, intrinsic resolution \sim 4-10fs

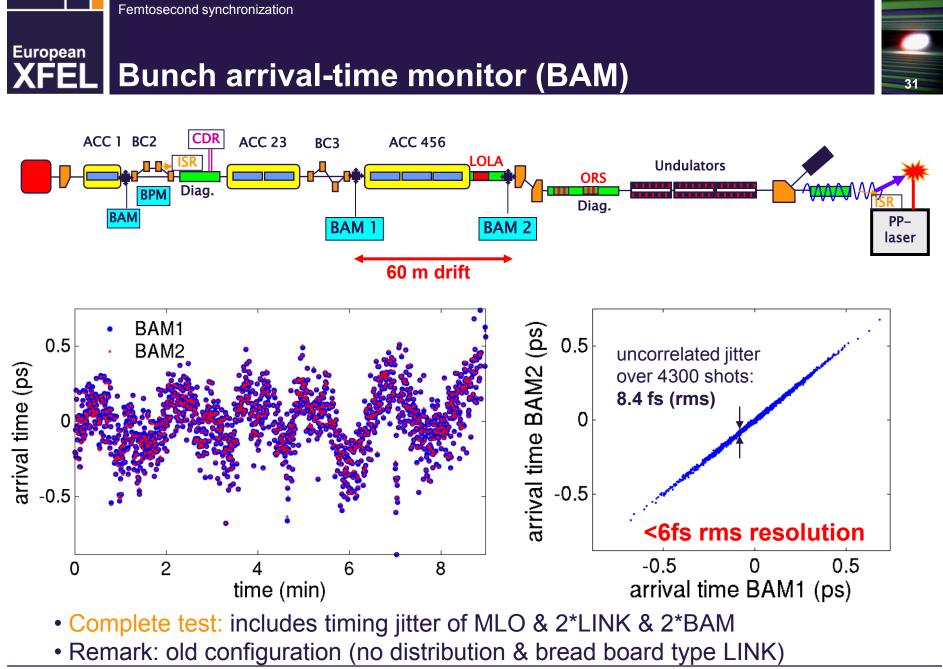
Coarse channel, dynamic range ~ 60 ps, intrinsic resolution ~ 60-100 fs

Fluctuations within pulse train

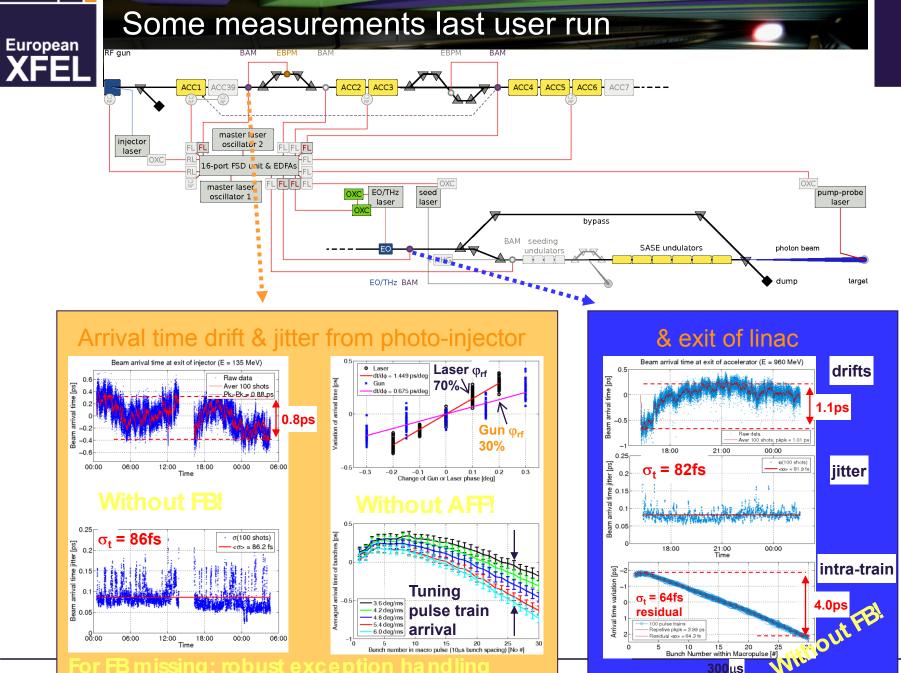




30



Courtes





XFEL Infrastructure for FLASH optical synchronization

- 300 cables to optical table (including 120 signal cables)
- 58 motors
- 20. . . 24 laser diodes for MLOs and EDFAs
- 16... 20 piezo stretchers
- 4 VME crates (change to uTCA in planning)
- optical fibers to all critical end-stations
- 42 temperature sensors, 2 active temperature regulations
- air conditioning and option for water cooling
- ... and almost everything assembled in-house!

Status: ~ 95% done

In house electronics developments:

- VME laser diode driver (FPGA)
- VME ADCs 130MHz, 16bit
- LN Piezo driver ±300V
- DSP regulation (old)
- ADC (9MHz, 14bit)
- DAC (9MHz, 14bit)
- Vector modulator

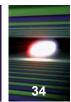
Status: exist or in productions











Thanks for your attention

