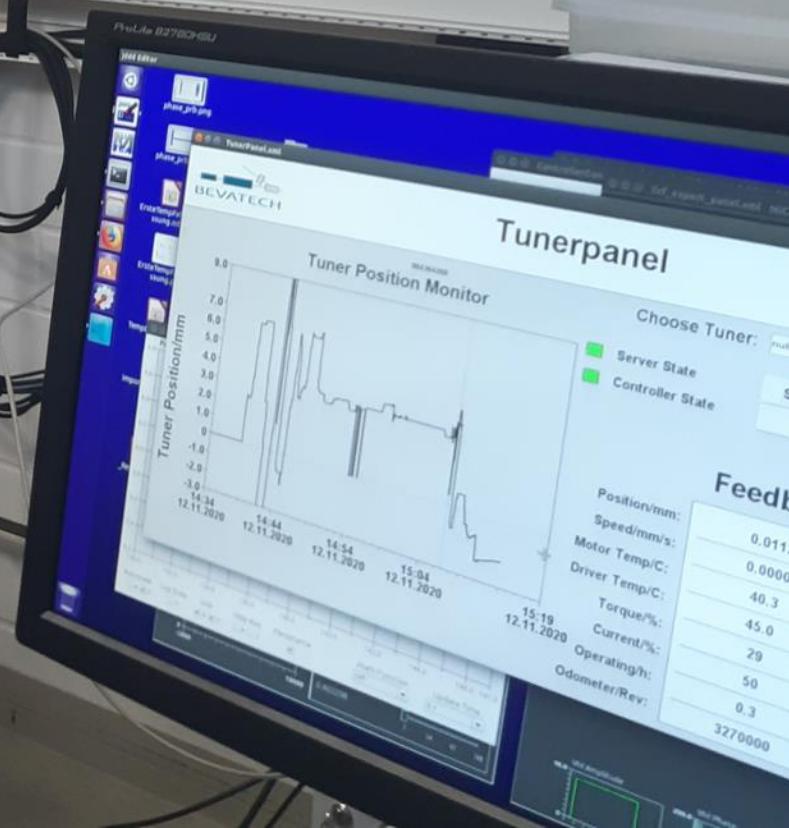
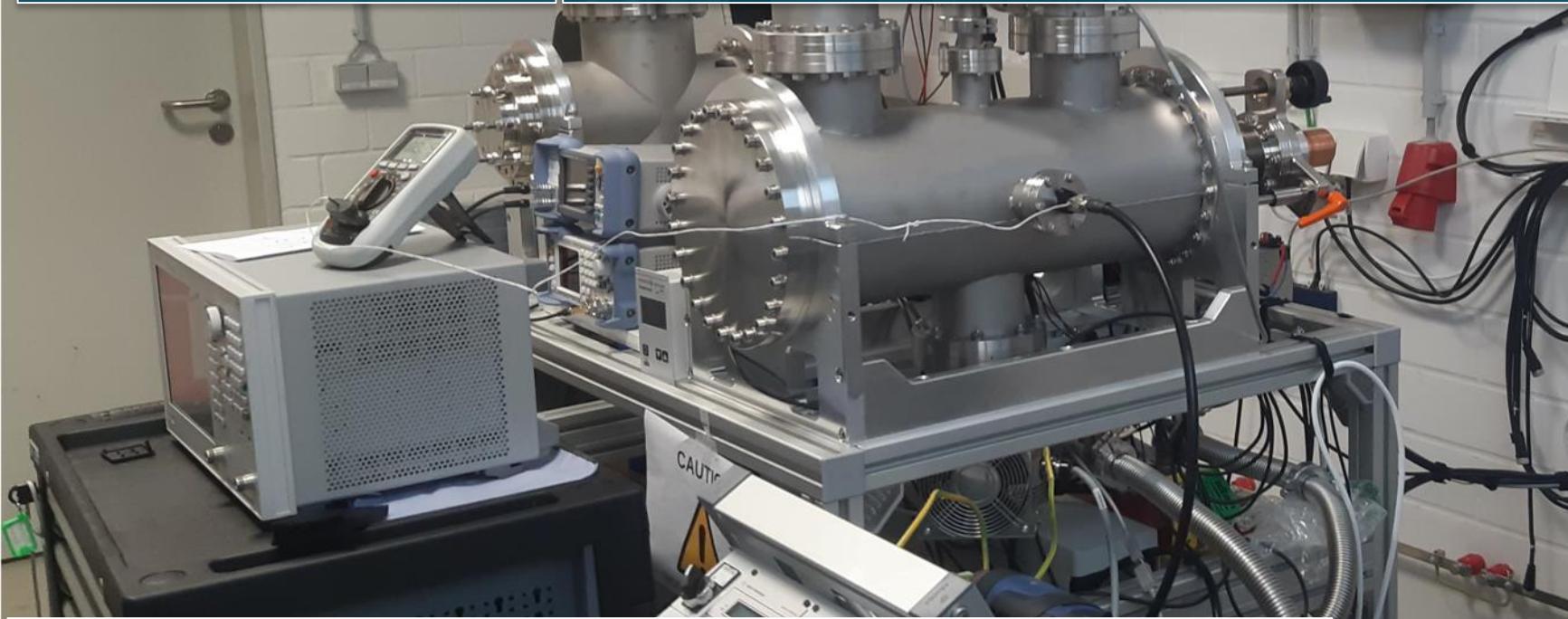


# A MTCA based LLRF for the LILAC injector of the NICA project

1. December 2020

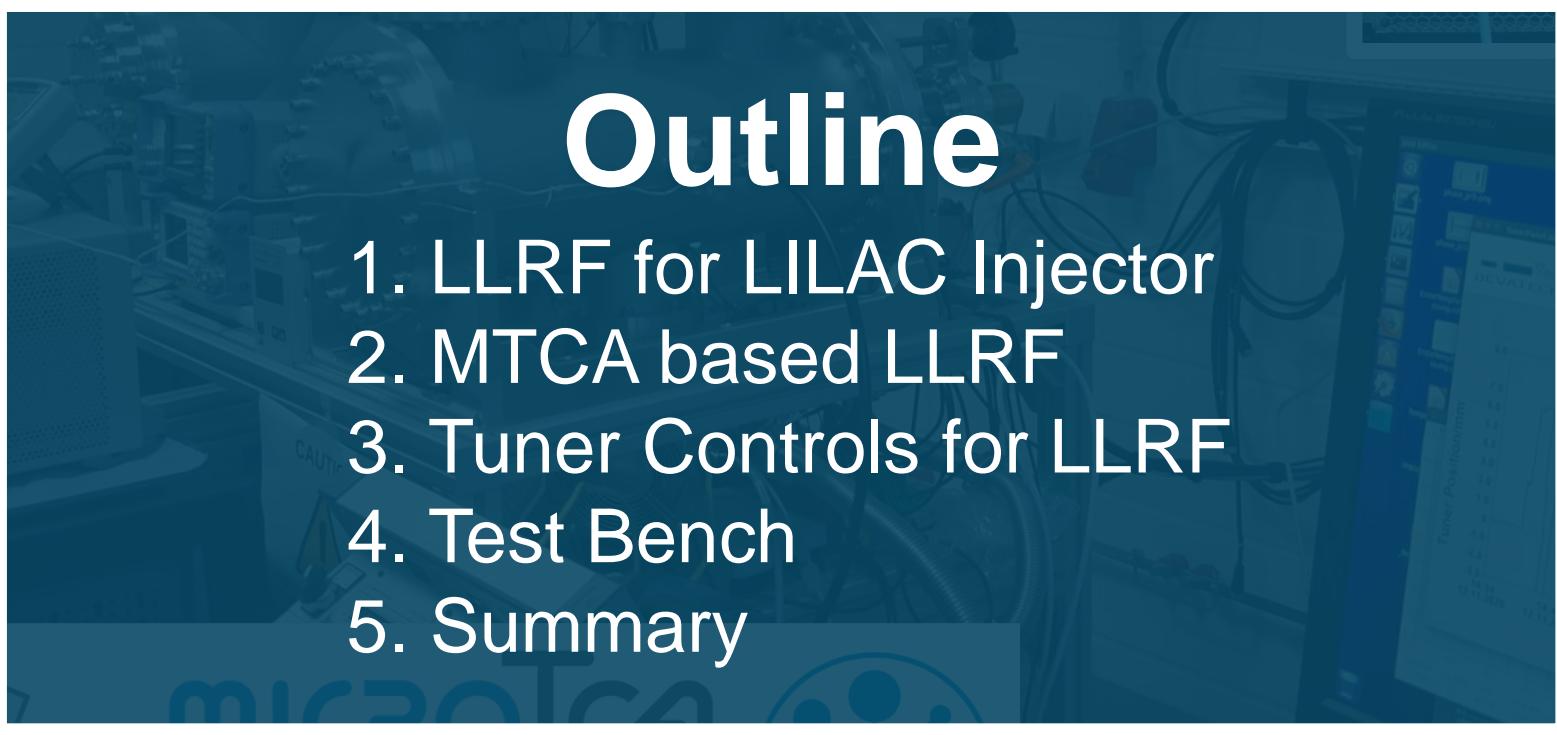
Christian Trageser, BEVATECH GmbH



# A MTCA based LLRF for the LILAC injector of the NICA project

1. December 2020

Christian Trageser, BEVATECH GmbH



## ACCELERATORS & VACUUM



- Complete LINAC Systems
- RF Cavities
- Beam Diagnostic
- Vacuum Technology
- Tumor Therapy

## ENGINEERING & CONSULTING



- Beam Lines
- Vacuum
- Design Studies
- RF Research
- Outsourcing
- Project Management

## ELECTRONICS & IT



- Micro TCA
- Automation
- Instrumentation & Controls
- Project Management

# 1. LLRF for LILAC Injector

## Nuclotron-based Ion Collider fAcility @ JINR



- Accelerator and Experimental Complex

Main Goal:

- Provide ion beams for hot and dense strongly interacting baryonic matter and spin physics

Facility parts:

- Light Ion LinAC (LILac)
- Heavy Ion LinAC (HILac)
- NUCLOTRON SC Synchrotron
- BOOSTER SC Synchrotron
- Colider Ring
- Experiments

LILac will  
replace LU-20

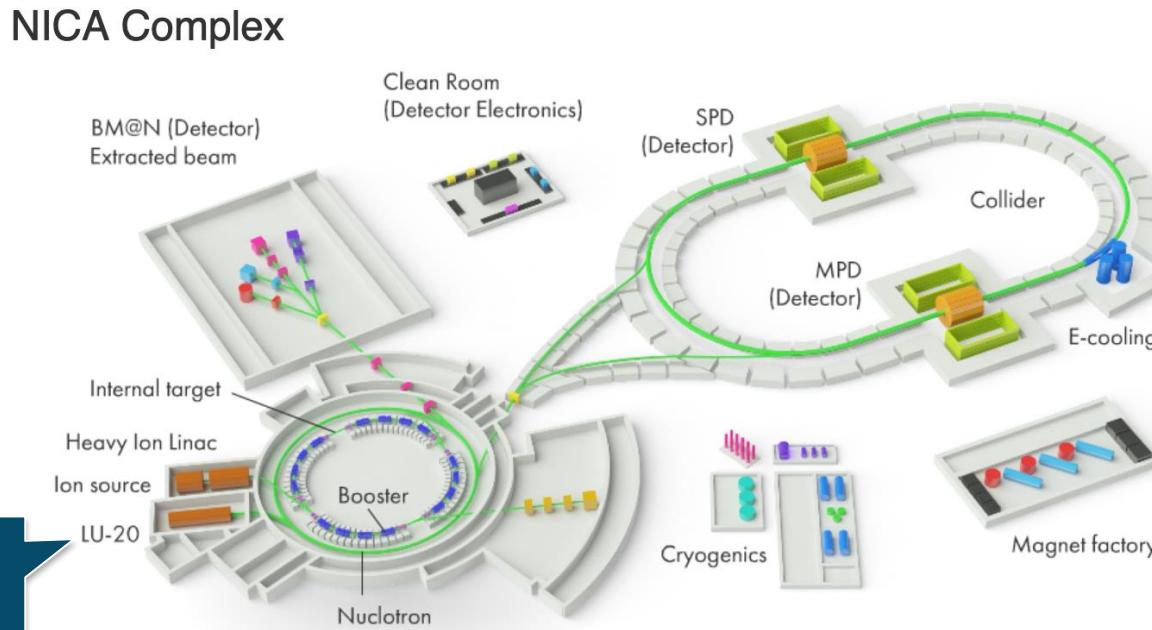


Image from <https://nica.jinr.ru/complex.php>

Parameter	Protons	C <sup>4+</sup>
Min/max A/q:	1	3
Injection Energy/keV:	30	60
Exit Energy/MeV:	7	21
Beam Current/mA	5	15
Frequency/MHz:		162.5
Repetition Rate/Hz		5
Current Pulse Length/μs		30
RF Pulse Length/μs		200
Pulse Power/MW		1.8
Transmission		> 80 %
Full Length LINAC/m		< 12.5



LEBT

RFQ

REBUNCHER

IH1

IH2

IH3

DEBUNCHER

NUCLOTRON

6 cavities

# What is a Low Level RF (LLRF)?

**LLRF**  
**Low  
Level  
Radio  
Frequency**

**What?**

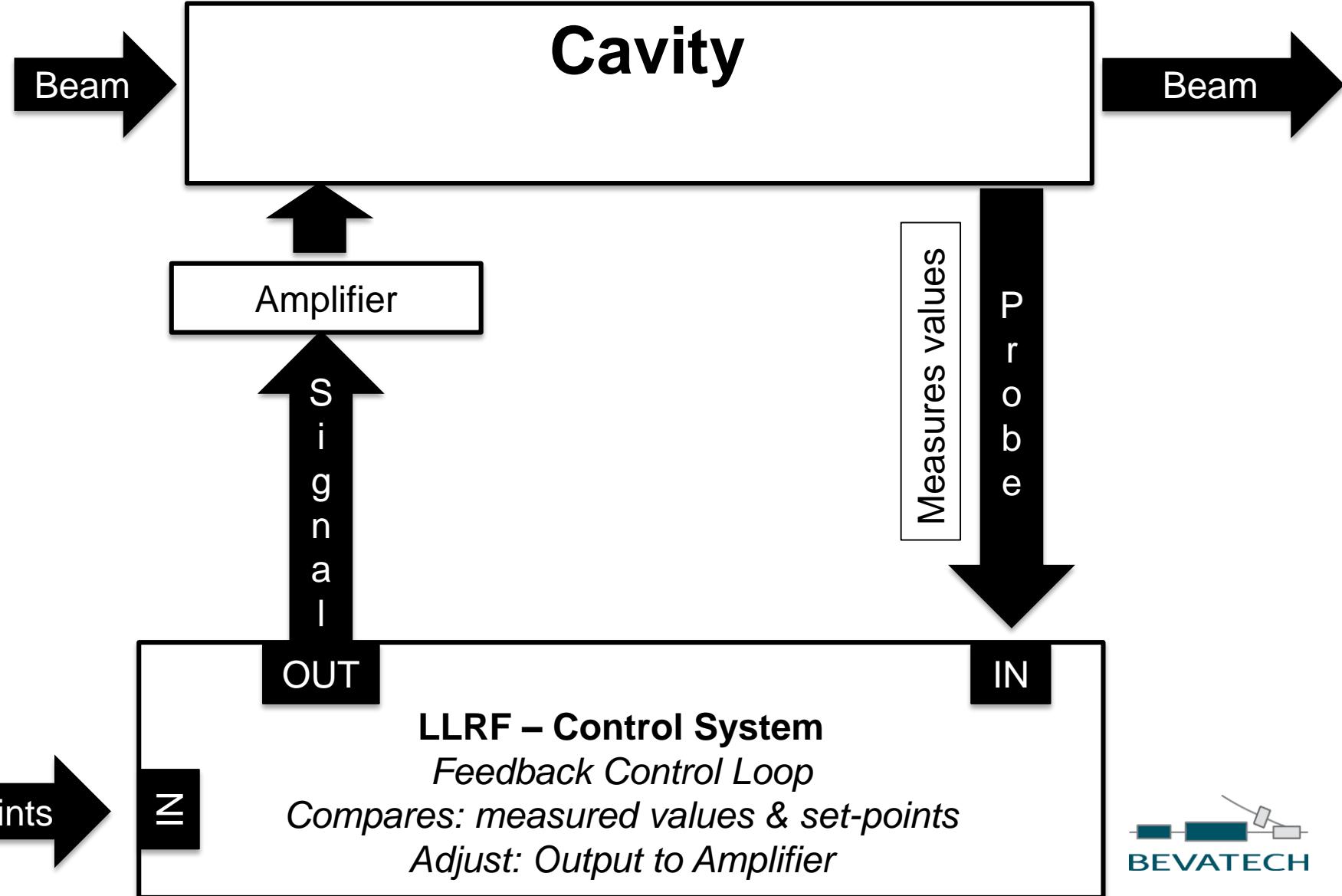
Stabilizes amplitude,  
frequency, phase

**Why?**

Improves beam loss,  
energy spread, beam  
phase

User

Set-points



# What is a Low Level RF (LLRF)?

## LLRF Low Level Radio Frequency

*What?*

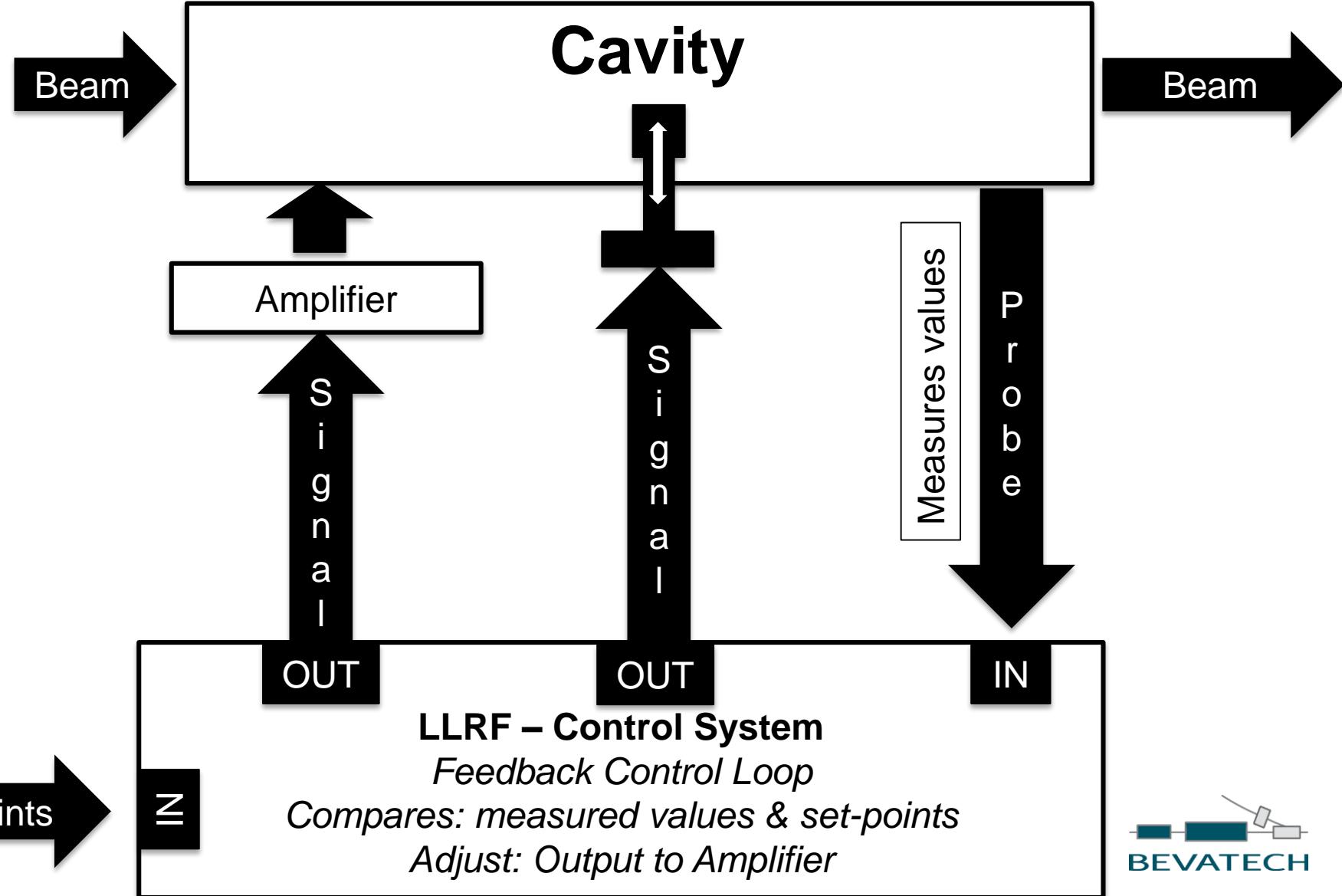
Stabilizes amplitude,  
frequency, phase

*Why?*

Improves beam loss,  
energy spread, beam  
phase

User

Set-points





# Cooperation

2017

Discussion to derive XFEL LLRF for Hadron Linacs

2018

Suggested TDR to JINR

2019

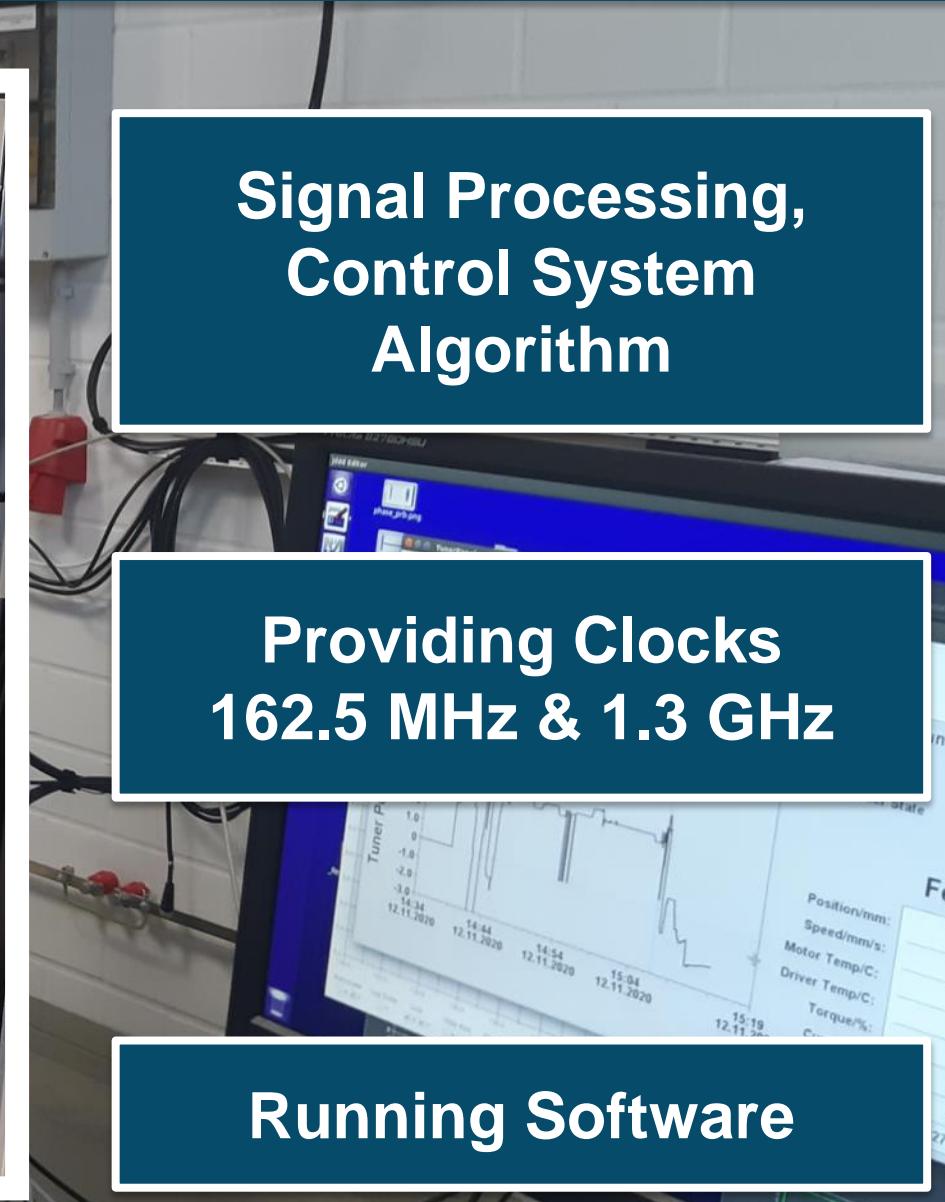
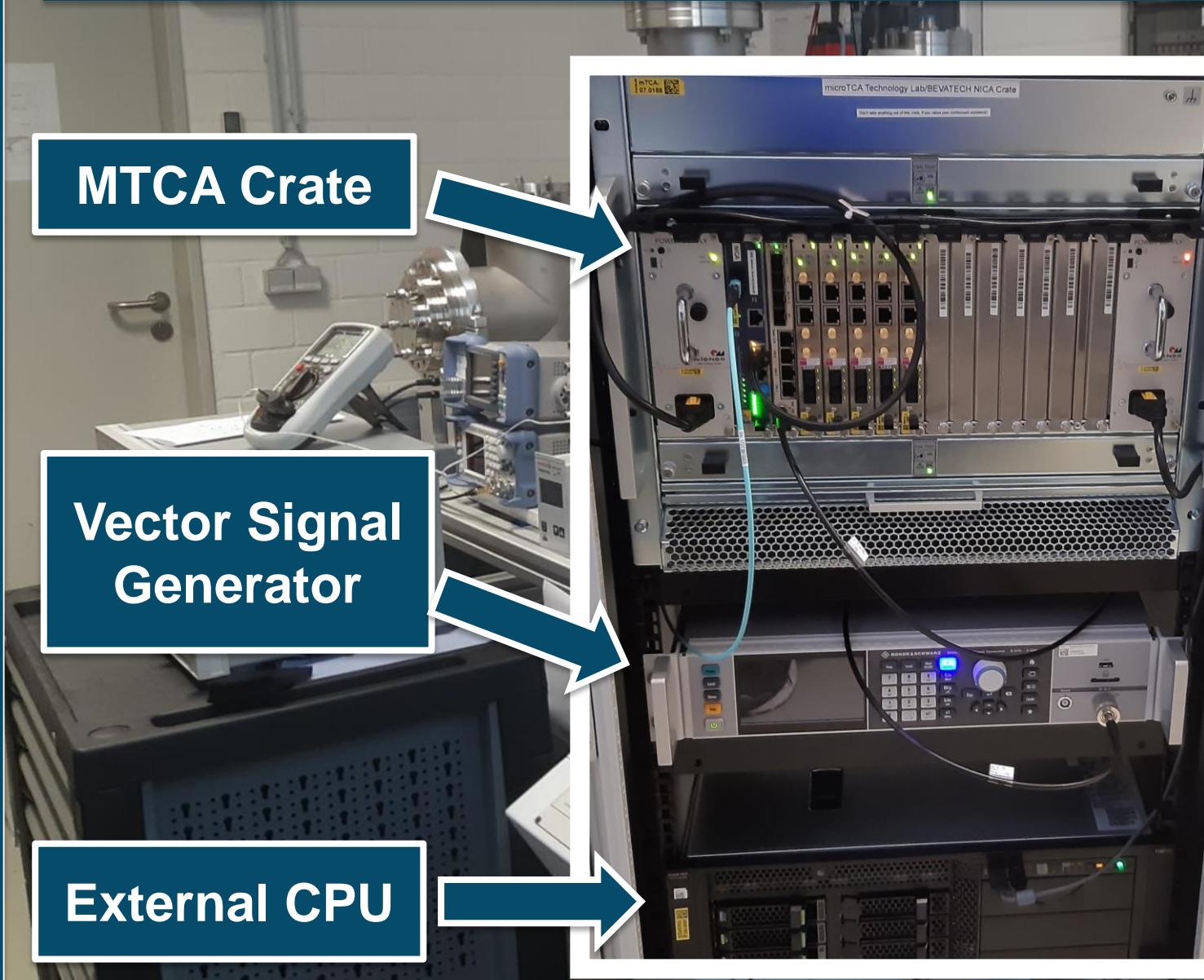
Developing and Testing LLRF

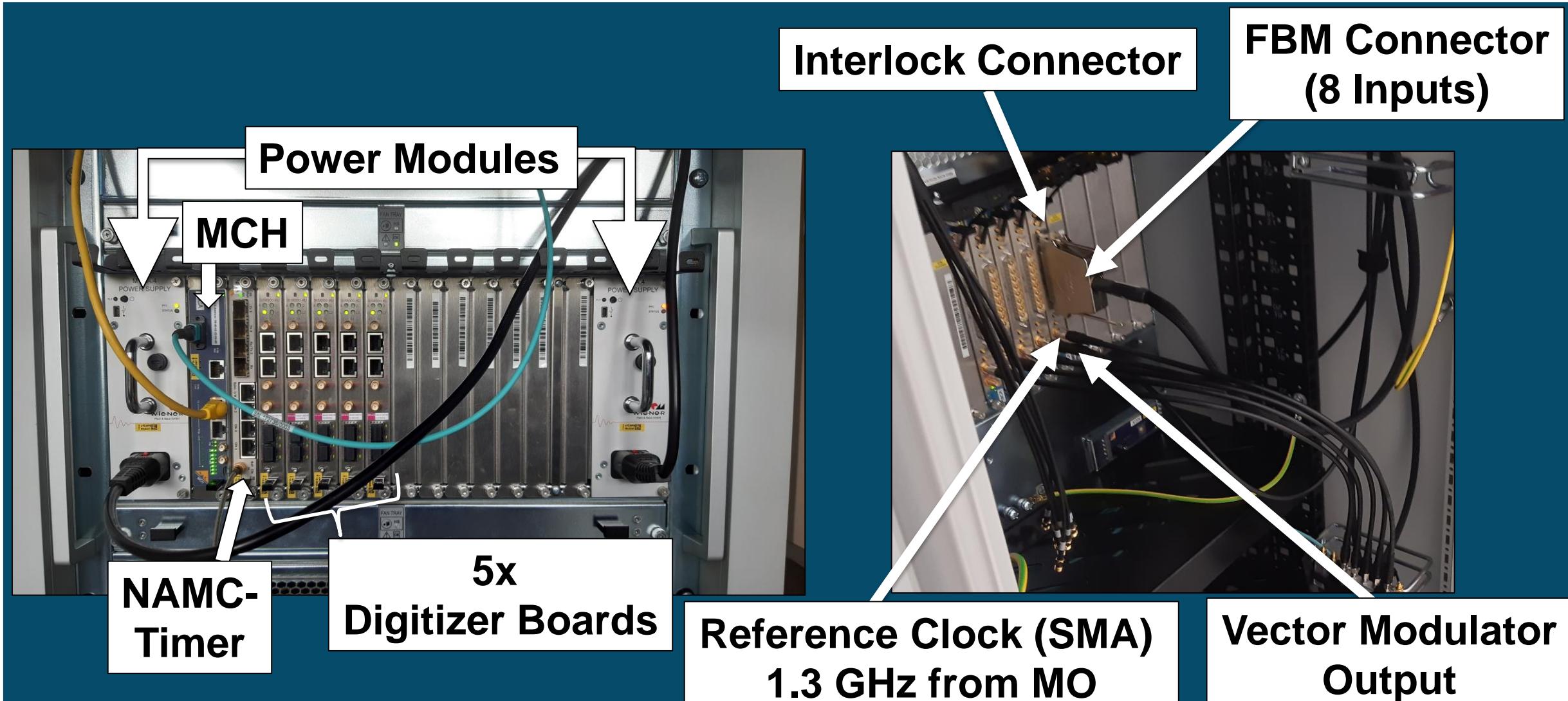
2020

Awaiting Commissioning  
with first cavity

## 2. MTCA Based LLRF

LLRF Parts

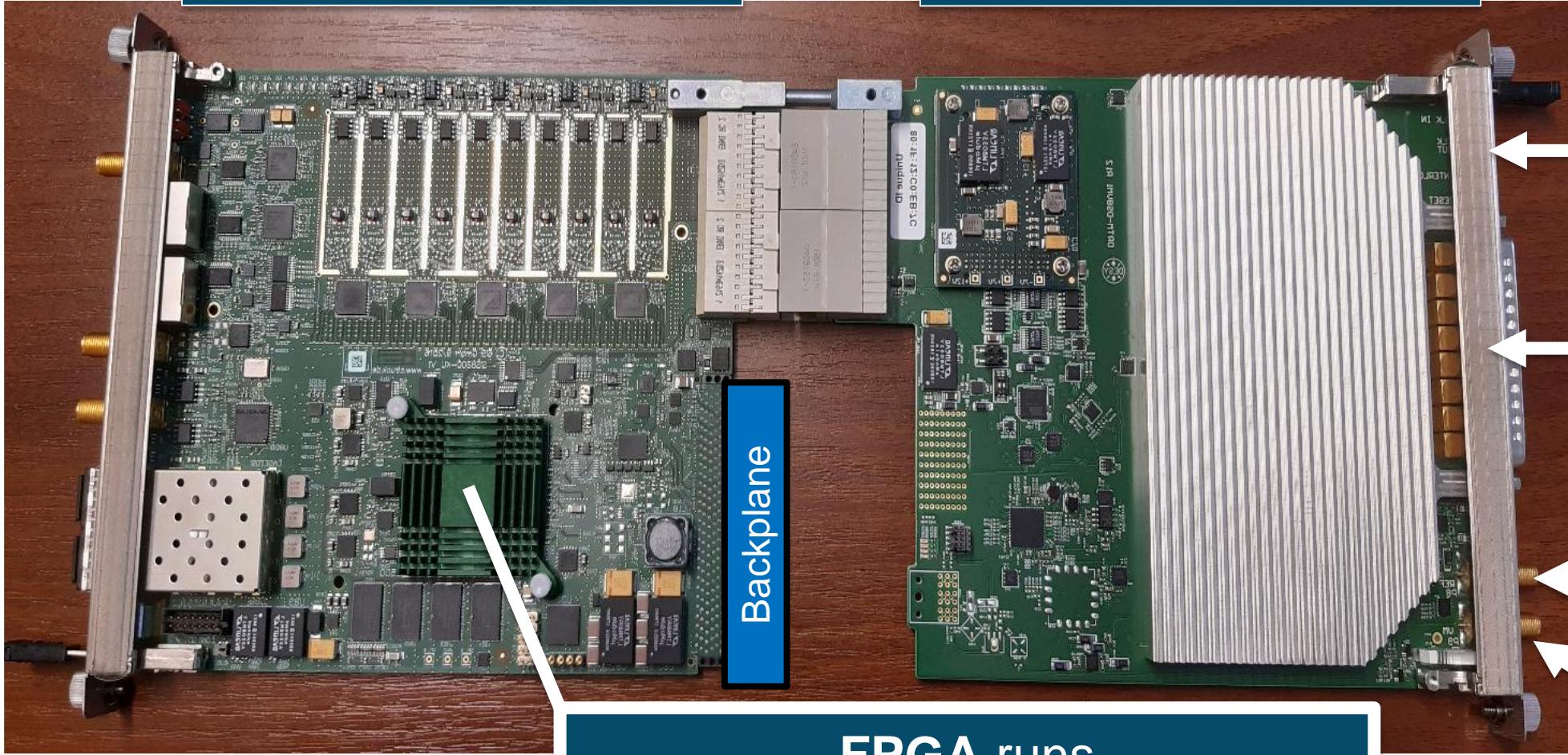




MTCA – Crate (front)

MTCA – Crate (rear)

AMC/RTM for each cavity



struck innovative  
systeme

Front  
AMC: SIS8300KU

Rear  
RTM: DS8VM1

FPGA runs  
control algorithm for  
Amplitude/Phase Control System



Interlock

Probe,  
Forwarded,  
Reflected  
Signals

Ref. Clock  
(162.5 MHz)

Vector  
Modulator  
(Output)

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# DOOCS.

The Distributed Object-Oriented Control System Framework

JAVA  
DOOCS  
DATA  
DISPLAY



## Firmware (on FPGA)

*Control Algorithm*



## DOOCS Server (on external CPU)

*Configuring, Controlling,  
Monitoring, Connecting  
Digitizer Boards*

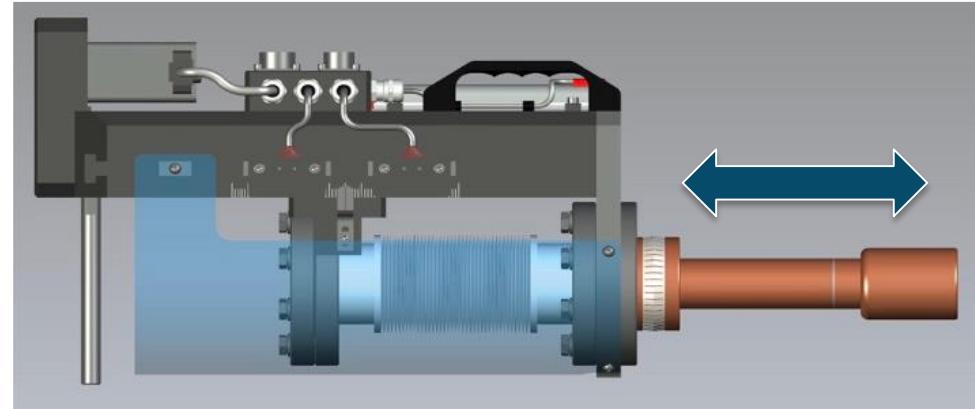


## GUI (remote)

*Interaction User*

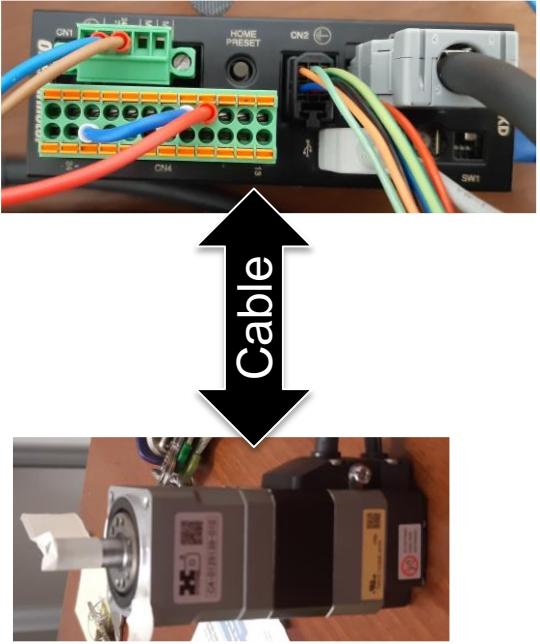
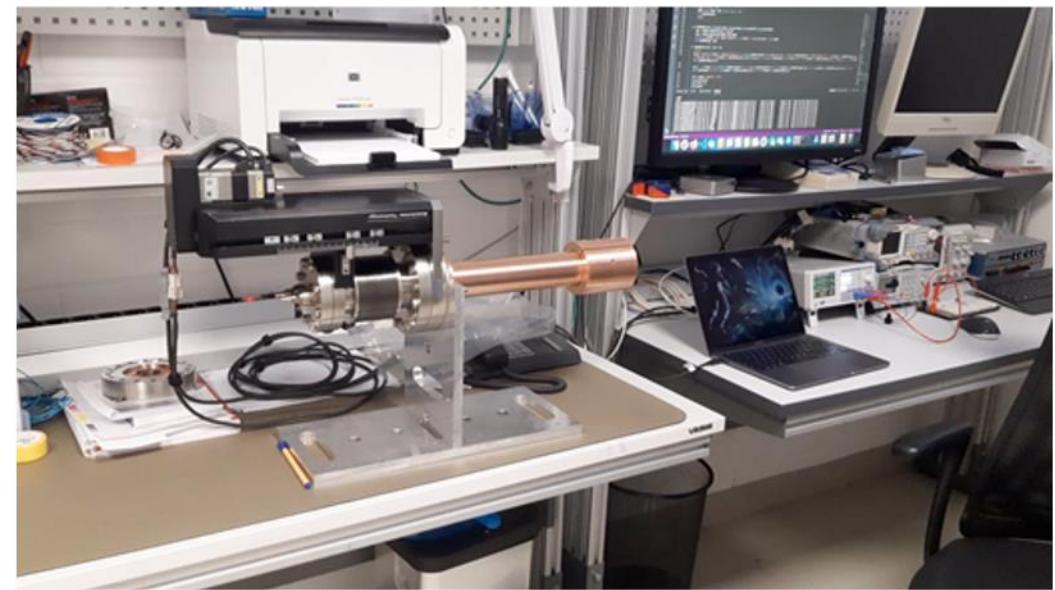


# 3. Tuner Controls



## Communication

Orientalmotor



Modbus



gridconnect®

TCP

LLRF



**microTca**  
TECHNOLOGY LAB

A HELMHOLTZ INNOVATION LAB

BEVATECH

**Write in ChimeraTK**

**Compile/link to**

**EPICS**

**OPC UA**

**DOOCS**

**TANGO**

*not yet available*

**Switch between**

**LLRF so far DOOCS**

**NICA uses TANGO**

**Control system and  
Hardware  
Interface with  
Mapped and  
Extensible  
Register-based device  
Abstraction  
-  
Tool  
Kit**

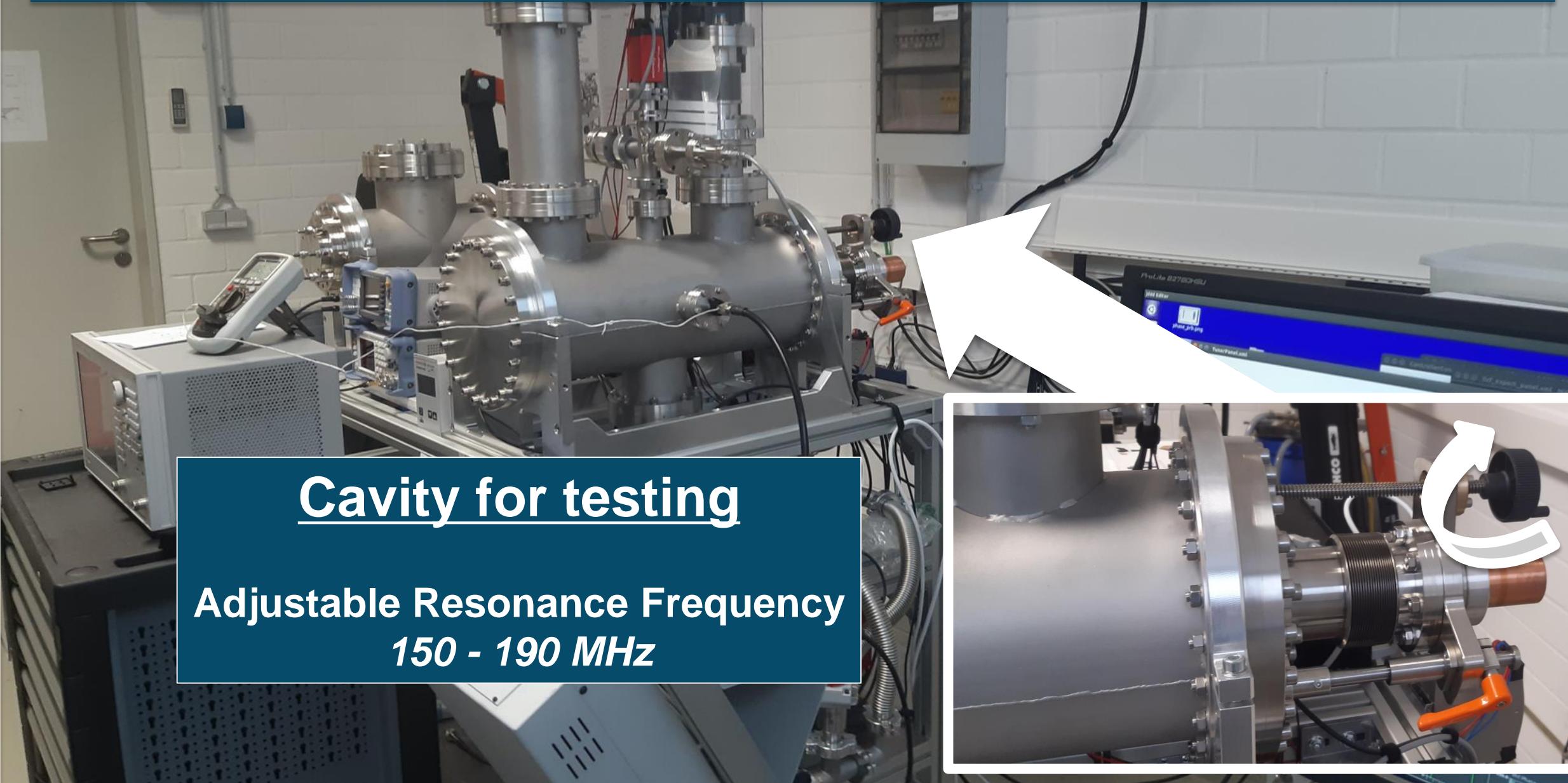
**Open Source**

<https://github.com/ChimeraTK>

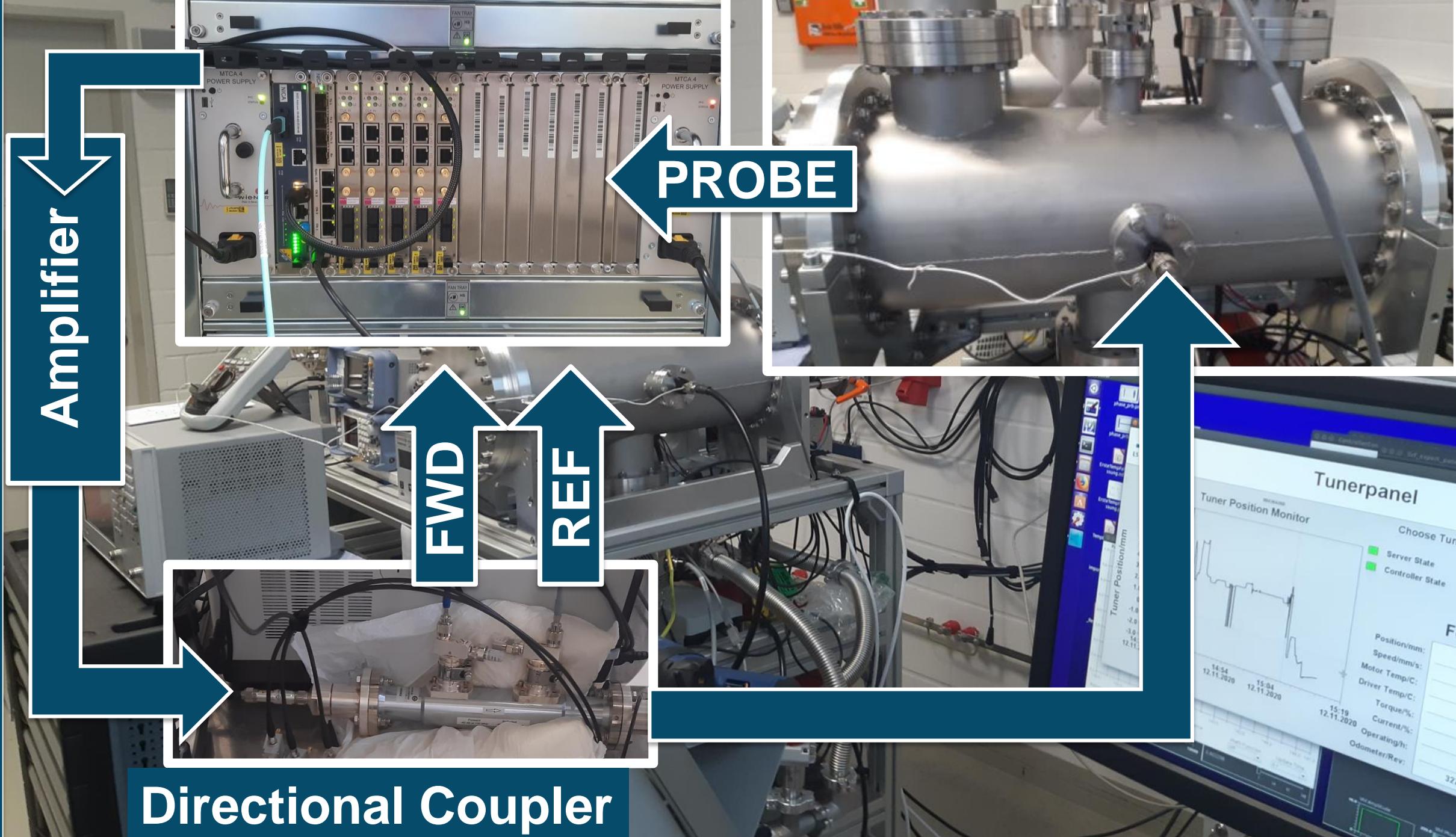


# 4. Test Bench

Test Bench - Cavity

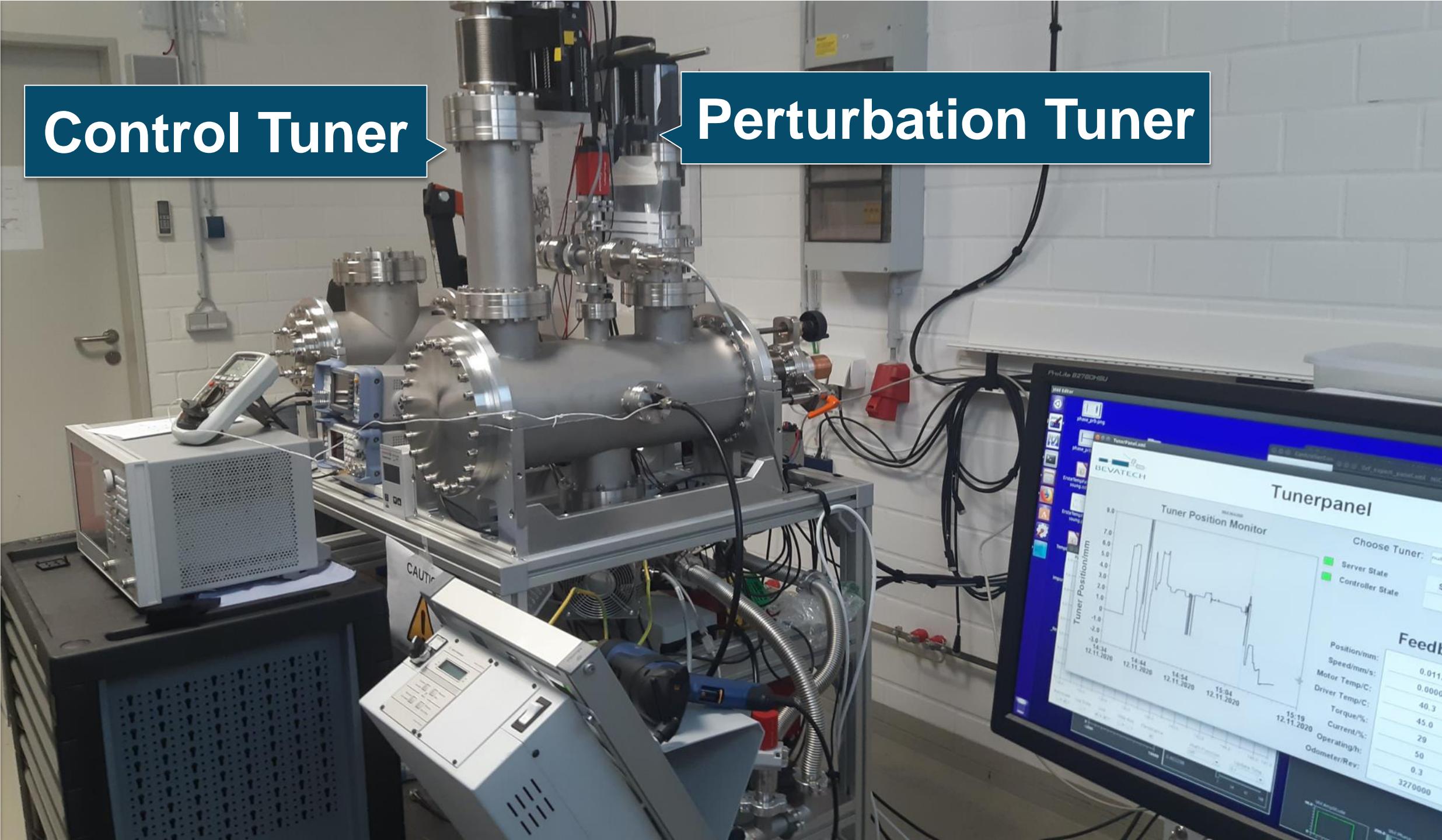


## Test Bench – Connection to LLRF



Control Tuner

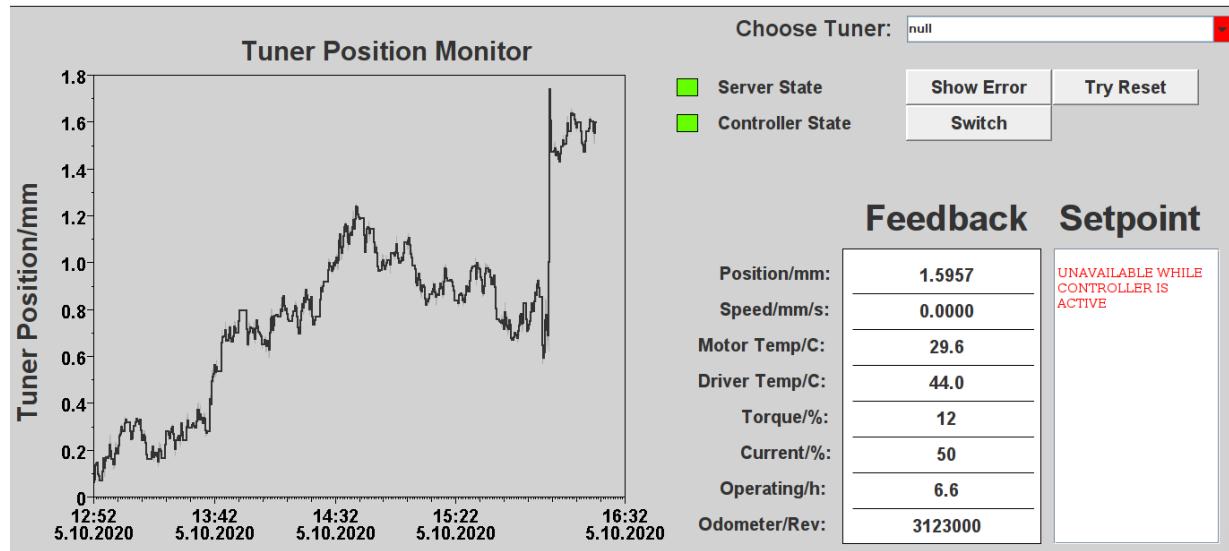
Perturbation Tuner





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## Tunerpanel



## Controller Configuration

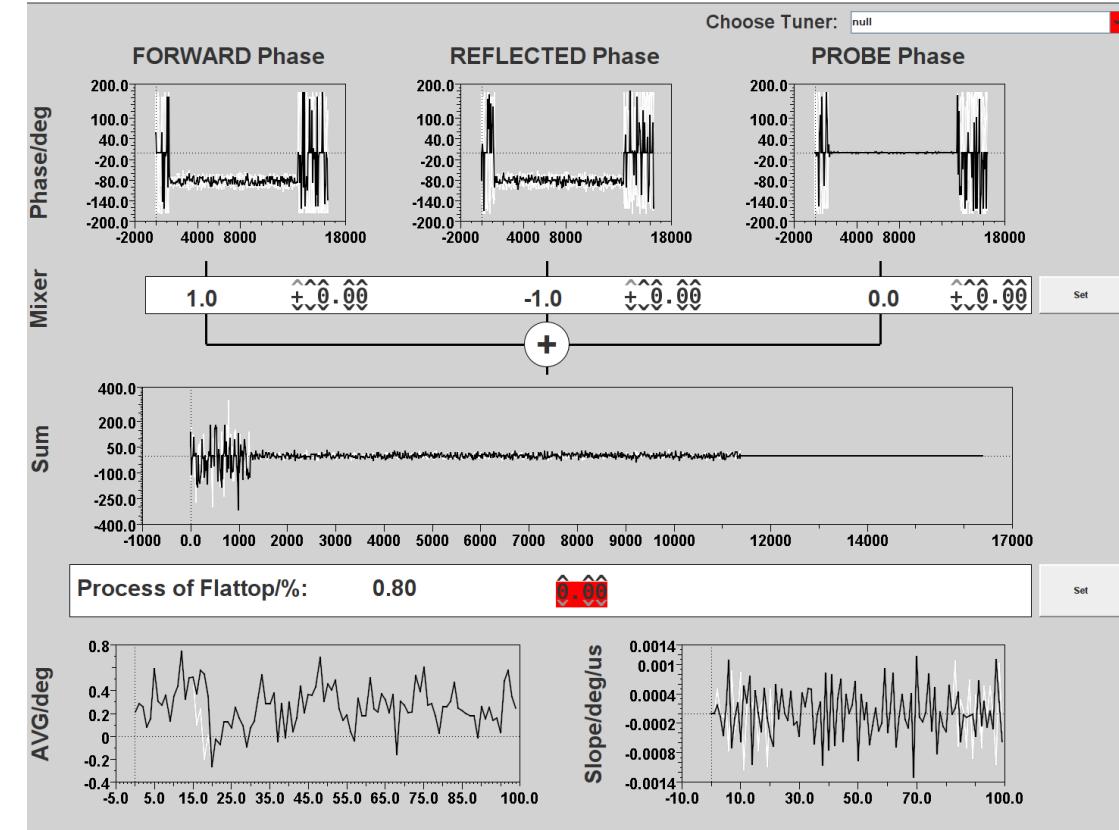
Choose Tuner: null

Feedback	Setpoint
Response Phase Coeff: 0.024000	<del>±0.024000</del>
Response Threshold: 0.000100	<del>±0.000100</del>
Puls Divider: 15	<del>15</del>
Min Valid Tuner Pos/mm: -20.0000	<del>-20.0000</del>
Max Valid Tuner Pos/mm: 20.0000	<del>+20.0000</del>
Tuner Speed/ mm/s: 4.00000	<del>4.00000</del>
Tuner Pos Gain: -0.0440	<del>-0.0440</del>
Tuner Pos Offset: 0.0000	<del>0.0000</del>

**Set**    **Default**



## LLRF Data Processor



# Lab GUI



# 5. Summary

- System under testing @ BEVATECH
- Tuner Control System is under development
- Testing with cavities is ongoing

- MTCA based LLRF highly customizable and flexible
- ChimeraTK allows for early use of the system and later easy adaption to NICA control system environment
- JDDD allows to create simple GUIs quickly
- Excellent Cooperation with MTCA Techlab