



The Phase-2 Upgrade of the CMS Outer Tracker

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Martin Lipinski for the CMS Collaboration

I. Physikalisches Institut B, RWTH Aachen University

GEFÖRDERT VOM



Bundesministerium
für Bildung
und Forschung

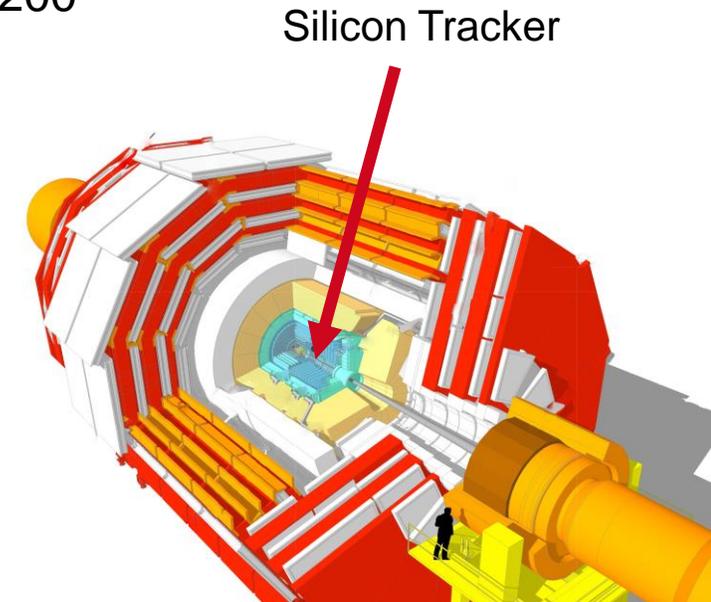


High-Luminosity LHC upgrade (HL-LHC): luminosity up to $5-7 \times 10^{34} \text{ cm}^{-2}\text{s}^{-1}$

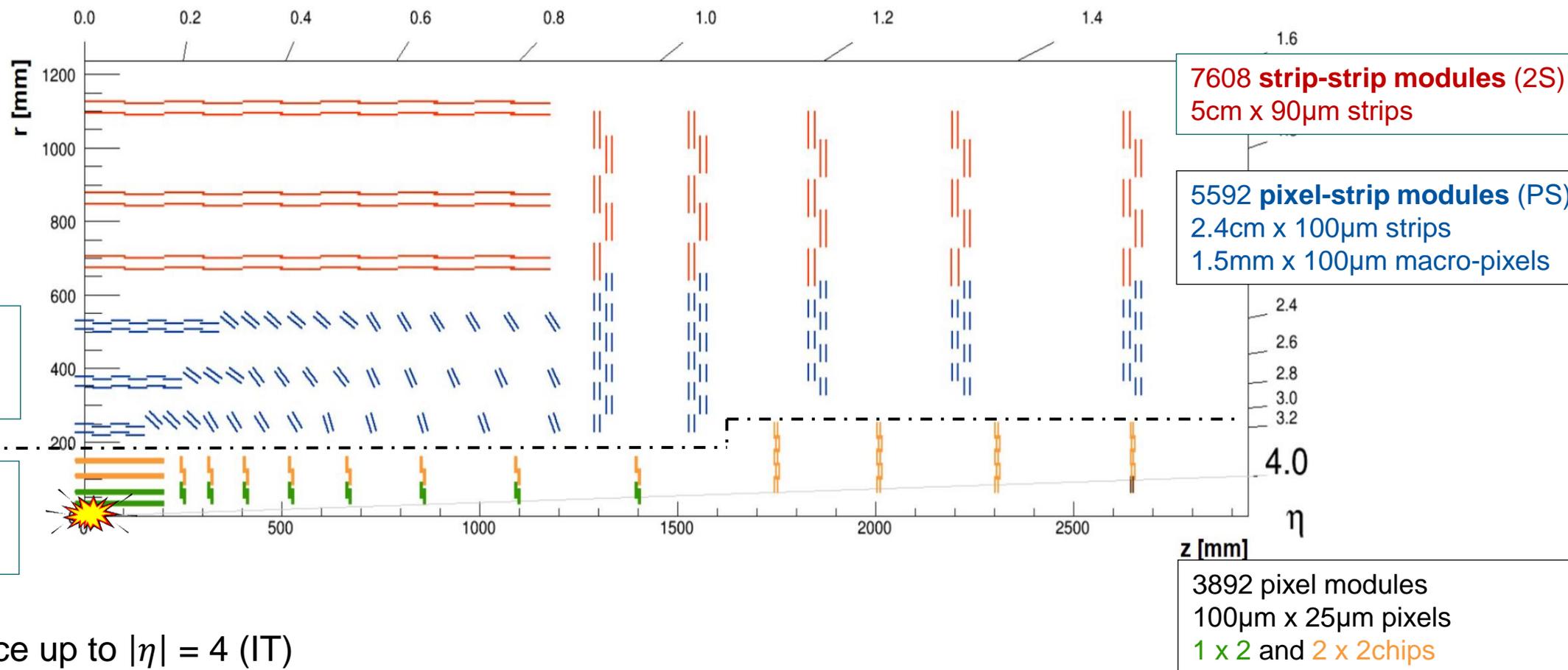
Requirement for the CMS tracker: Maintain or improve tracking performance with 200 simultaneous interactions (pile-up)

- 8x higher pile-up: 25 \rightarrow 200
- 8x higher hit-rate: up to 3.2 GHz/cm^2 (innermost pixel layer)
- 7.5x higher trigger rate: 100 kHz \rightarrow 750 kHz
- 4x longer trigger latency: $3.2 \mu\text{s} \rightarrow 12.8 \mu\text{s}$
- 10x larger lifetime radiation dose: $300 \text{ fb}^{-1} \rightarrow 3000 \text{ fb}^{-1}$

\rightarrow A new silicon tracker will be installed in CMS in Long Shutdown 3 (2025-2027)

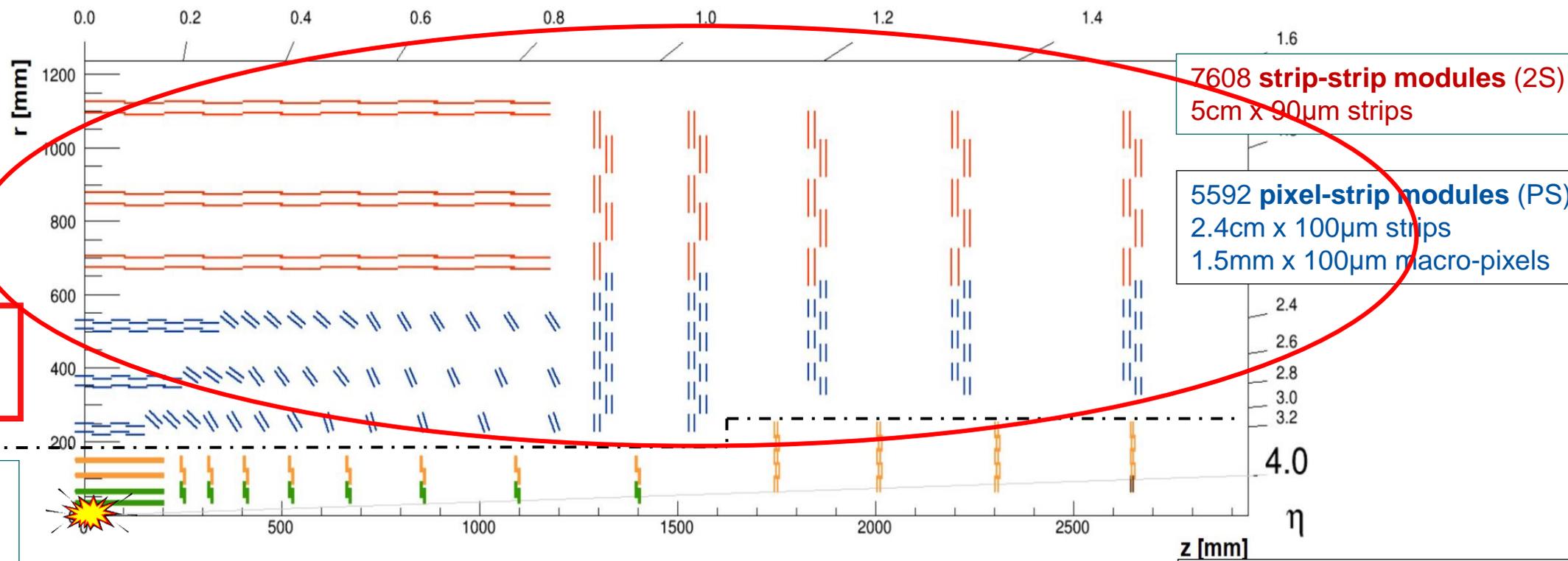


Layout of the CMS Phase-2 Tracker



- Acceptance up to $|\eta| = 4$ (IT)

Layout of the CMS Phase-2 Tracker



Outer Tracker (OT)

- 190 m²
- 213M channels

Inner Tracker (IT)

- 5 m²
- 1.9G channels

7608 strip-strip modules (2S)
5cm x 90µm strips

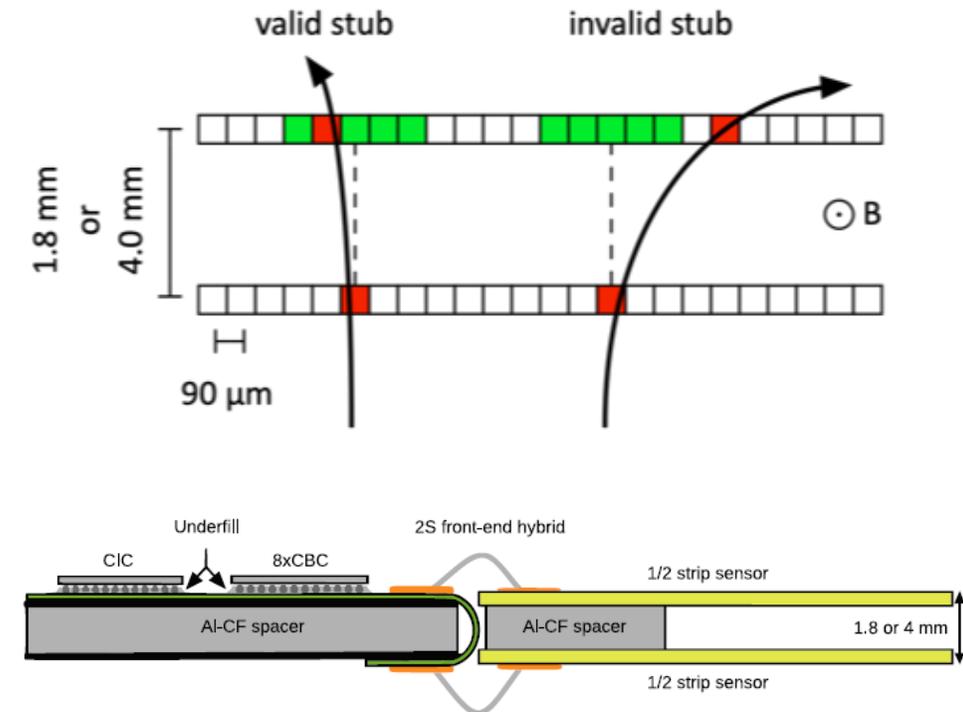
5592 pixel-strip modules (PS)
2.4cm x 100µm strips
1.5mm x 100µm macro-pixels

3892 pixel modules
100µm x 25µm pixels
1 x 2 and 2 x 2 chips

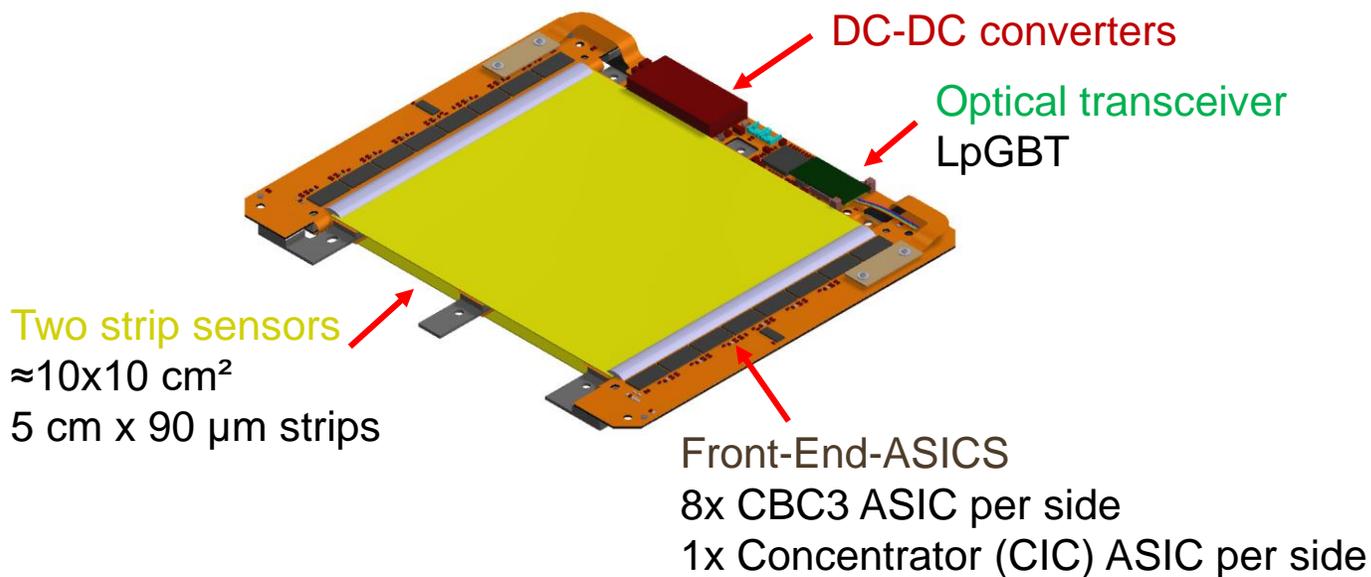
- Acceptance up to $|\eta| = 4$ (IT)
- OT: 6 cylindrical layers in the barrel and 5 discs in the endcap
- Tilted geometry in inner barrel
- Contribution to Level-1 trigger (OT)

Main Design Diver: Use of Tracking Information at Level 1 Trigger

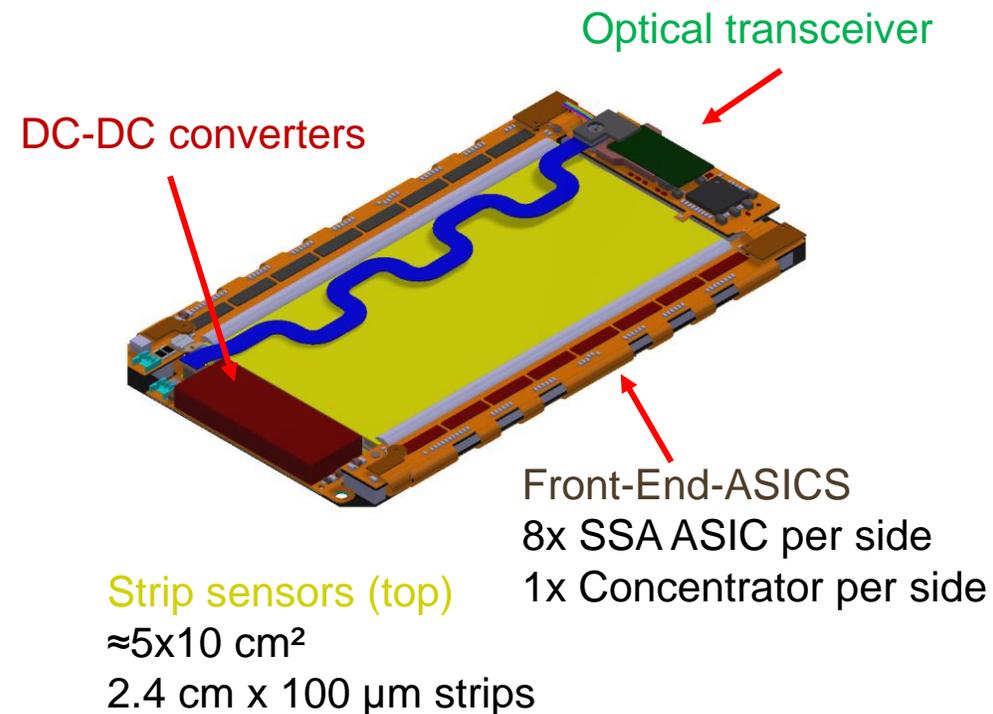
- Today: Level 1 trigger uses data from the muon system & calorimeter only
- The Phase-2 OT will contribute to the L1 trigger
 - Mitigate effects of pile-up
- Readout of whole tracker at 40 MHz not possible → data reduction at frontend needed:
- Modules made of **two closely spaced sensors**
- Frontend electronic compares hit patterns, which depend on bending in 3.8T magnetic field
 - Select tracks with $p_T > 2-3$ GeV (“stubs”)
- Stubs sent to the backend at 40 MHz → L1 trigger decision
- Whole detector read out in case of L1-Trigger accept (≈ 750 kHz)



2S Module

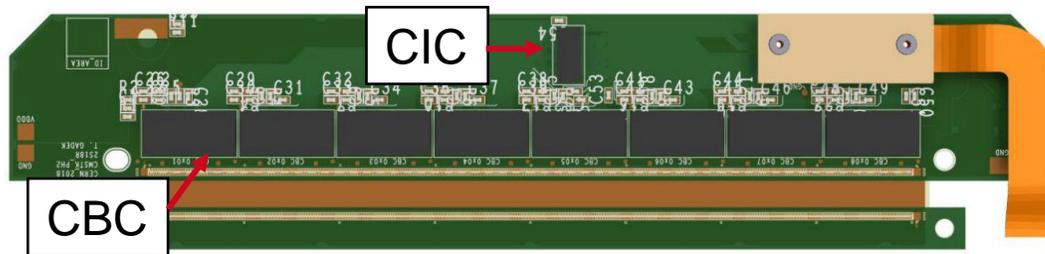


PS Module

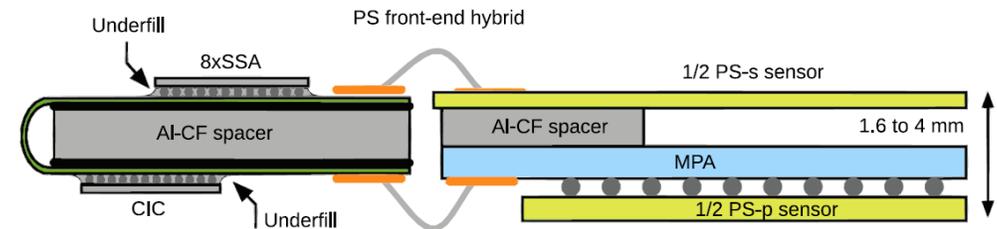


Macro-pixel sensor (bottom)
 1.5 mm x 100 μm macro-pixels
 Bump-bonded to MPA readout ASICs

2S Module



PS Module



CMS Binary Chip (CBC):

- Binary readout at 320 MHz
- 254 channels (127 top sensor + 127 bottom sensor)
- Correlation logic builds the stubs
- 8 Chips per half-sensor
- Final version in mass production

Concentrator Integrated Circuit (CIC):

- Receives data from all chips, performs sparsification
- Communication with the backend via LpGBT ASIC
- Submission of CIC2.1 imminent

Short Strip ASIC (SSA):

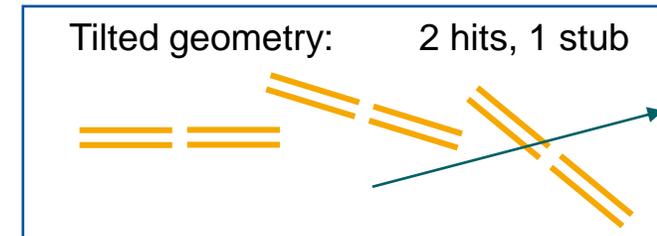
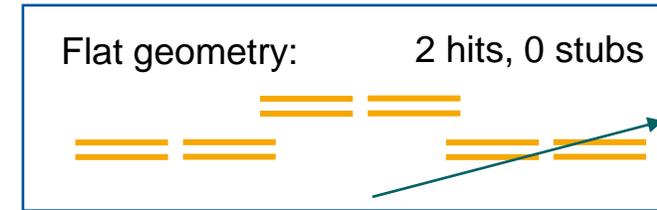
- 120 channels
- 8 SSA read out half top sensor
- Data sent to MPA via fold-over
- SSA2.1 ready for submission

Macro-Pixel ASIC (MPA)

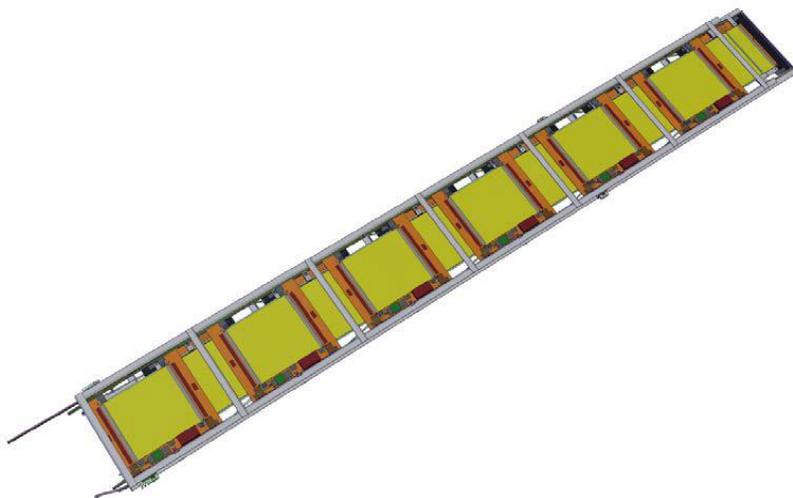
- 1888 channels
- 16 MPAs bump-bonded to bottom sensor
- Correlation logic builds the stubs

Modules are mounted on carbon fiber & foam support structures:

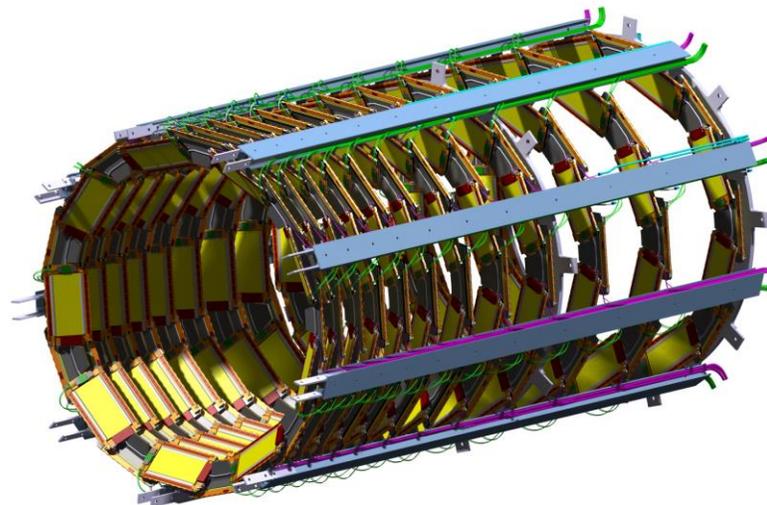
- Barrel: ladders and planks oriented along the beam axis
 - Innovative tilted barrel section: increases stub efficiency at high $|\eta|$, reduces material budget
- Endcap: modules are mounted on double-disks perpendicular to the beam axis
- Low-mass two-phase CO₂ cooling at -35 °C



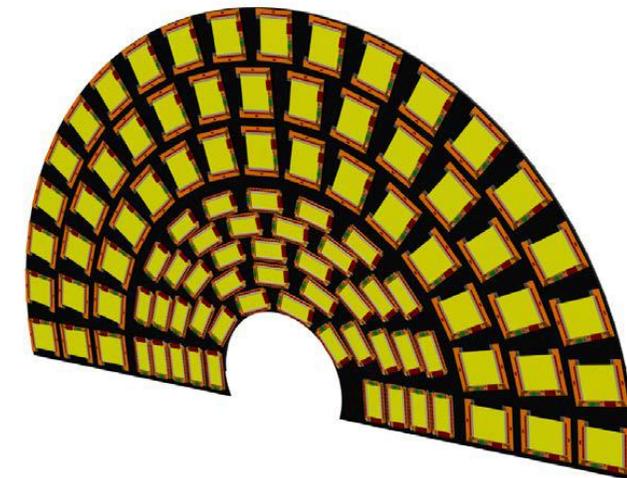
2S ladder

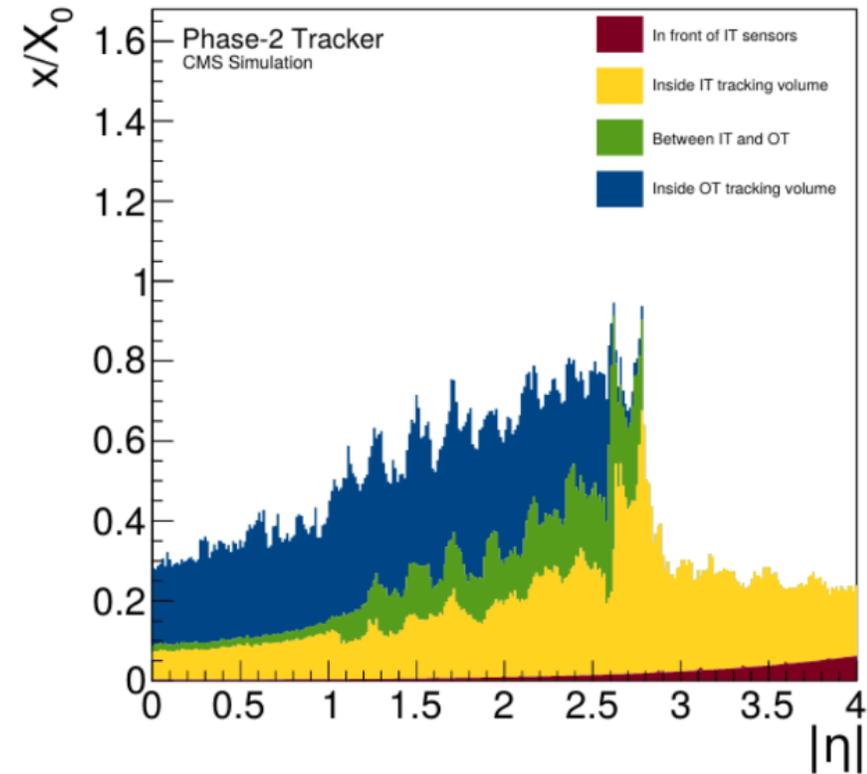
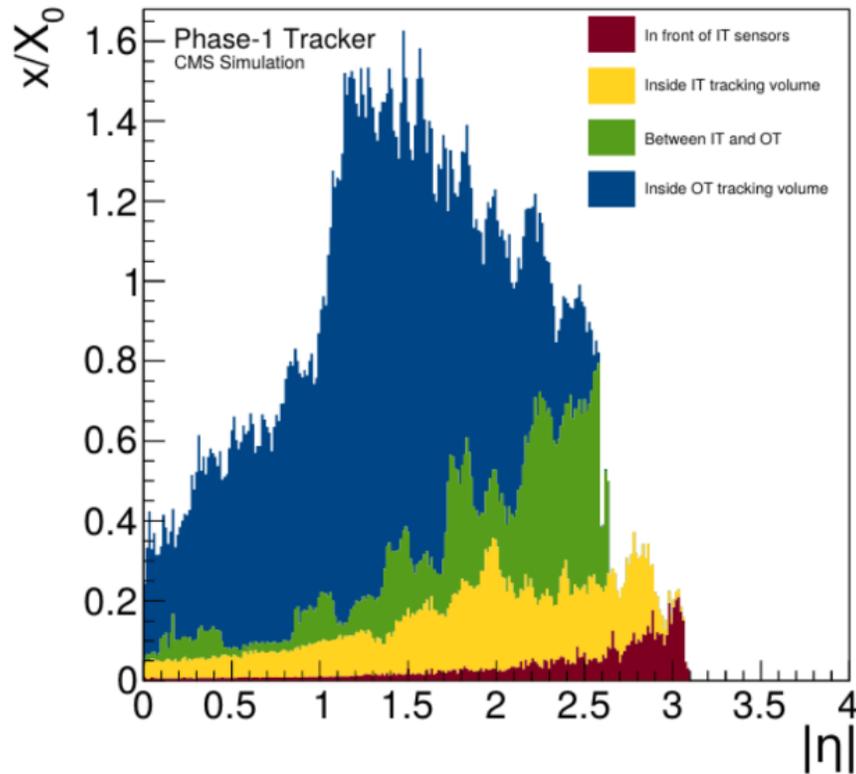


Tilted barrel section



Endcap “dee”





- Material budget of the Phase-2 OT significantly reduced compared to the current strip tracker
 - CO₂ cooling
 - DC-DC conversion based powering
 - Better routing of services
 - More lightweight support structure, tilted barrel region

- Upgraded tracker will need more power at lower supply voltages
 - Higher granularity, more complex electronics
 - Smaller feature size electronics: 250 nm → 130 nm, 65 nm

Phase-0 CMS Tracker

Direct powering

- 9.3 M channels
- Front-end power ≈ 33 kW
- ≈ 34 kW loss in cables



Phase-2 Outer Tracker

DC-DC conversion based powering

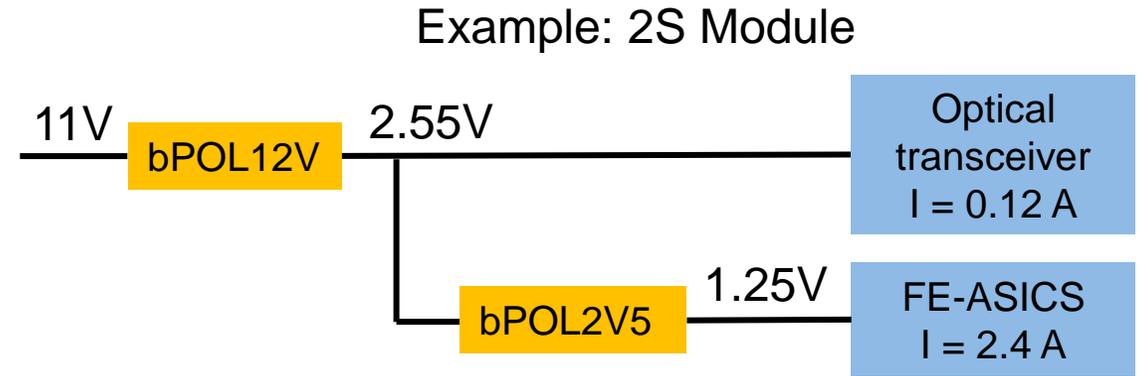
- 213 M channels
- Front-end power ≈ 85 kW

- Currents will increase substantially (CMS outer tracker: factor 4-5)
- Cable losses would rise quadratically: $P_{\text{loss}} = R \cdot I^2$
- New powering scheme using DC-DC converters

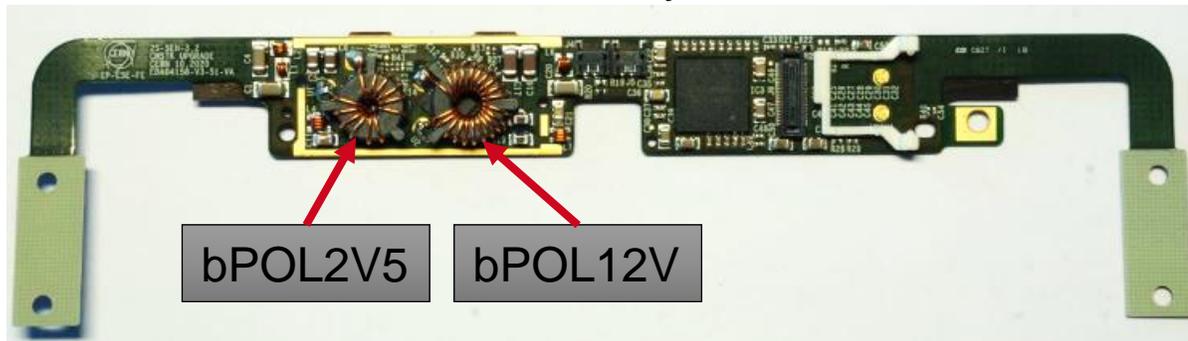
- Two-stage DC-DC conversion powering
 - Currents reduced by \approx conversion ratio r
 - Cable losses reduced by r^2
- DC-DC converters located on the modules
 - First time that converters will be used at point of load
 - Switching noise is a concern!

- Final prototypes of service hybrids (2S), and power hybrids (PS) with early versions of bPOL12V & bPOL2V5 available:

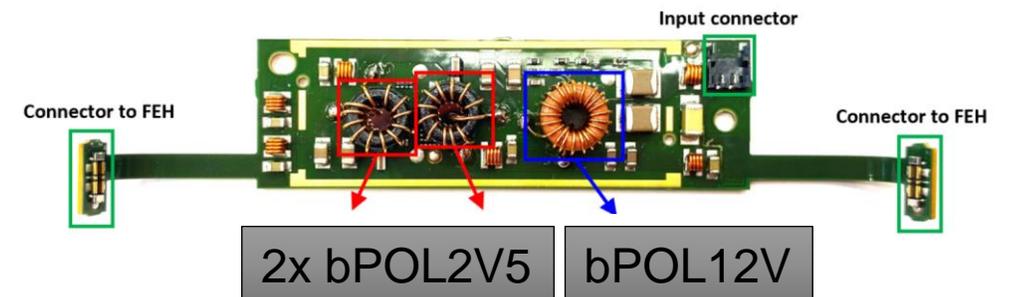
→ No significant increase of noise due to DC-DC converters observed



2S Service Hybrid

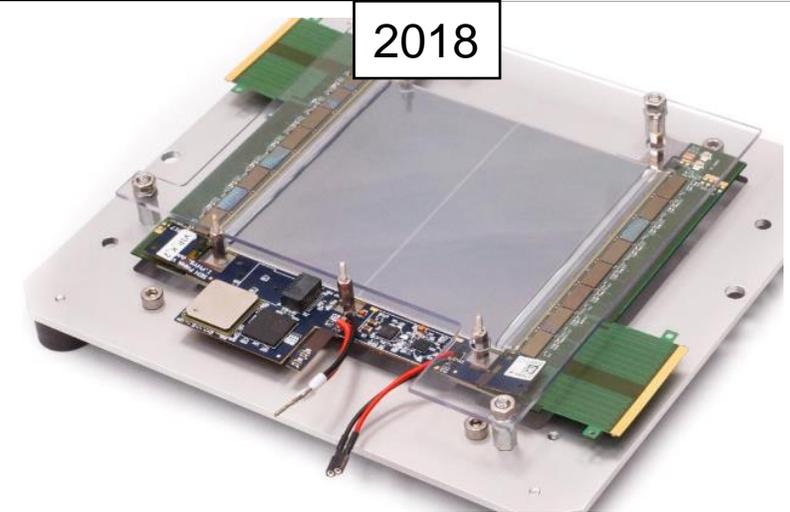


PS Power Hybrid

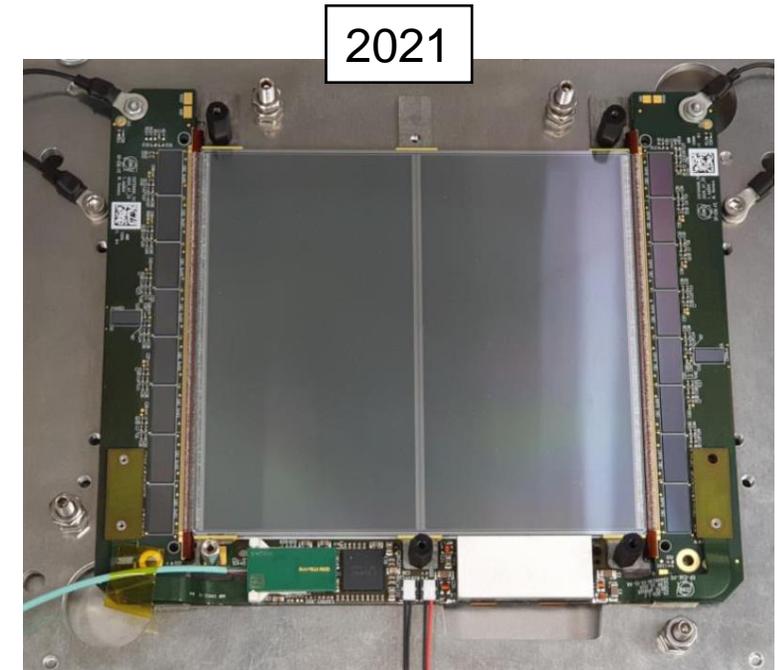
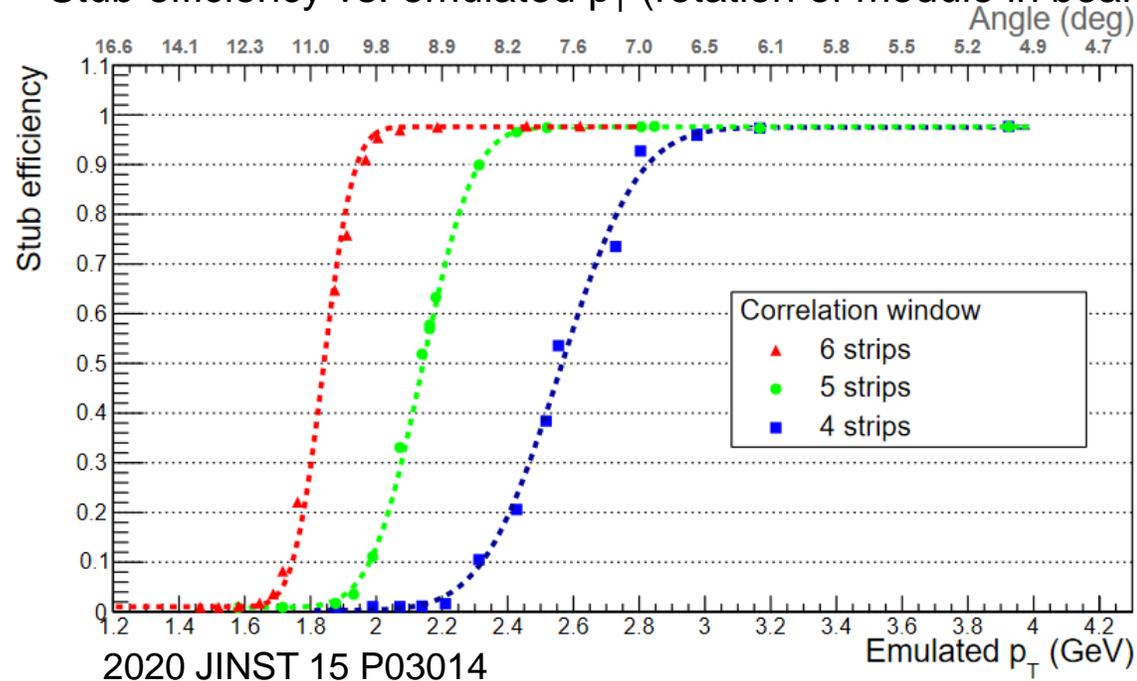


Prototyping

- More than 30 functional prototype modules have been built
- Final readout chain available
- First modules with **final prototype components built this year**
 - Several beam tests, performance as expected
 - Magnet test just finished

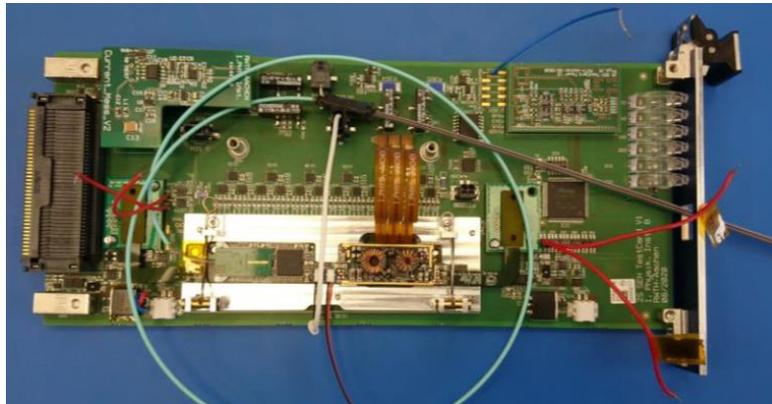


Stub efficiency vs. emulated p_T (rotation of module in beam)

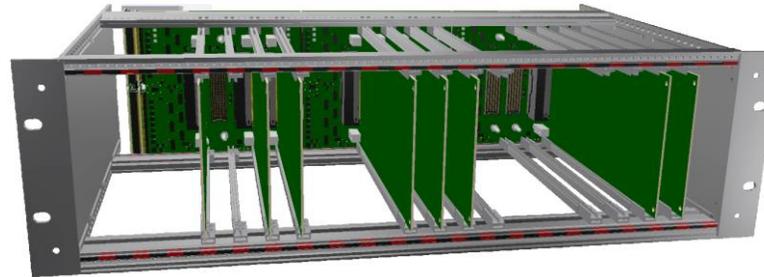


- 2 years of series production foreseen
 - 1000 - 2000 modules per site
- Several test systems have been developed, ensuring high module quality
- Final commissioning of the test systems is ongoing, mass production will start this year

2S-SEH Testcard



Crate System for Hybrid Testing



Module Thermal Cycling Setup



- Phase-2 upgrade of the CMS silicon tracker is an important project to ensure a good performance of the CMS detector after the HL-LHC upgrade
- Delivering tracking information for the L1 trigger requires an innovative detector design
- The Phase-2 tracker will have a better performance than the current tracker under much harsher conditions
- Project is progressing well and so far on track for installation in LS3 (2025 – 2027)