## New SoC-based MicroTCA Hardware Developments

Thursday, 30 September 2021 11:40 (5 minutes)

For future installations, new and powerful hardware components are required. In this talk, an 8-channel low-latency RF-digitizer, a distributed motion controller card and a RTM Class D1.3 digital processing board targeting serial JESD204 ADCs will be presented. All these boards are designed on the basis of ARM-based MPSoCs, which allow convenient board bring-up, configuration and maintenance.

Summary:

A low-latency RF-digitizer, a distributed motion controller card and a RTM Class D1.3 AMC board will be presented.

## Summary

A low-latency RF-digitizer, a distributed motion controller card and a RTM Class D1.3 AMC board will be presented.

Primary authors: Mr FENNER, Michael; ZINK, Johannes (DESY); Mr MARJANOVIC, Jan

**Presenter:** Mr FENNER, Michael

Session Classification: Session Beam Controls