

PCIe Gen4/5 and 100G-Ethernet on mTCA platform

Tuesday 7 December 2021 17:30 (15 minutes)

During last months PICMG mTCA-NG working group carried out channel signal-integrity simulations to find out whether 100GBASE-KR4 Ethernet and PCIe Gen4 and Gen5 are realizable on the mTCA platform. This presentation is intended to give an overview of the work carried out. The structure of the simulated channels and the models will be presented here. In addition, it will be shown how the evaluation of the AMC-/MCH-cards and backplanes will be carried out in the future with regard to signal integrity.

Summary

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