Real-Time Image Acquisition and Processing for Plasma Diagnostics

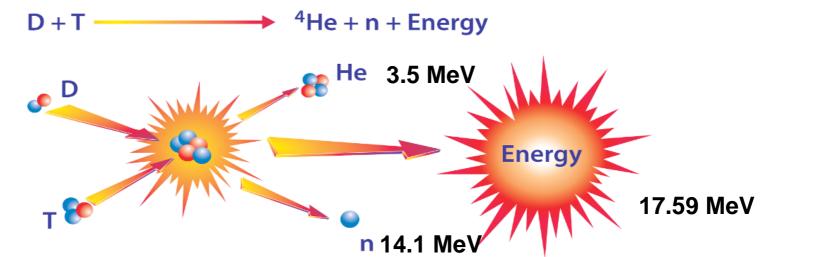
D. Makowski, B. Jabłoński, P. Perek, A. Mielczarek A. Winter and IPP Team





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Fusion - Plasma Conditions



Sustained fusion reactions require **enough particles (density)** that are **energetic enough (temperature)** and collide **often enough (confinement time)**.

The fusion **triple product** is the figure of merit:

 $nT\tau_E \gtrsim 5 \times 10^{21} \text{ keV s m}^{-3}$ (Lawson criterion)

T = 100 - 200 milion °K (10 - 20 keV, peak at 70 keV) $n = 2-3 * 10^{20}$ ions/m³ $T_F = 1-2$ s

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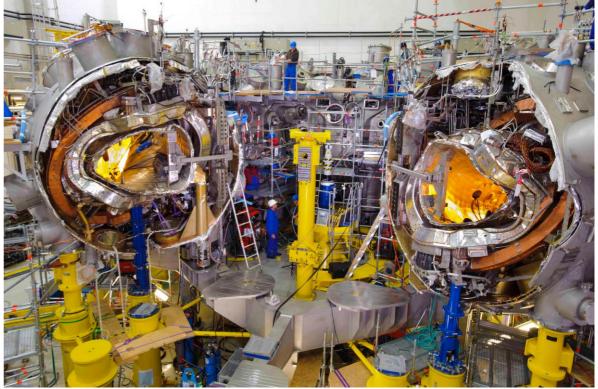
Wendelstein 7-X Stellarator

Mission of Wendelstein 7-X:

- Demonstrate feasible modular coil design
- Good equilibrium properties with small error fields
- Steady-state compatible power and particle exhaust concept
- Optimized plasma performance

Results:

- First plasma OP1.1 in Dec'15 (helium, 1 MK for 0.1 s)
- More in OP1.2 in 2017-2019 (max. 100 MK, max. 100 s)
- OP 2.X is planned for 2022 (<u>cooled divertor</u>, up to 18 GJ, 30 minutes at 10 MW ECRH)



Wendelstein 7-X Stellarator Greifswald, Germany

Axel Winter talk Thu 9/12, 14:20





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Infrared Image Mapped to CAD Models

Divertor tiles: Carbon Fibre Composite (CFC) joined to CuCrZr cooling structure

Max. Operational temperature is limited by a Cu to 475 °C

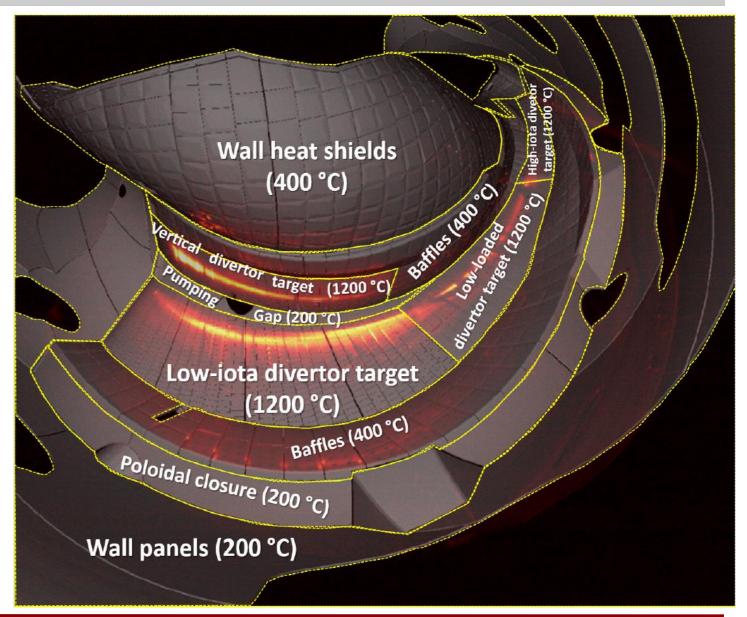
Max. surface temperature is 1200 ° C for 10 MW/m2

PFCs (graphite tiles) up to 400 °C

Wall and pumping gap panels up to 200 °C

A. Puig, IAEA 2021

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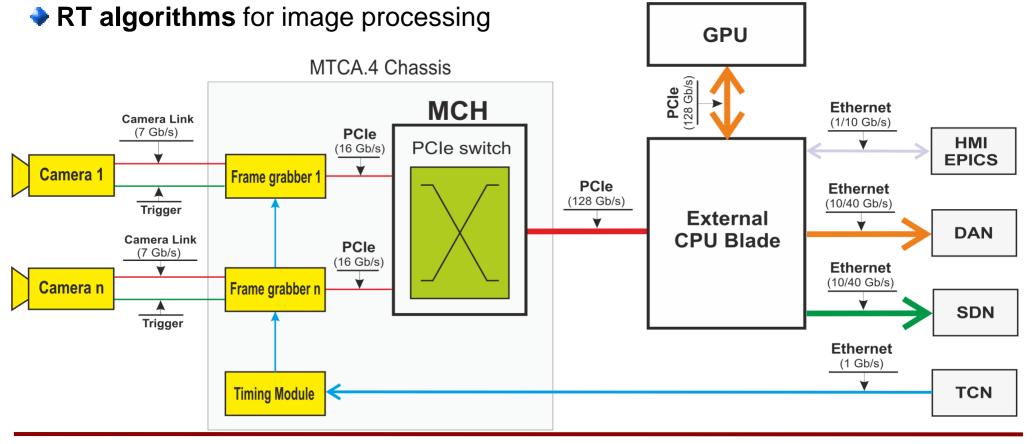


 $4/2^{-1}$

Image Acquisition and Processing

Tasks:

- Propose a scalable and reliable hardware architecture, long support time
- Develop new hardware components frame grabber and firmware
- Develop software framework for IA and IP



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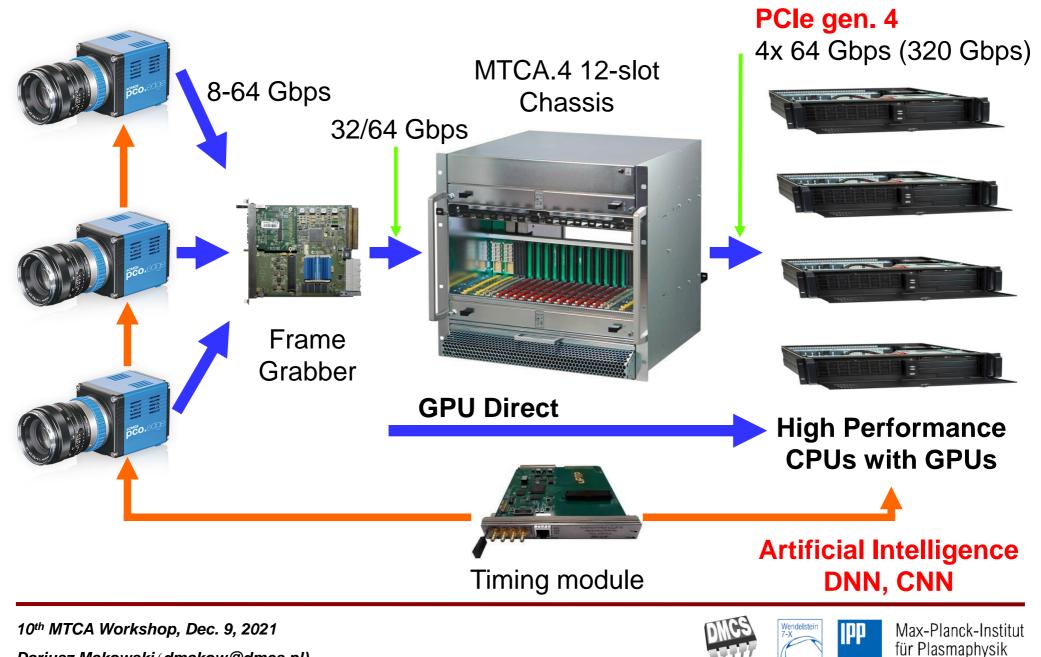
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Image Acquisition and Processing with MicroTCA.4



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Various Cameras and Interface Standards

Camera Link

- Camera Link-HS
- CoaXPress 2.1
- 1 GigE Vision
- 10-25 GigE Vision
- IEEE1394/Fire Wire
- HD-SDI

SCD Hercules (CL)



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2.04 Gb/s, 5.44 Gb/s, 6.8 Gb/s

Active Silicon (CXP-12)

Link HS

Coa Press





HDSDí

Imperx Cheetah (10GigE Vision)

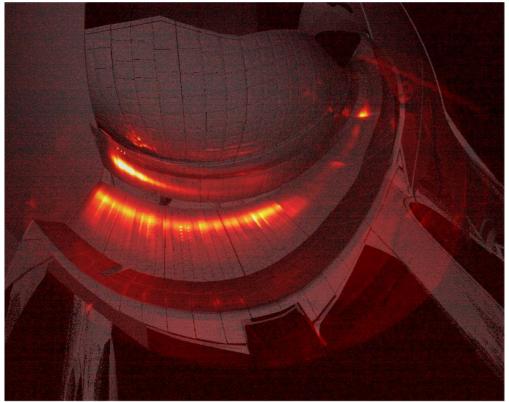




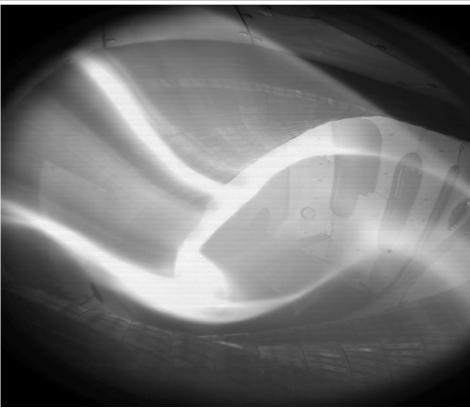




Imaging System for Plasma Facing Components Protection



- Imaging system for PFC protection:
 - IR cameras with frontal view of divertors
 - Video cameras with tangential view
- OP 1.2
 - 9 immersion tubes
 - 1 prototype endoscope



- OP 2.1
 - 2 endoscopes
 - 8 immersion tubes
 - 2 simple systems for high iota target
- OP 2.2
 - 10 endoscopes
 - 2 simple systems for high iota target





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W7-X: OP2.1





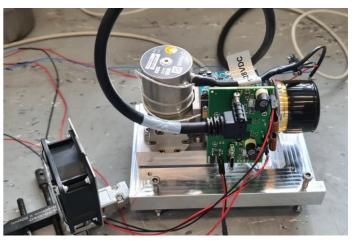
IRCAM Caleo 768k L



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W7-X: OP2.X





SCD Hercules

- IR Camera:
 - Hercules sensor InSb
 - Spectral range: 2-5.7 µm (MWIR)
 - Sensor size: 1280x1024
 - Pixel size: 15 x 15 µm
 - Bit depth: 14 bit
 - Frame rate: 100 Hz







W7-X: OP2.1





Raptor Cygnet 2.1MP





PCO Edge 5.5



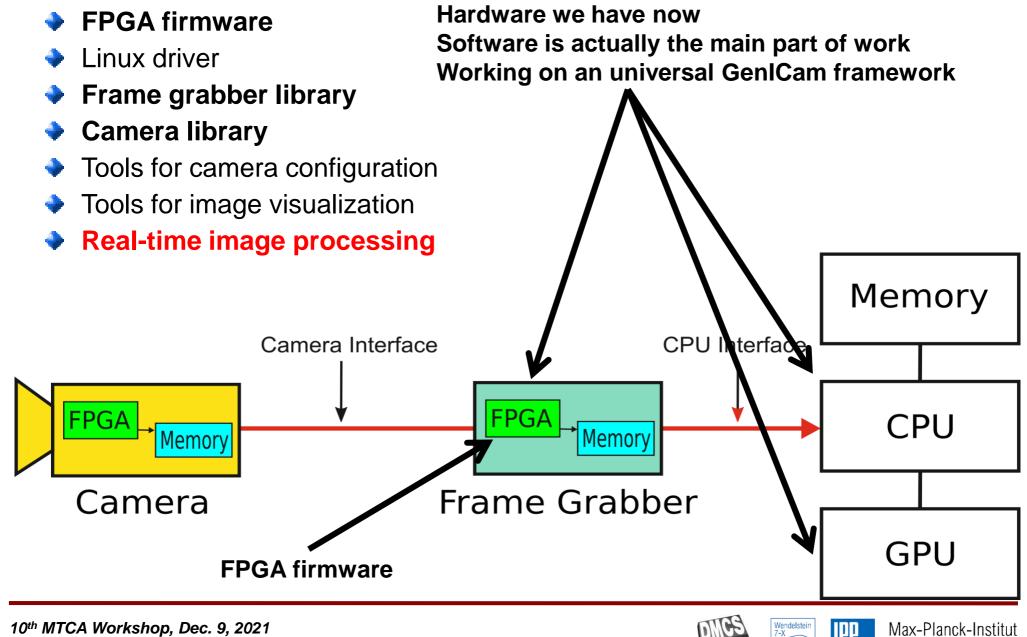
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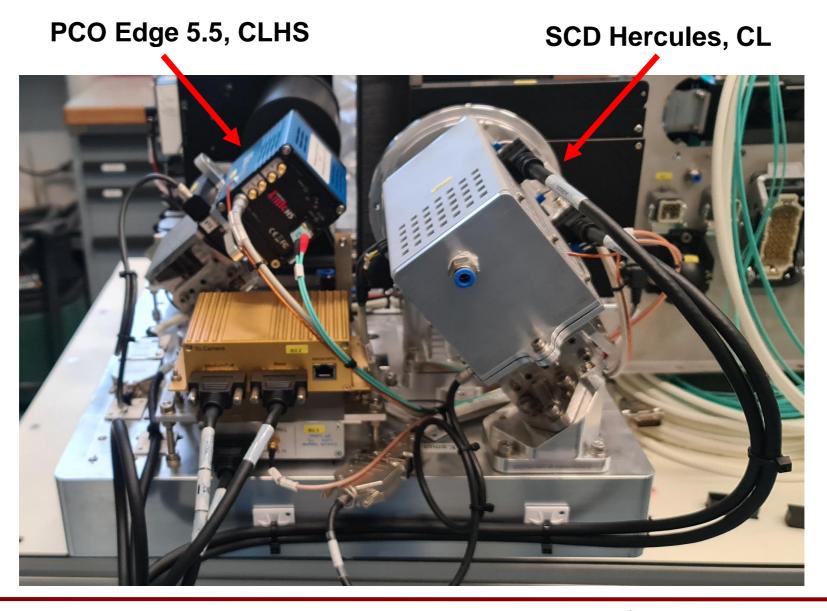
Universal Frame Grabber Module - Software



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für Plasmaphysik

Cameras Assembled in Endoscope



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Image Processing

High-speed digitisers or high-resolution/frame-rate cameras requires significant processing power

- Programmable devices (FPGA) are used for signal conditioning and low latency real-time processing (NUC, bad-pixel correction, background subtraction, etc.)
- CPU or Graphics Processing Unit (GPU) are suitable for more complex algorithm, especially image processing

Convolutional Neural Network for image analysis and recognition

- Data copying is always expensive (both processing power and memory)
 - Avoid data copying
 - Use Direct-Memory-Access when possible
 - Ideal situation is direct transfer to data processing unit



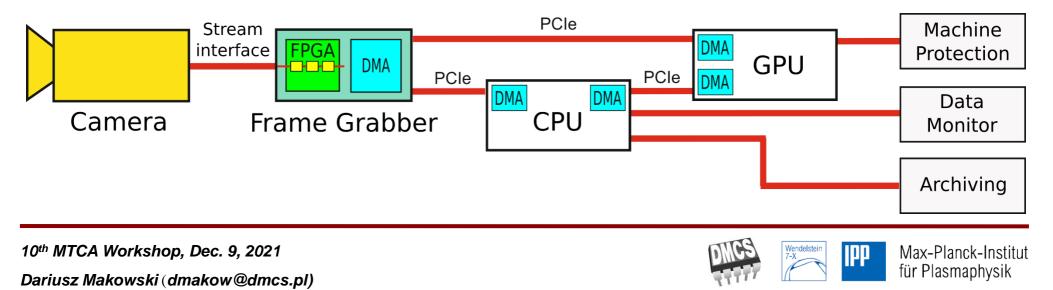




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Image Acquisition Architecture with DMA – First Step

- A single station always process data from a single camera
- During algorithms development we need at least two parallel acquisition path
 - Data archiving and visualisation
 - Machine protection (realisable deterministic algorithms)
 - New algorithms testing (deterministic and AI-based)
- Multiple DMA transfers:
 - Frame grabber to CPU and GPU (Tesla T4)
 - CPU to GPU, GPU to CPU,...

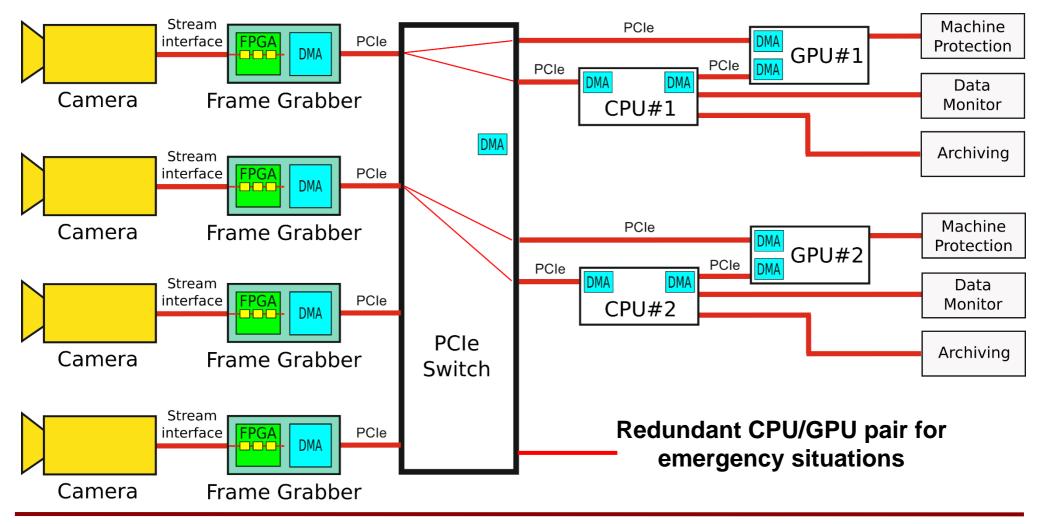


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Image Acquisition Architecture with DMA – Second Step

Single MTCA.4 system process images from 4 IR cameras

Four fast stations (CPU and GPU) involved in image processing

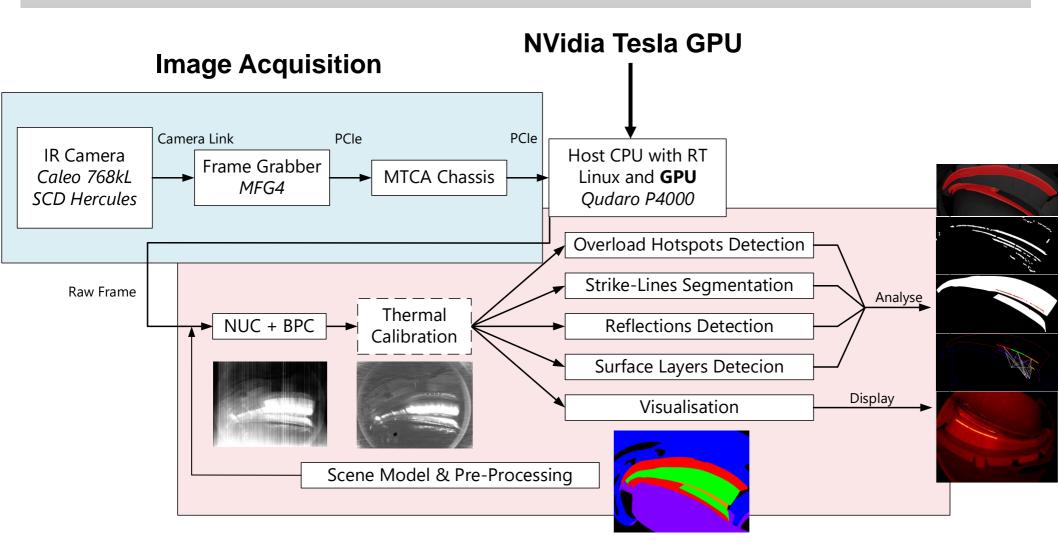


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Image Acquisition and Processing Pipeline



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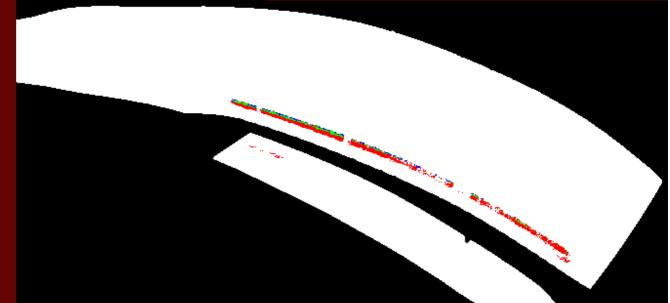
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Analysis of 20171114.053 - AEF20 dataset



Segmented Strike-Line & Hotspots

Surface Layers During... Heating During... Cooling During... Both



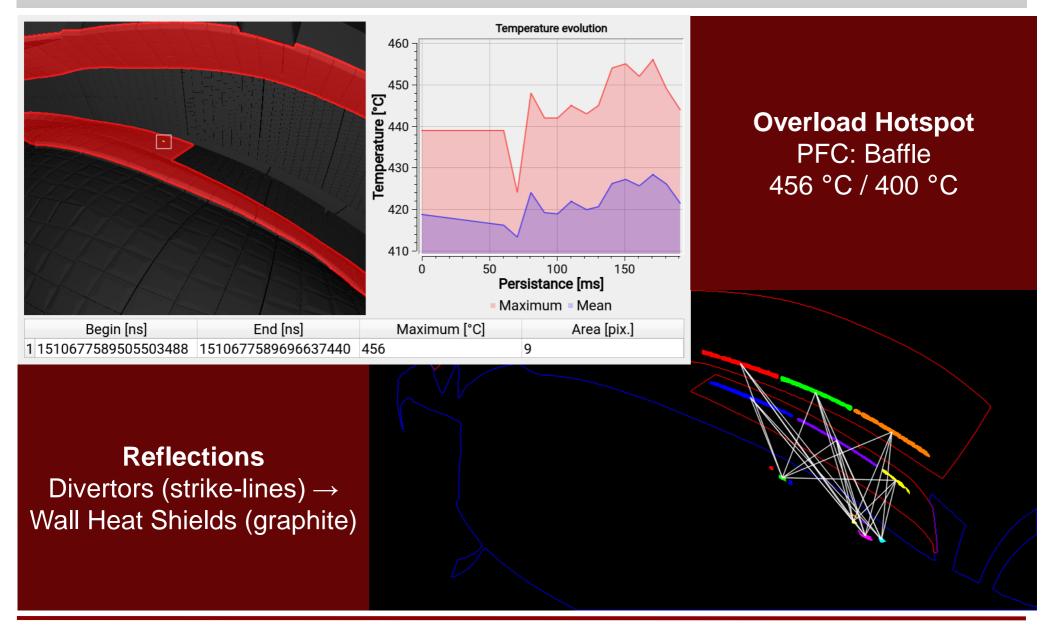
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Analysis of W7-X 20171114.053 - AEF20 dataset



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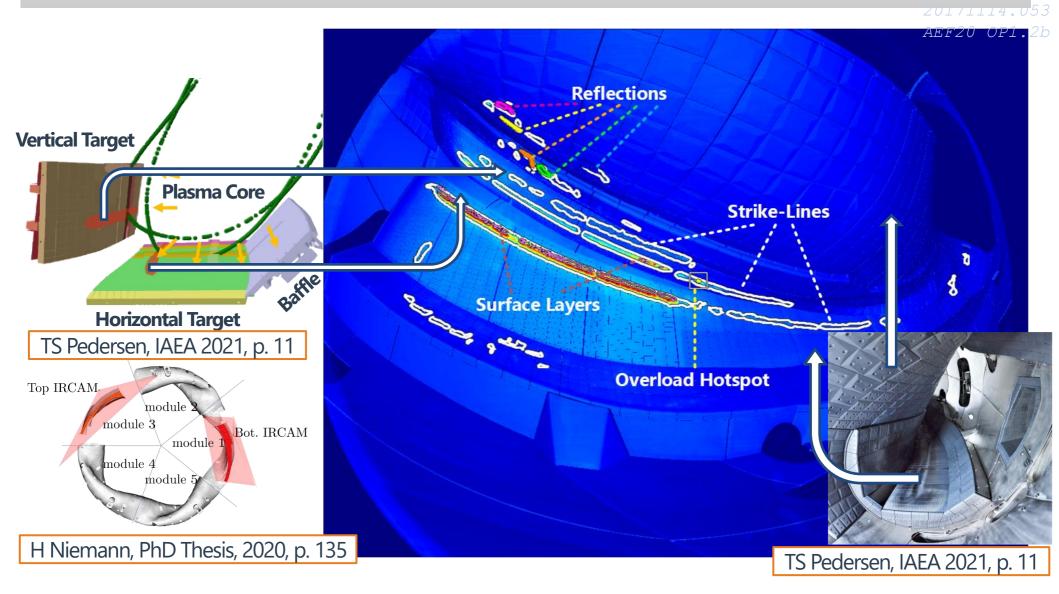


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Real-Time Image Acquisition and Processing for Plasma Diagnostics

19/21

W7-X Stellarator – Machine Protection



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Future Work – Real-time Image Processing

1. GPU-Accelerated Real-Time Algorithms for Machine Safety

- Pre-Processing
- Real-Time Filtering
- Real-Time Analysis



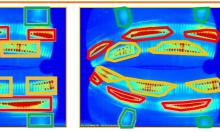




20/21

2. Artificial Intelligence for Machine Protection

- Detection
- Instance Segmentation
- Classification





3. Artificial Intelligence for Machine Control 1000 \leq Regression 500 Reinforcement -500 Learning? -1000 output 20 58 62 56 60 64 66 time [s]

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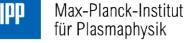


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Thank you for your attention

DMC





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