Event Counter Overflow

- Only 24 bits dedicated in the event header (maximum number of triggers: 16777216)
  - Current mode: count all triggers (not only accepted)
- Counting overflows of that memory location
- Cognition of all overflows guaranteed, even for large readout windows
  - Maximum dead time: 4 ms
  - Maximum trigger rate (BPTX): $4 \cdot 10^7\ s^{-1}$
  - ⇒Lost triggers: max. $16 \cdot 10^4 < 16 \cdot 10^6$ [OK]

ADC Data

- Trigger rate and data rate determined
- Itamar included the offline analysis for spectra in the readout process (many thanks!)
  - Needed at testbeam in Hamburg (beta test :-D)
  - However, data rates at LHC will be higher (computing time needs to be checked)

Team Play of BCM1F Boards

- VME interface integrated in ADC process
  - Groundwork to address other boards
  - Example: Select ADC trigger input at LUT board
- Access to the discriminator
  - Read trigger threshold and write to log file
  - Change of the threshold by ADC process possible