



# High spatial resolution pad and pixelated TPC technology R&D for e<sup>+</sup>e<sup>-</sup> collider

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on behalf of LCTPC international collaboration

1<sup>st</sup> ECFA Workshop on e+e- Higgs/EW/Top Factories October 5-7, 2022, Hamburg

- **TPC detector for e+e- colliders**
- High spatial resolution pad readout TPC
- Pixelated readout TPC R&D
- Summary

#### TPC R&D in LCTPC Collaboration

- MPGDs for TPC readout is a **baseline solution and further R&D** features many benefits:
  - Small pitch of gas amplification regions => strong reduction of  $E \times B$ -effects
  - No preference in direction => all 2 dim. readout geometries possible
  - **Ion backflow** can be reduced significantly (Gating, Hybrid structure...)
  - Continue electronics, cooling, UV laser track and low power consumption FEE development



LCTPC-collaboration studies MPGD detectors for the ILD-TPC: 24 Institutes from 11 countries + 24 institutes with observer status

Various **gas amplification stages** are studied: GEMs, Micromegas, GEMs with double thickness and GridPixes.

#### TPC technology for the future e+e- colliders

- A TPC is the main tracking detector for **some candidate experiments at future e+e- colliders** 
  - ILD at ILC and the baseline detector concept of CEPC
- TPC technology can be of interest for other future e+e- colliders



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#### TPC requiremetns from e+e- Higgs/EW/Top factories

- TPC can provide hundreds of hits (for track finding) with high spatial resolution compatible with PFA design (very low material in chamber)
  - $\sigma_{1/pt} \sim 10^{-4}$  (GeV/c)<sup>-1</sup> with TPC alone and  $\sigma_{point} < 100 \mu m$  in r $\phi$
- **Provide dE/dx and dN/dx with a resolution** <4%
  - Essential for Flavor physics @ Z run
  - Beneficial for jet at higher energy





#### Pad and pixelated readout TPC technology

- TPC as the main tracker detector to satisfy the physics requirements :
  - For Higgs, W and top running, **no problem** for all TPC readout technologies.
- For high luminosity  $(2 \times 10^{36})$  Z running
  - Pixelated readout TPC is a good option at **high luminosity** on the circular e+e- collider
  - Pixelated readout TPC is a realistic option to provide
    - High spatial resolution under 2T or 3T magnetic field
    - Better momentum resolution
    - High-rate operation (MHz/cm<sup>2</sup>)
    - dE/dx and Cluster counting (in space)
    - Excellent two tracks separation

#### **Standard charge collection:**

Pads (1 mm $\times$ 6 mm)/ long strips

#### **Pixelated readout:**

Bump bond pads are used as charge collection pads.  $55 \mu m \times$  55  $\mu m$  or larger



• High spatial resolution pad TPC technology

#### Pad TPC technology - double GEMs

- GEMs: copper-insulator- copper sandwich with holes
- Double GEMs module are being tested:
  - GEMs with 100µm LCP insulator
  - Standard GEM from CERN
- Design idea of the GEM Module:
  - **No frame** at modules both sides

#### GEM Module 1:

- 2 GEMs made of 100 µm thick LCP
- 1.2×5.4mm<sup>2</sup> pads

• Spatial resolution of  $\sigma_{r\phi} \le 100 \ \mu m$ , more stability by the broader arcs at top and bottom



#### Pad TPC technology - triple GEMs

- Design idea of GEM Module 2:
  - Minimize dead area
  - Without frame to stretch GEMs, but a 1 mm grid to hold GEM
- Spatial resolution of σ<sub>rφ</sub>≤100 µm, and double track resolution and dE/dx calculated in dependence on the pad sizes

#### GEM Module 2:

- 1.26 × 5.85mm2 pads staggered
- Field shaping wire on side of module to compensate the field distortions





#### Pad TPC technology - Resistive Micromegas

- Resistive Micromegas:
  - Bulk-Micromegas with 128 µm gap size between mesh and resistive layer (developed in LCTPC)
- A new HV scheme of the module (ERAM) places grid on ground potential
  - Reduces **field distortions** between modules by a factor of 10







#### Pad TPC technology – Detector Module

- **GEM and Micromegas** groups have finished analysis of test beam data with previous set of detector modules. Both technologies show **very similar performance**.
- LCTPC want to implement improvements in a **new generation of modules** => **common modules** 
  - Common readout electronics (sALTRO)
  - Only the gas amplification stage differs
- Combined Micromegas + GEM readout has been developed, which promises a **lower ion backflow** (IBF) at CEPC TPC group without gating.
  - IBF×Gain ~ 1 at total gain of 2000



#### TPC prototype with 266nm UV laser tracks

- The TPC prototype integrated 266nm UV laser tracks has successfully developed.
- Analysis of UV laser signal, the spatial resolution, dE/dx resolution
  - Spatial resolution can be less than 100 µm along the drift length of TPC prototype
  - Pseudo-tracks with 220 layers (same as the actual size of CEPC baseline detector concept) and dE/dx is about 3.4  $\pm$  0.3%



ي ⊗ 30

15

10

data

 $\sigma_{\rm dE/dx} = 3.36 \pm 0.26 \,\%$ 

 $- \sigma_0 \cdot (N_{hits})^{-k}$ 

https://doi.org/10.1016/j.nima.2022.167241 Huirong Oi • Pixelated TPC technology

#### Pixelated TPC technology - Timepix3-based GridPix

- GridPix detector have moved from Timepix to Timepix3 ASICs. Tests with single and quad devices have been successfully done.
- A module **with 32 GridPixes has been constructed** and was in a test beam in B=1.0T at DESY in June 2021.
- Very high detection efficiency results in **excellent tracking and dE/dx performance.** Timepix4 development is ongoing.
- Ion back flow of the module has been measured and can be further reduced by applying a double grid and the resistivity of the protection layer.







https://arxiv.org/abs/1902.01987 Huirong Oi

#### Pixelated TPC technology – Large scale readout

- TPC prototype with GridPixes:
  - ~100-120 chips/module 240 module/endcap (10 m<sup>2</sup>) → 50000-60000 GridPixes Demonstration of mass production: One LP-module covered completely with GridPixes (96 → coverage 50%) and two partially covered modules.
  - In total 160 GridPixes covered **an active area of 320 cm**<sup>2</sup> (10M pixel detector).
- During the test beam ~10<sup>6</sup> events were successfully collected, all results showed that a pixel TPC is realistic.







DESY testbeam in June 2021

arrival time (ns

#### dN/dx cluster counting

- Challenging for the **low power consumption** electronics (>40mV/fC needed at 2000 of gas gain)
- Pixelated readout
  - $\rightarrow$  high granularity readout in endplate
  - $\rightarrow$  the reasonable pixilation reveals the underlying cluster structure in 3D chamber
- Occupancy of the pixelated TPC
  - Occupancy is very key issue at the high rate or high luminosity
  - Smaller pad/pixel size
    → smaller occupancy
  - To be addressed by R&D

     → A detailed simulation would
     be necessary to determine the
     scaling factor
    - $\rightarrow$  Simulation ongoing at IHEP



# High granularity for improved PID in TPC

- For **traditional dE/dx detection**, the charge summation is performed using the center-of-gravity method.
- In most experimental study from small to large TPC
  - L: track length
  - N: number of readout rows
  - Constant L and changing granularity G = N/L

$$\frac{\sigma_{dE/dx}}{\langle \mu_{dE/dx} \rangle} \propto L^{-0.45} G^{-0.13}$$

- If pad size is at the level of cluster distances of primary ionization
  - i.e. ~ **300-500 µm in Ar-based**
  - Cluster counting becomes effective
- PID improvement
  - The potential of **better resolution by at least a factor 2**
  - Novel method studied by several R&D groups for the TPC for the e+e- collider



Hauschildt http://ific.uv.es/~ilc/ECFA-GDE2006/0

## Simulation of ILD TPC with the pixel readout

- To study the performance of a large pixelated TPC, the pixel readout was implemented in the full ILD DD4HEP (Geant4) simulation
- Changed the existing TPC pad readout to a pixel readout
- Momentum resolution preliminarily indicated 15% better with realistic (59%) coverage of the endplate by the pixel readout.



https://www.nikhef.nl/pub/services/biblio/theses\_pdf/thesis\_C\_Ligtenberg.pdf Huirong Qi



Simulation of 50 GeV muon track with pixel readout

#### High granularity for improved PID in TPC

- Current full ILD reconstruction: 6mm pads → ~4.8% dE/dx resolution
- 6mm  $\rightarrow$  1mm: 15% improved resolution via the charge summation (dE/dx)
- 6mm  $\rightarrow$  0.1mm: 30% improved resolution via the cluster counting (dN/dx)
  - Pad size of about 300µm can record ~1 primary cluster along track length at T2K gas
  - High **readout granularity** VS the primary cluster size optimization



All studies ongoing in

LCTPC

- In LCTPC international collaboration, TPC tracking using GEM, Micromegas and GridPix pad and pixelated readouts have been developed for the future e+e- colliders.
- Analysis of test beam data with similar set of GEM, Micromegas and Gridpix modules demonstrated the proof-of-concept and validated these technologies.
- Many simulations are still necessary to understand the detailed requirements of the pixelated detector (e.g. number of ADC bits, pixel readout sizes, occupancy, ion backflow, etc.), but also new ideas are welcome.
- Synergies with CEPC/FCCee/EIC/T2K/ALICE allow us to continue R&D and ongoing, we learn from their experiences and R&D beyond the scope of ILC.

# Many thanks!