

The High-Luminosity LHC Phase

In the High-Luminosity phase, the Large Hadron Collider (LHC) will undergo enhancements and upgrades to broaden the possibilities for **Beyond the Standard Model (BSM)** and **precision physics**:

- ▶ $L = 7.5 \cdot 10^{-34} \text{cm}^{-2} \text{s}^{-1}$, 4 times more than the LHC;
- ▶ $L_{\text{integrated}} = 3000 \text{fb}^{-1}$, 10 times more than the LHC;
- ▶ Up to 200 collisions per bunch crossing.

The High-Granularity Calorimeter (HGCal)

The CMS detector must be upgraded:

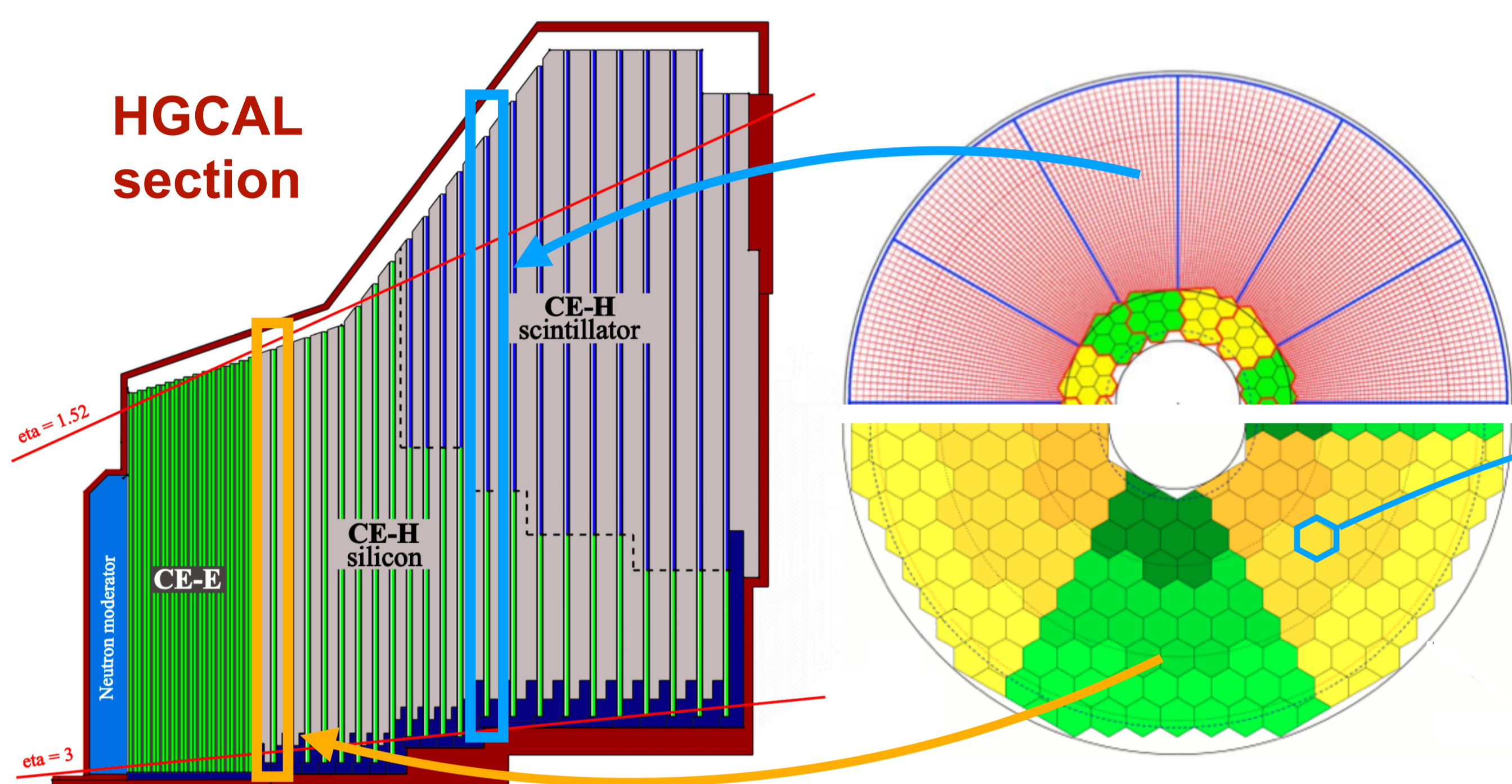
- ▶ **Increasing the granularity** for particle flow (PF) reconstruction;
- ▶ Mitigating the effects of pile-up (PU) events;
- ▶ Endcap calorimeters will be exposed to unprecedented levels of radiation.

The High Granularity Calorimeter (HGCal) will meet these requirements:

- ▶ 5D measurements detector: **position, energy and timing**;
- ▶ Replacing the current ECAL endcap, HCAL endcap and ES.

The HGCal trigger primitives generation (TPG)

Front-end electronics (FE)



Hadronic section:
Si + Scintillator tiles
21 layers, $8.5 X_0$
Absorber: Cu, steel

EM section:
Si sensors
26 layers, $28 X_0$
Absorber: CuW, Pb

HGCROC

- ▶ Groups cells into TCs
- ▶ Compresses to 7-bit floating point

ECON-T:

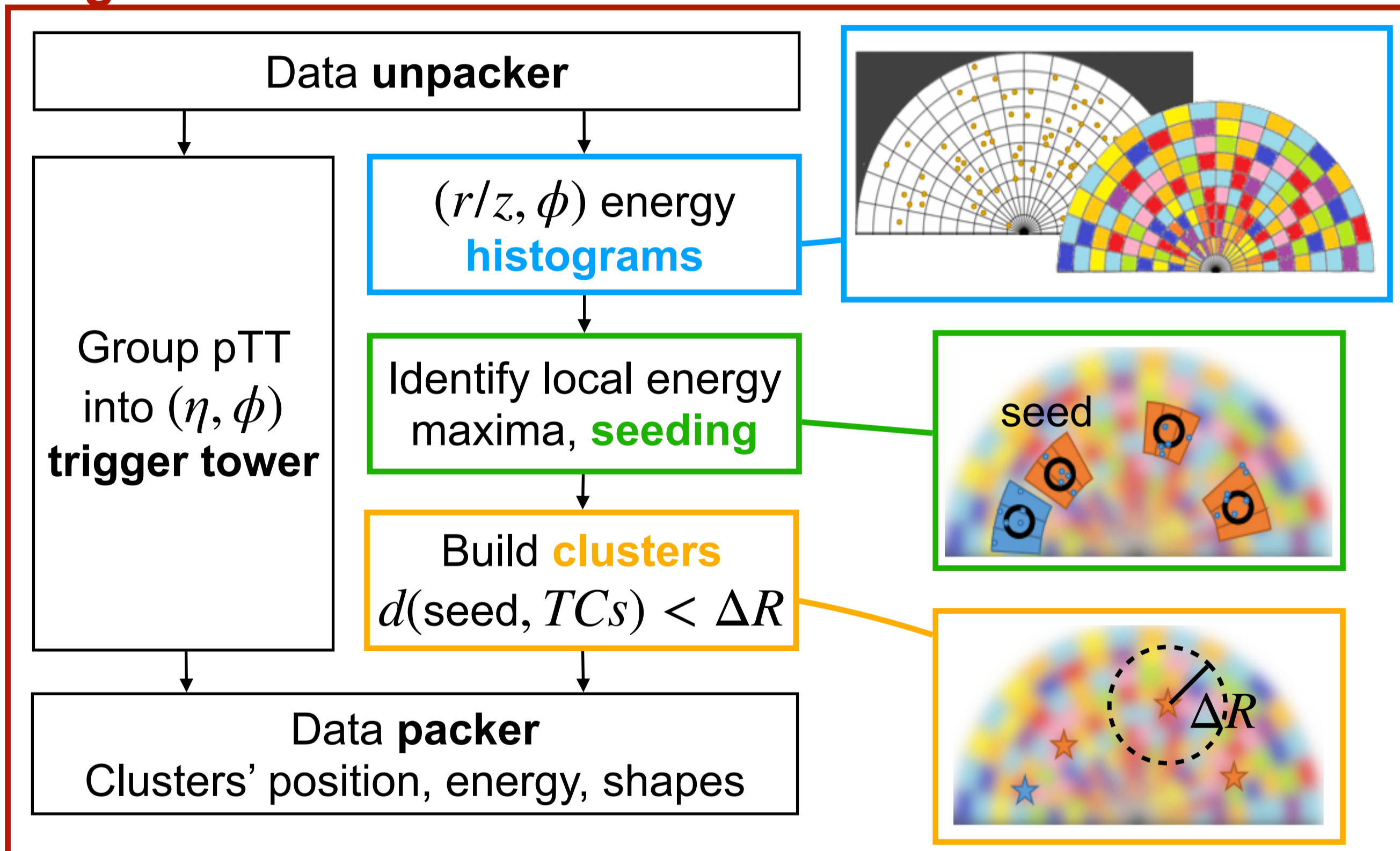
- ▶ Selects or compresses TCs:
 - TCs above a threshold
 - or N most energetic TCs
 - or TCs aggregation (STCs)
- ▶ Computes **module sums** w/o any threshold

Trigger cell (TC):
2x2 or 3x3 cells

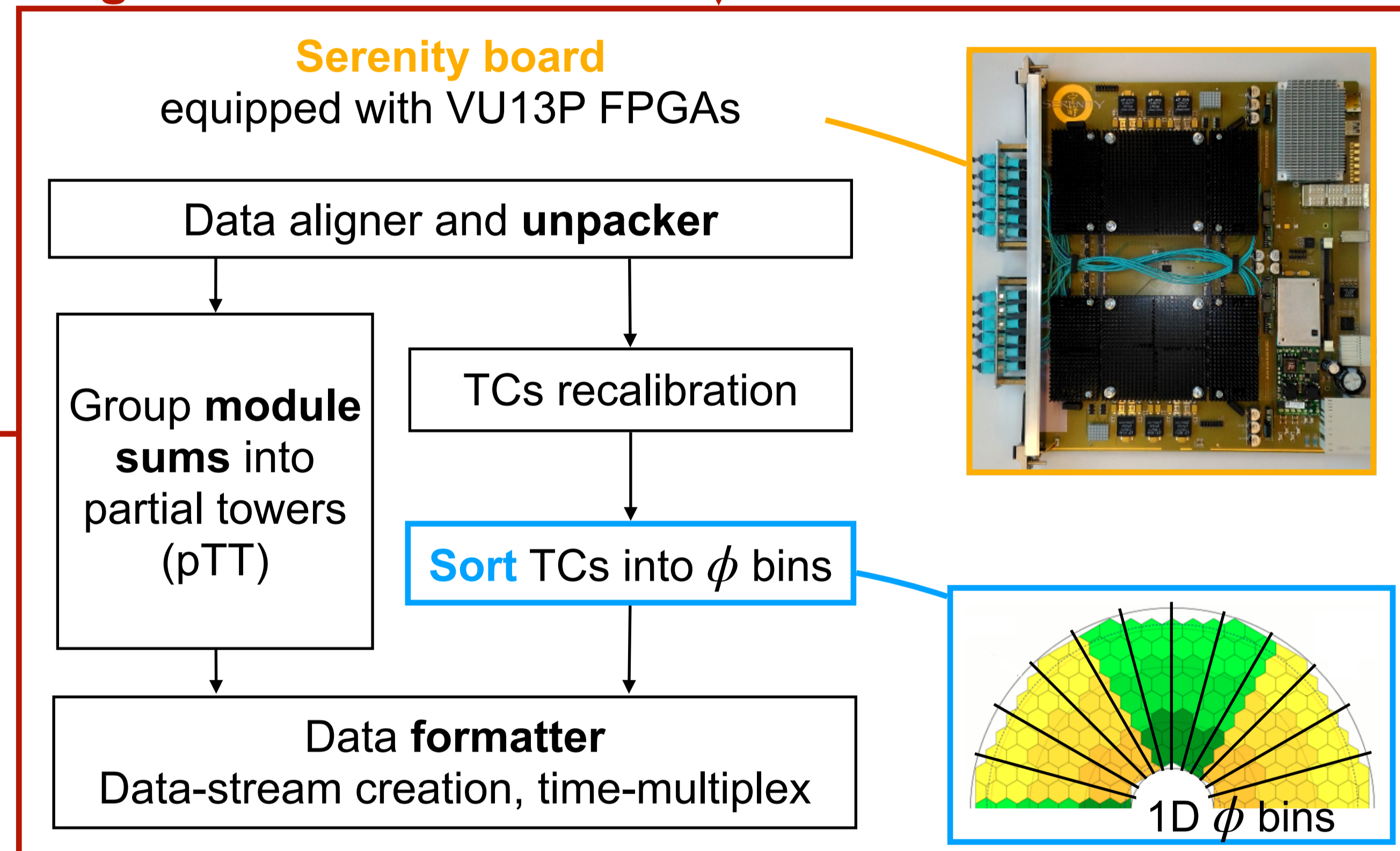
Max 120 links @ 10Gbit/s per FPGA

Back-end electronics (BE)

Stage 2 FPGAs



Stage 1 FPGAs



L1 Trigger

Global Calorimeter Trigger

- ▶ TP from calorimeters barrel and endcaps are grouped

Correlator Trigger

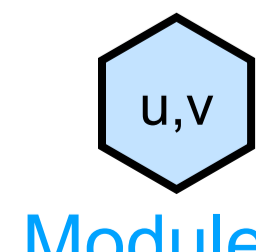
- ▶ The HGCal, the muon and the tracker TPs are combined for the purpose of particle flow reconstruction.

Module and motherboard geometry

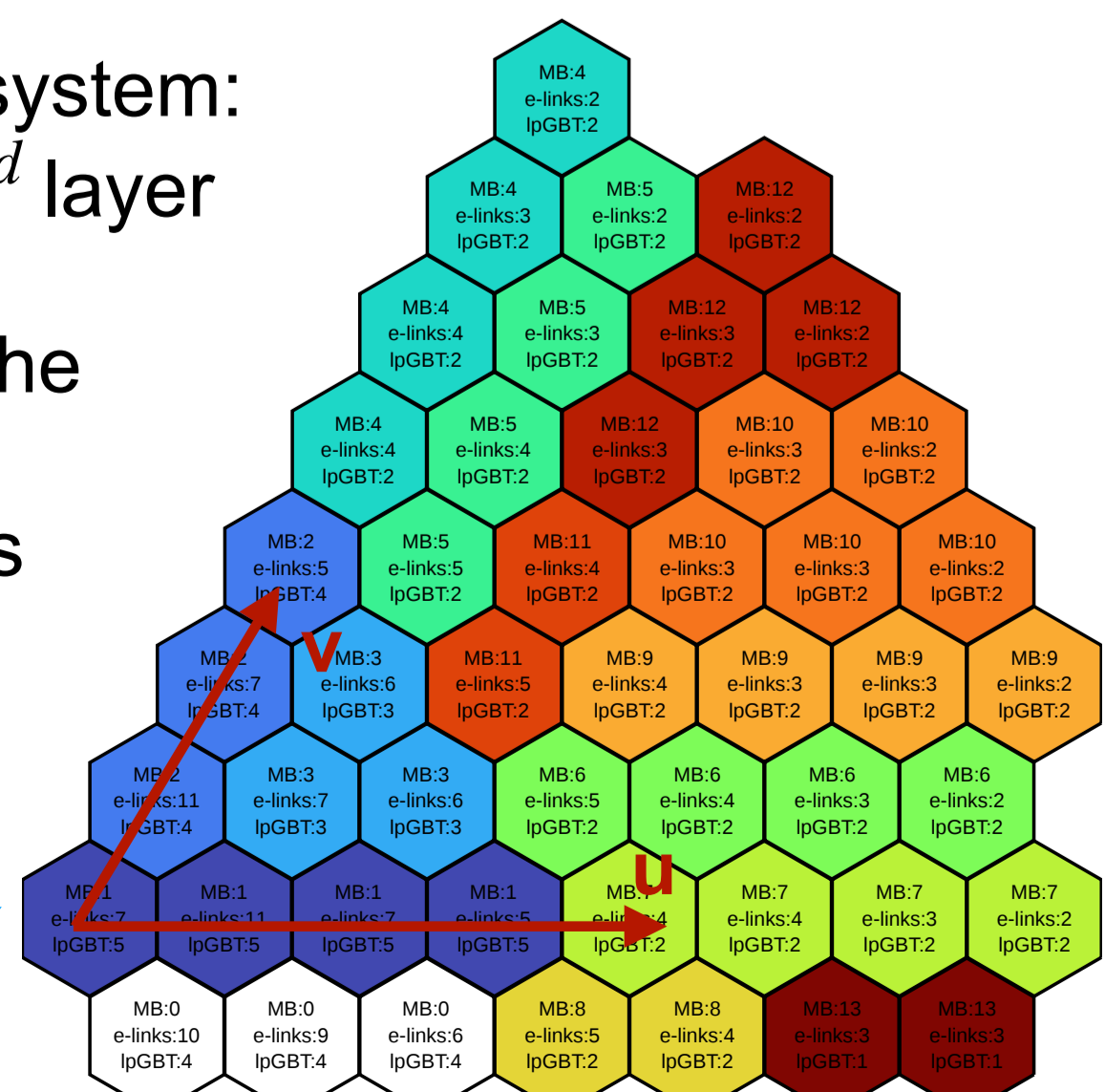
- ▶ FE data follows **module** and motherboard (MB) **geometry**:
 - Each MB collects e-links from several modules and forwards the collected data to the BE electronics;
- ▶ BE produces TPs using **projective coordinates** $(r/z, \phi)$ to create 2D energy histograms;
- ▶ **Non-trivial dataflow** in the BE and FE interface:
 - Event-by-event dataflow is variable within the same $(r/z, \phi)$ bin and requires special techniques to handle it.
 - FE and BE constraints must be taken into account;

Detector non-orthogonal coordinates system: the internal structure of the HGCal 3rd layer

- ▶ Several modules are mapped into the same motherboard (MB)
- ▶ MBs are mapped in Stage 1 FPGAs
- ▶ Currently investigating the most suitable configuration



Colour-coded modules based on MB



Investigating clustering improvements

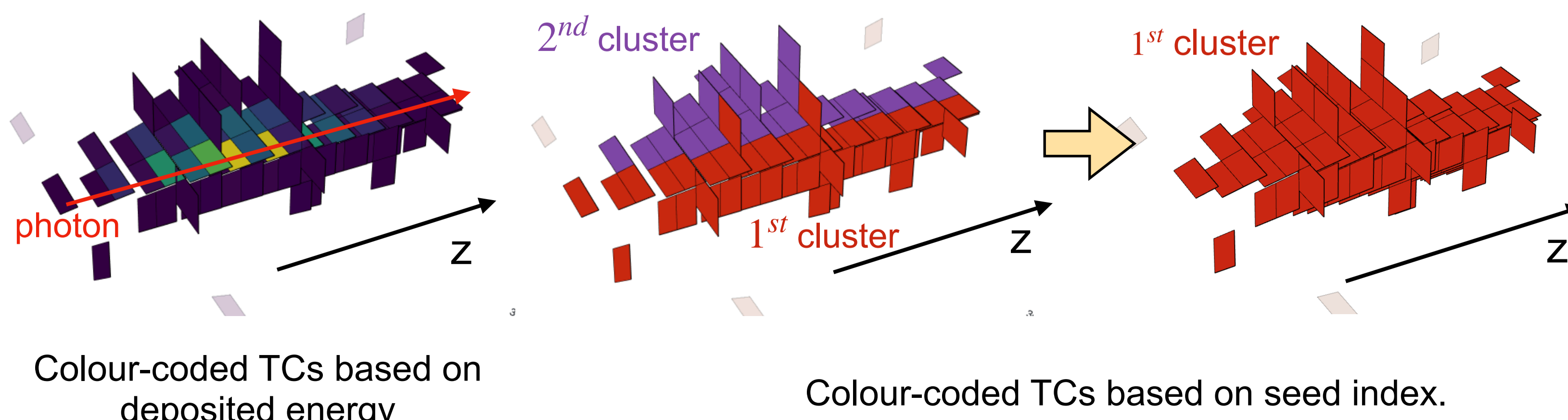
In $(r/z, \phi)$ histograms the distribution of TCs into bins is non-homogeneous:

- ▶ Bins size smaller than TCs at high η : nonphysical **cluster splitting** observed;
- ▶ Large bin at low η : difficult to separate nearby showers.

Various methods are being explored to mitigate cluster splitting:

- ▶ **Reducing the variance** of the number of TCs/bin along ϕ :
 - Artificial TC shifts are applied within bin triplets;
- ▶ Extending along ϕ the detector region where to find local maxima, i.e. seeds;
- ▶ Modifying the smoothing kernel along ϕ before seeding;
- ▶ Using **detector coordinates** (u, v) instead of r/z and ϕ in the BE FPGAs.

Example of a cluster splitting removal process for simulated 0PU photon in the HGCal:



Colour-coded TCs based on deposited energy

Colour-coded TCs based on seed index.