

# Development of the Belle II Hardware Track Trigger for High Luminosity

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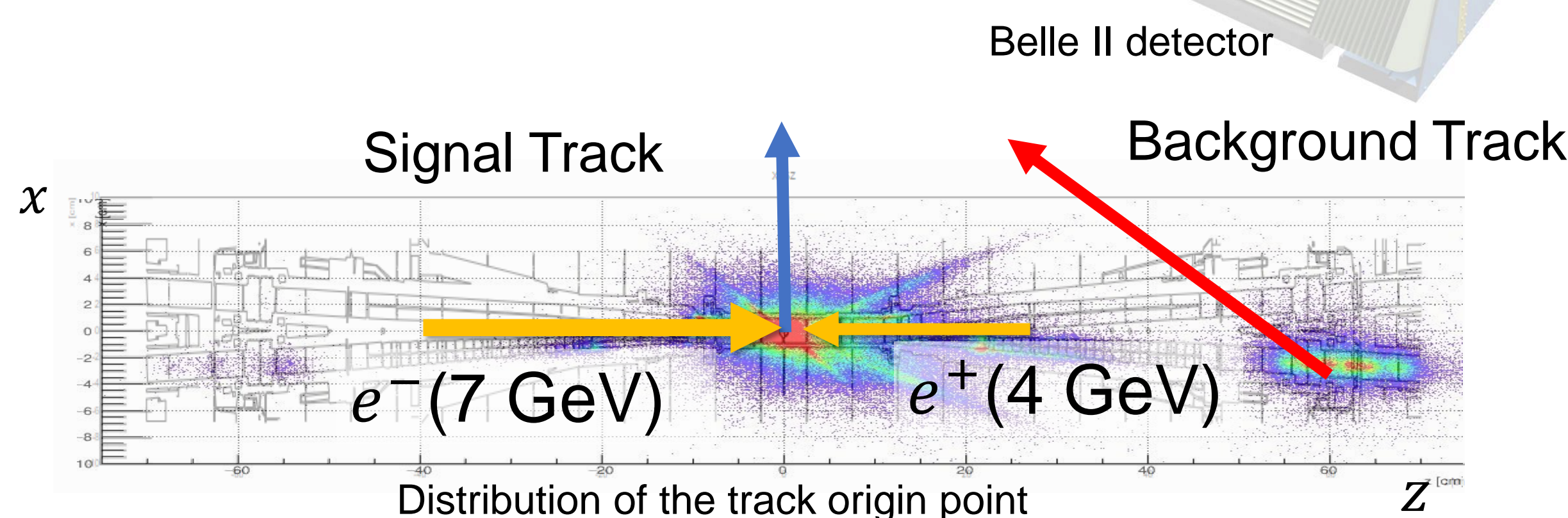
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## 1. Motivation

- The Belle II detector detects huge events from electron-positron collisions.
- Central Drift Chamber(CDC) is used mainly for particle tracking.

Central Drift Chamber(CDC)



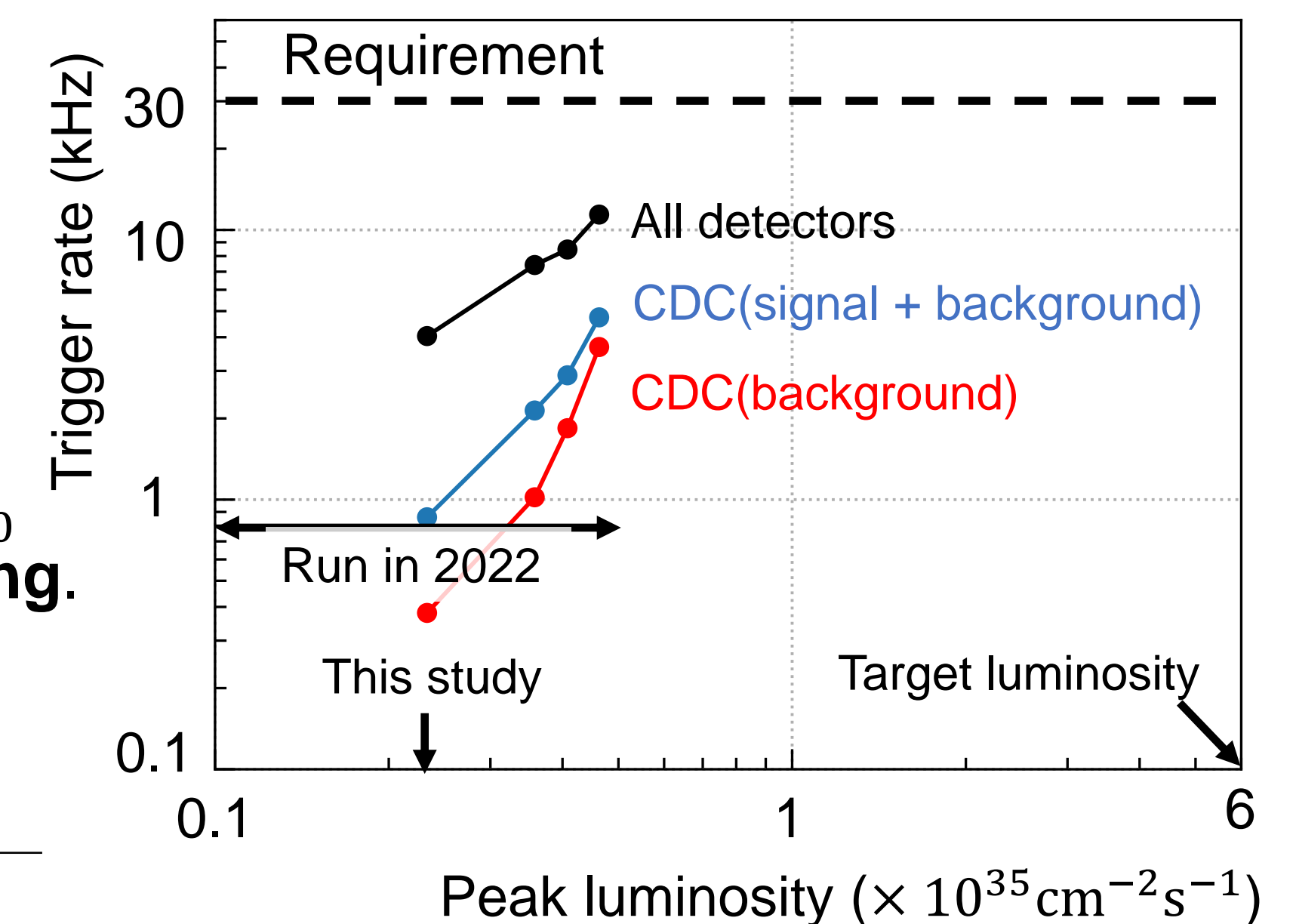
- Belle II uses the hardware trigger system to reduce the readout rate and data size.

Requirements of the trigger system

1. Trigger rate < 30 kHz
2. Latency < 4.5  $\mu$ s
3. Trigger efficiency for the B physics ~100%

- The current trigger system would exceed the limit of 30 kHz at the target luminosity.
- We can reject background tracks based on  $z_0$  (z position of the track origin) obtained by **3D tracking**.
- The target performance of this study

	target	(cf. original)
efficiency	90%	~ 90%
background rejection rate	80%	~ 40%



## 2. Original 3D tracking algorithm

Track Segment Finder  
Center hit information  
- position  
- timing  
- left/right

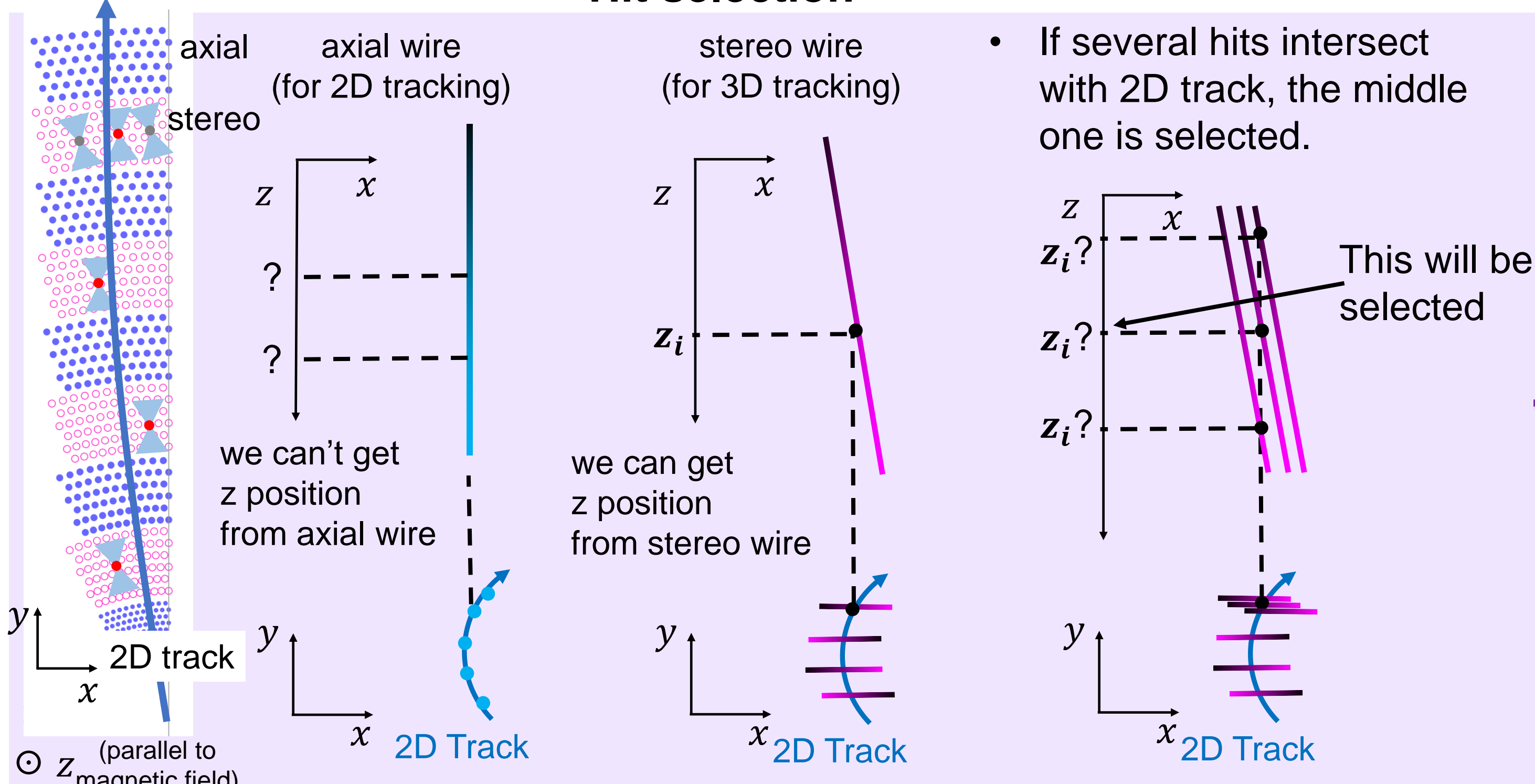
2D Track Finder  
azimuth angle  $\phi$   
track curvature  $\omega$

Event Time Finder  
event time  $T_0$

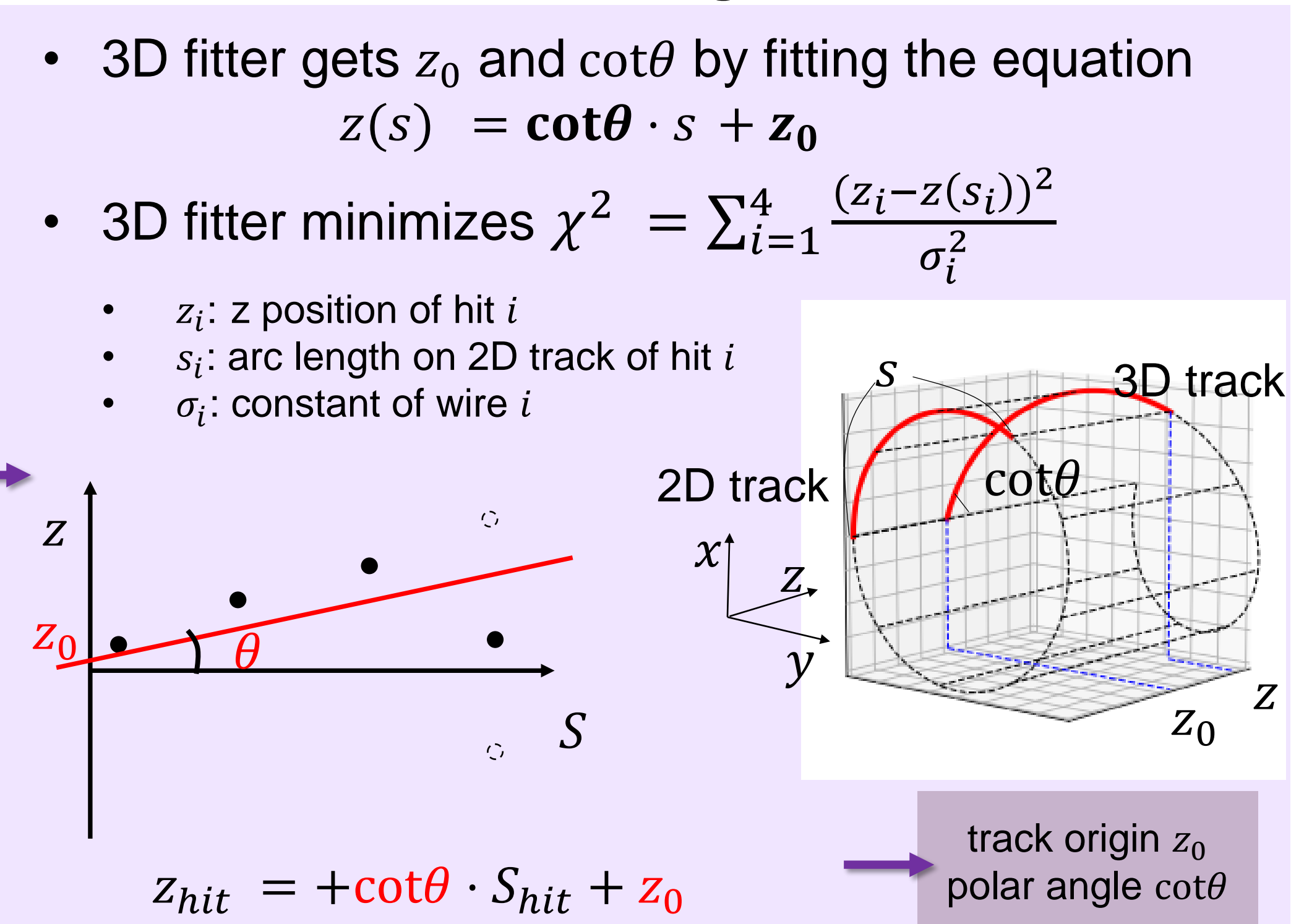
input

3D Fitter  
track origin  $z_0$   
polar angle  $\cot\theta$

Hit selection



Fitting



## 3. New 3D tracking algorithm

Track Segment Finder  
Full hit information  
- position  
- timing  
- left/right

2D Track Finder  
azimuth angle  $\phi$   
track curvature  $\omega$

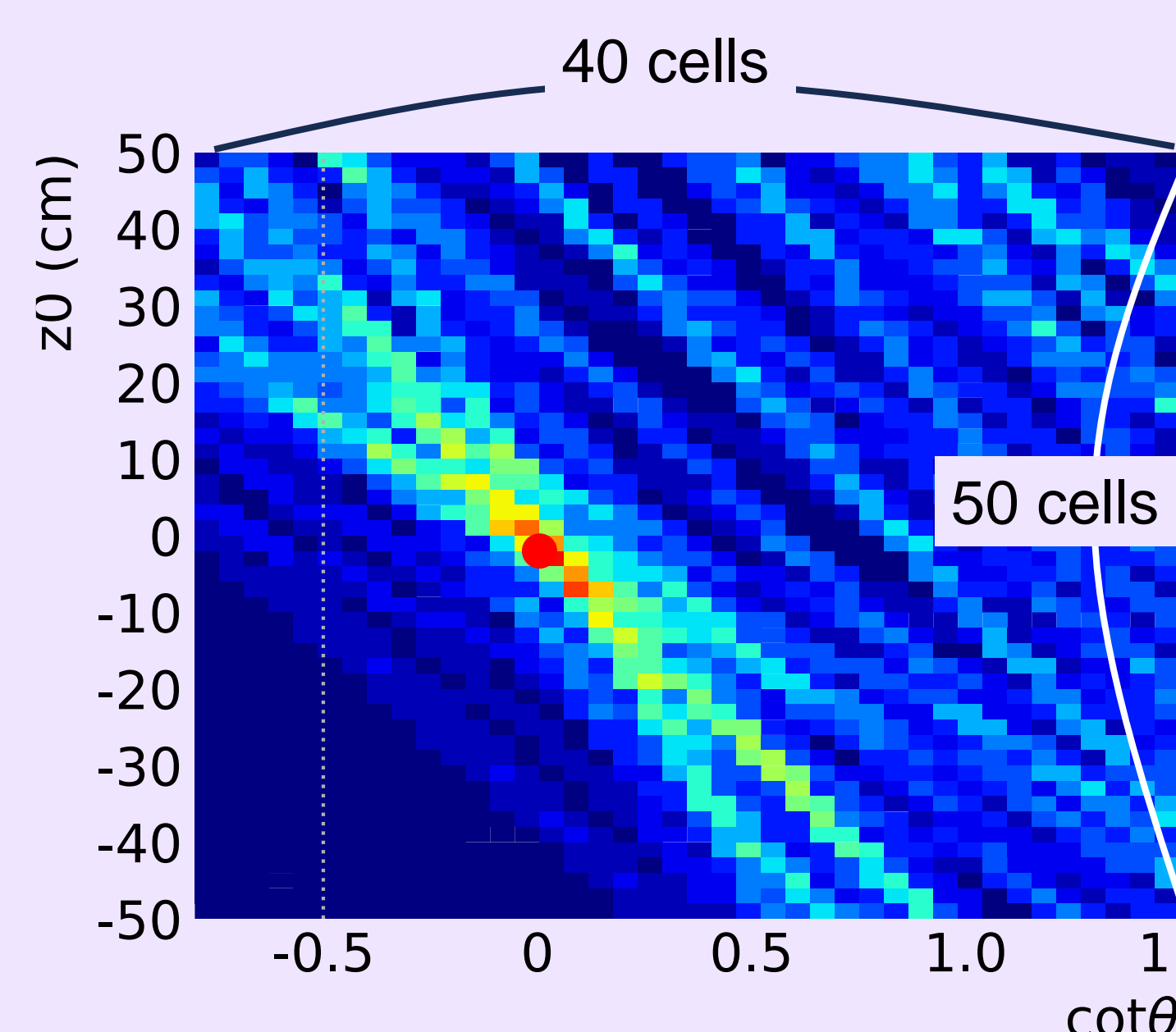
Event Time Finder  
event time  $T_0$

input

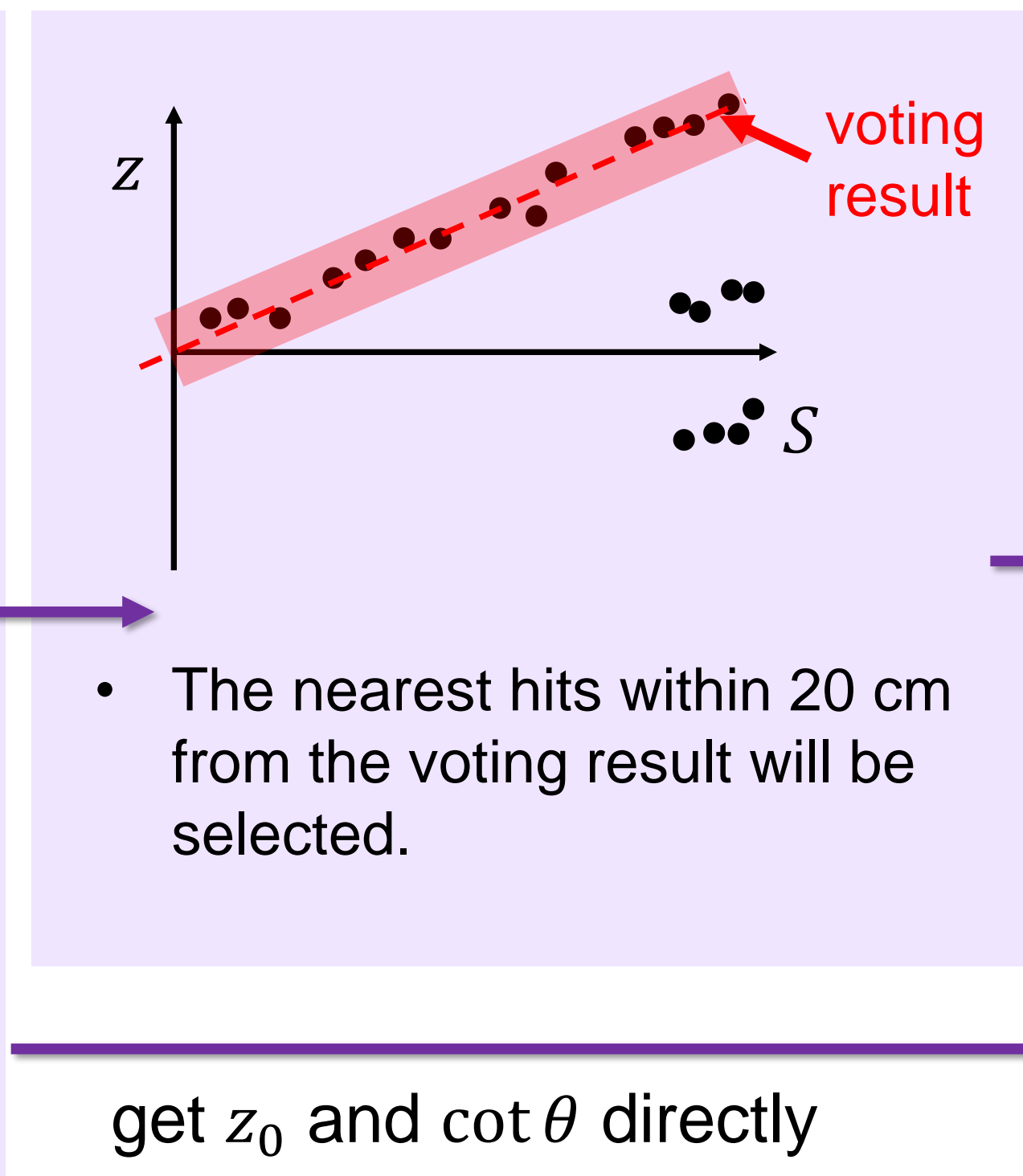
3D Fitter  
track origin  $z_0$   
polar angle  $\cot\theta$

Voting

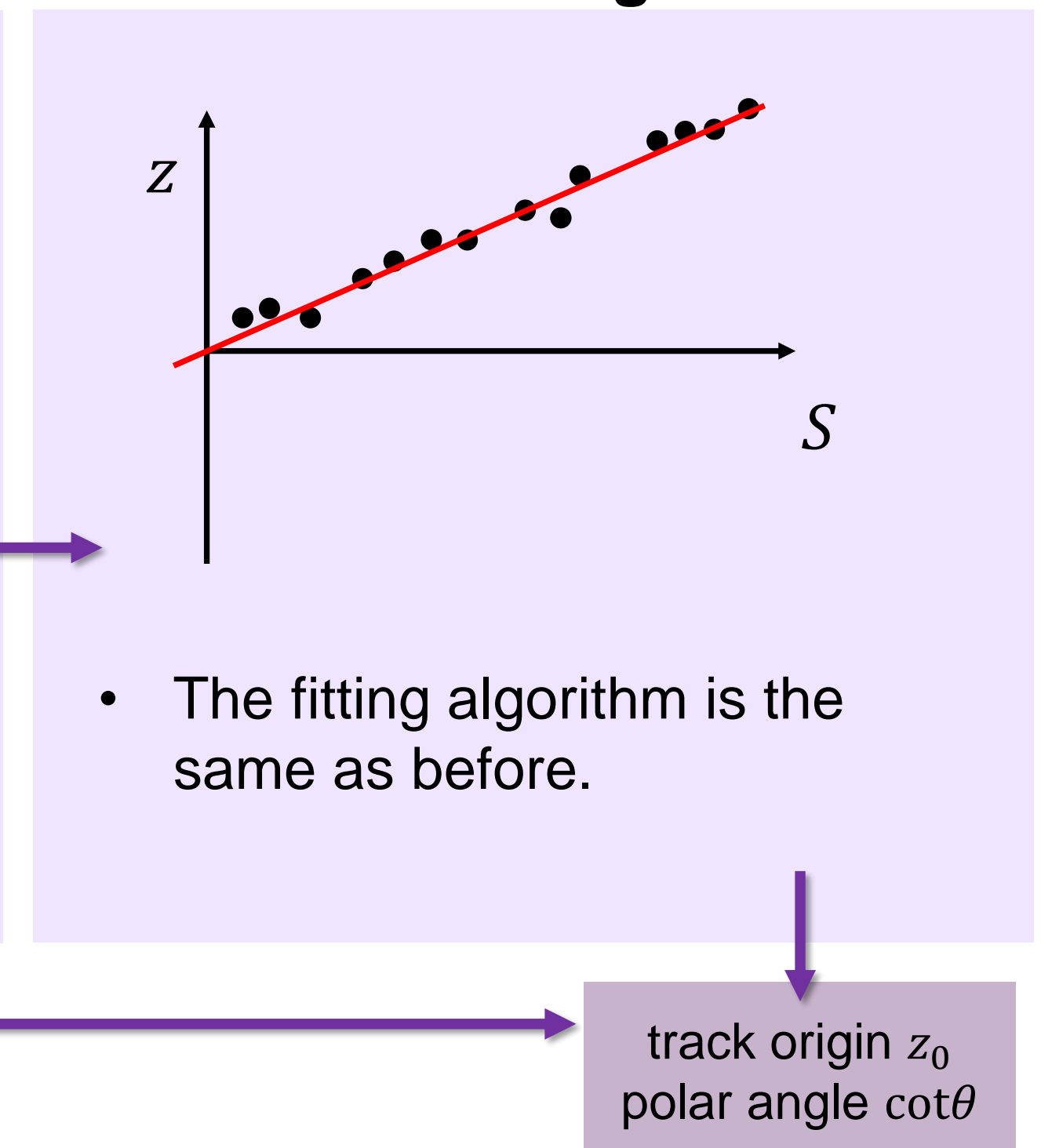
- The  $z_0 - \cot\theta$  parameter space is divided into cells.
- Each hit corresponds to each line.  
 $z_0 = -S_{hit} \cdot \cot\theta + z_{hit}$
- The most voted cell is the result.
- The voting method is **noise-robust**.



Hit selection

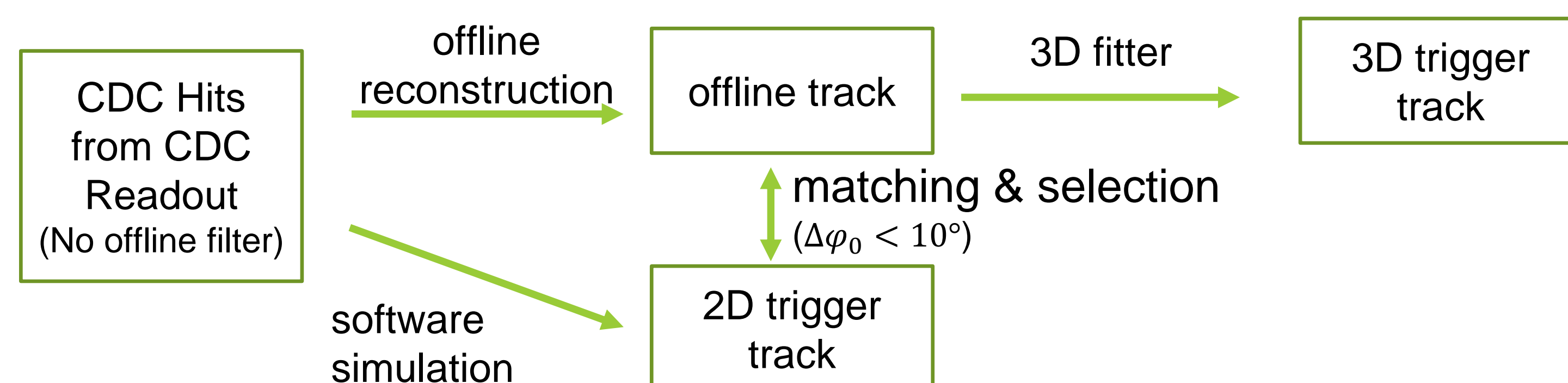


Fitting



## 4. Performance evaluation

Dataflow



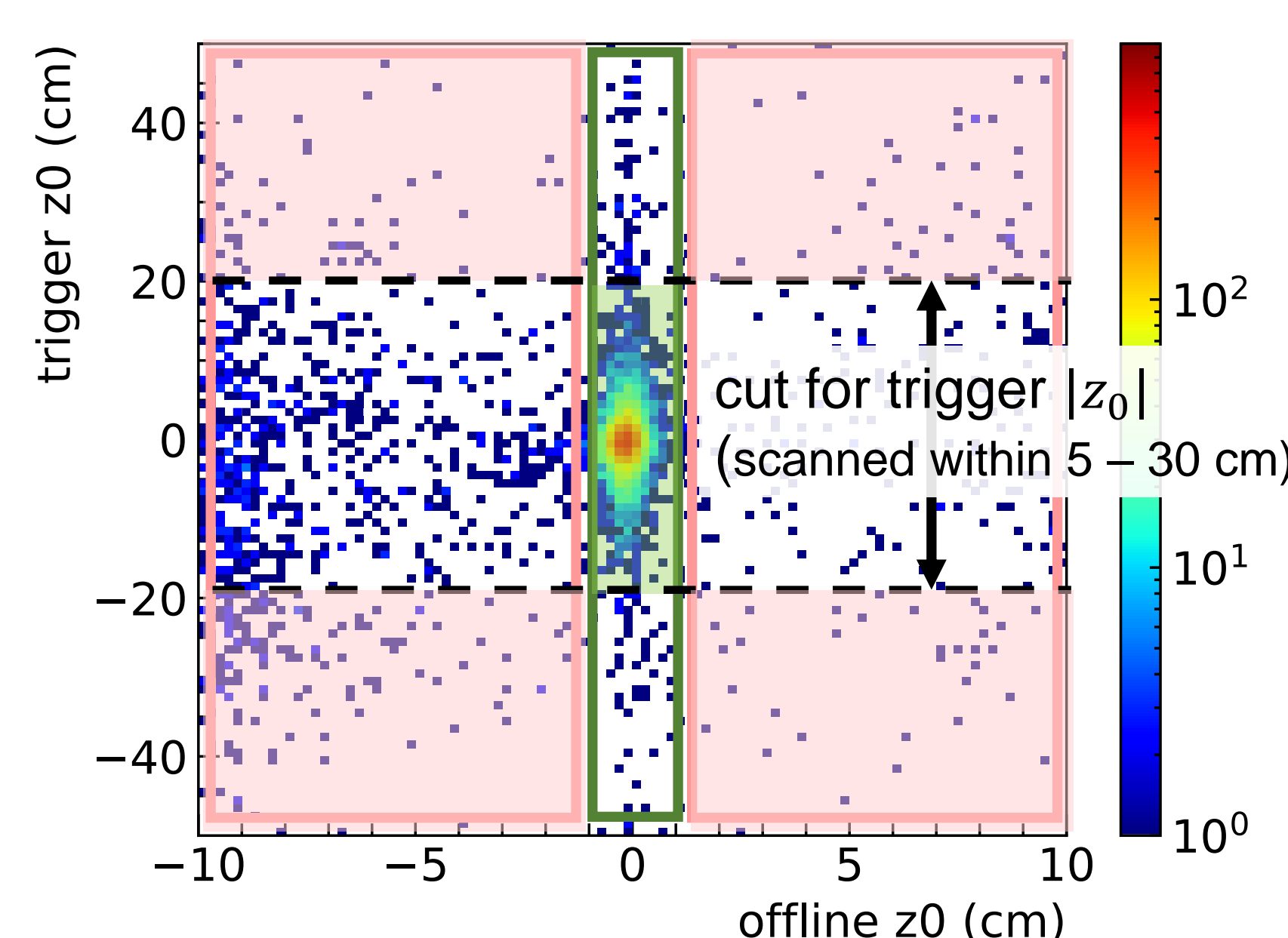
Performance indices

3D efficiency

$$:= \frac{\#(\text{triggered signal track})}{\#(\text{signal track})}$$

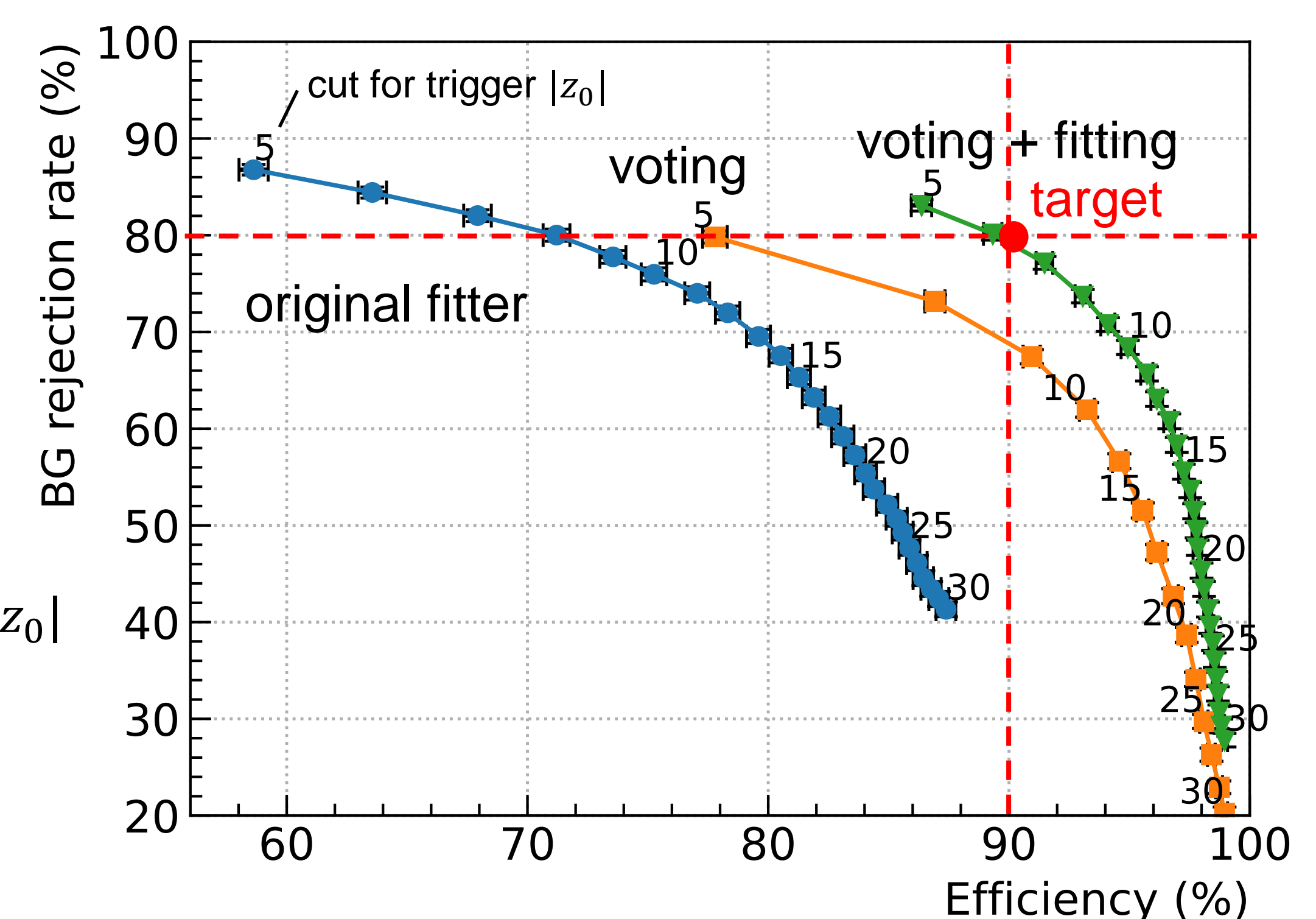
BG rejection rate

$$:= \frac{\#(\text{rejected BG track})}{\#(\text{BG track})}$$



## 5. Result

- Cut for trigger  $|z_0|$  was scanned within 5-30cm



## 6. Summary & Future

- To reduce the trigger rate, I developed new algorithms for the hardware track trigger.
- The **voting + fitting** method achieved the target performance (efficiency ~ 90% and BG rejection rate ~ 80%).
- Implementation of this method to the FPGA is ongoing.