Keynote: Future Trends and Technologies of CPU's & FPGA's

Wednesday 7 December 2022 16:00 (30 minutes)

As advancements in technologies continue to occur, component capabilities utilized in driving these advancements must continue to evolve.

These advancements drive the need for new considerations in consumption of these technologies. These considerations may be as simple as acceptable I/O voltages at given silicon process geometries to more advanced capabilities of tight coupling of CPU and accelerations technologies utilized to secure or improve a particular application's performance, such as Artificial Intelligence or Machine Learning algorithms. In this keynote, Intel's Senior Platform Architect and Principal Engineer in the Networking and EDGE division, Dirk Blevins, shares the impact to Intel's CPU and FPGA products emanating from these future technology trends.

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