

AMD new ACAP Architecture enables close to Zero Dark Silicon

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AMD / Xilinx achieved the world's highest compute efficiency at 90%, becoming the first vendor to achieve near Zero Dark AI Silicon in modern AI benchmarks.

Planning for AI requires an understanding of how much data needs to be processed and how quickly that needs to happen. This talk will talk about data bubbles and domain-specific designs, why dark silicon is no longer as useful as in the past, and how to optimize power and performance in both the data centre and at the edge.

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