

Developing the Next Generation of MicroTCA

Tuesday 6 December 2022 17:00 (15 minutes)

The current MicroTCA specification has a data transfer speed limit of 8 GT/s for PCIe that is required for PCIe gen 3. Recent CPUs already implement PCIe gen 5 with a factor four communication speed increase. To update the PICMG standard accordingly a working group was formed to tackle the challenges. Connectors, backplanes, routing and power issues for example had to be solved. The presentation will discuss the status and future steps to keep MicroTCA viable over many more years.

Primary author: REHLICH, Kay (DESY)

Presenter: REHLICH, Kay (DESY)

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