

Obsolescence Management regarding the Renesas 8V54816ANLG low jitter clock multiplexer and future solutions for the NAT-MCH

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The low jitter clock multiplexer 8V54816ANLG from Renesas (ex. IDT) is a well recognized silicon used with many applications in science and industry whenever the clock distribution inside a MicroTCA system requires maximum flexibility regarding multiple possible clock sources and the ability to switch among these at run-time.

Its end of life with a last time buy date in 2023 and the fact that there is no 1:1 replacement available today requires that MicroTCA component vendors develop strategies to help customers maintain their current applications and explore option for future upgrades.

By the example of the NAT-MCH and its associated clock modules this presentation will explore a go-forward strategy and the impact on the future use of the product and on the applications it is used in.

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