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Type: **Poster without speed talk**

Monolithic Active Pixels in 180 and 65 nm CMOS: Technologies for ALICE at the LHC

The poster presents an overview of the R&D work on MAP sensors within the ALICE group at GSI/Heidelberg. Detailed simulations of in-pixel charge behaviour are utilized to improve detector resolution.

Bent ALPIDE sensors were used to prove the feasibility to realize cylindrical layers for vertex detectors, and flat sensors are employed in a general-purpose beam test telescope.

The novel CMOS TPSCo 65 nm technology has been tested, and first results from test structures are shown.

Speed Talks

Normal

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