

# Silicon sensors and their quirks - understanding humidity sensitivity

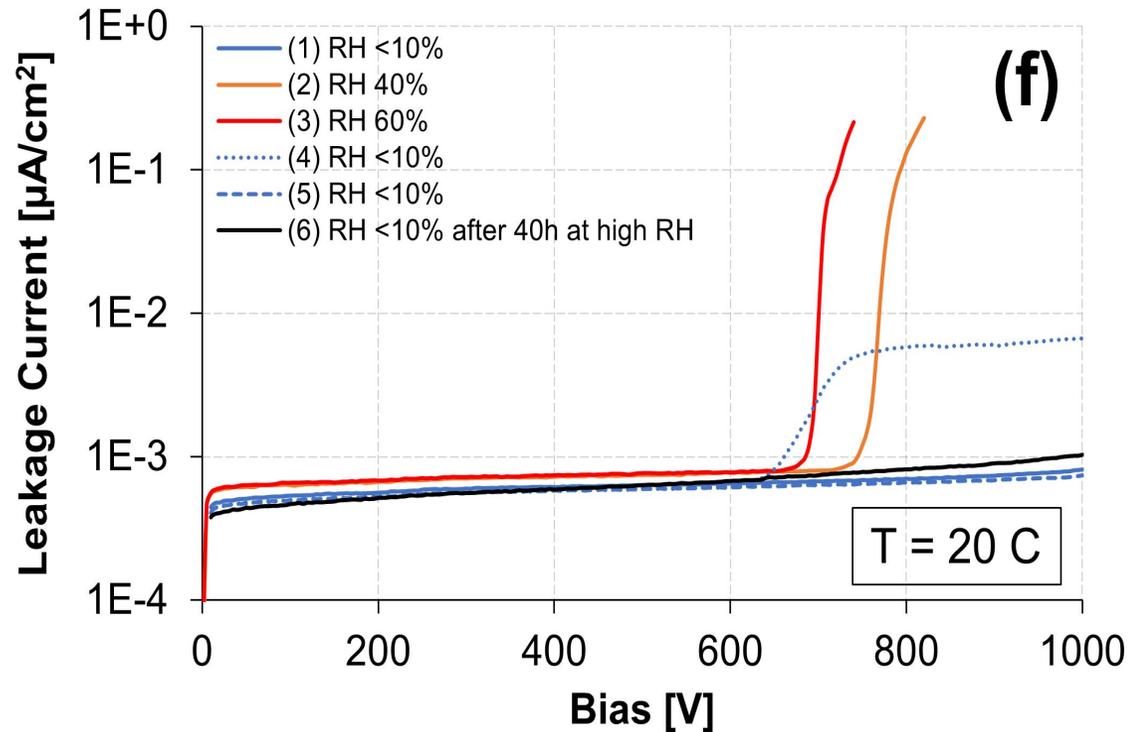
**Ilona-Stefana Ninca**<sup>1</sup> with special thanks to Ingo Bloch<sup>1</sup>, Ben Brüers<sup>1</sup>, Peilin Li<sup>2</sup> and Christian Scharf<sup>2</sup>  
<sup>1</sup>Deutsches Elektronen-Synchrotron (DESY) — <sup>2</sup>Humboldt-Universität zu Berlin, Berlin, Germany

**DESY Zeuthen Particle Physics Mini-Retreat 2023**

Monday, June 12th, 2023



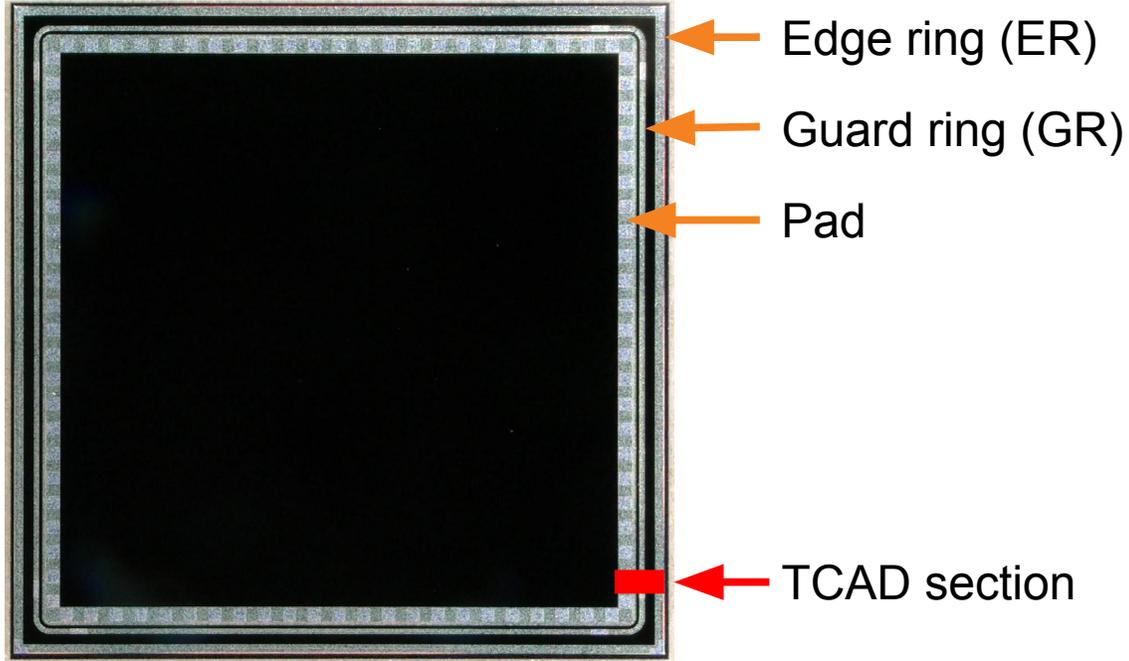
# Humidity Studies Motivation



ATL-ITK-PROC-2022-009

- Large area sensors in ATLAS ITk showed humidity sensitivity => early breakdown
  - => can irreversibly destroy the sensor
- pn junction with reverse-bias => blocks current flow
  - small amount of current, called leakage current, can still flow through the sensor
- Region where breakdown happens: visible as bright spots

# ATLAS18 Test Structures

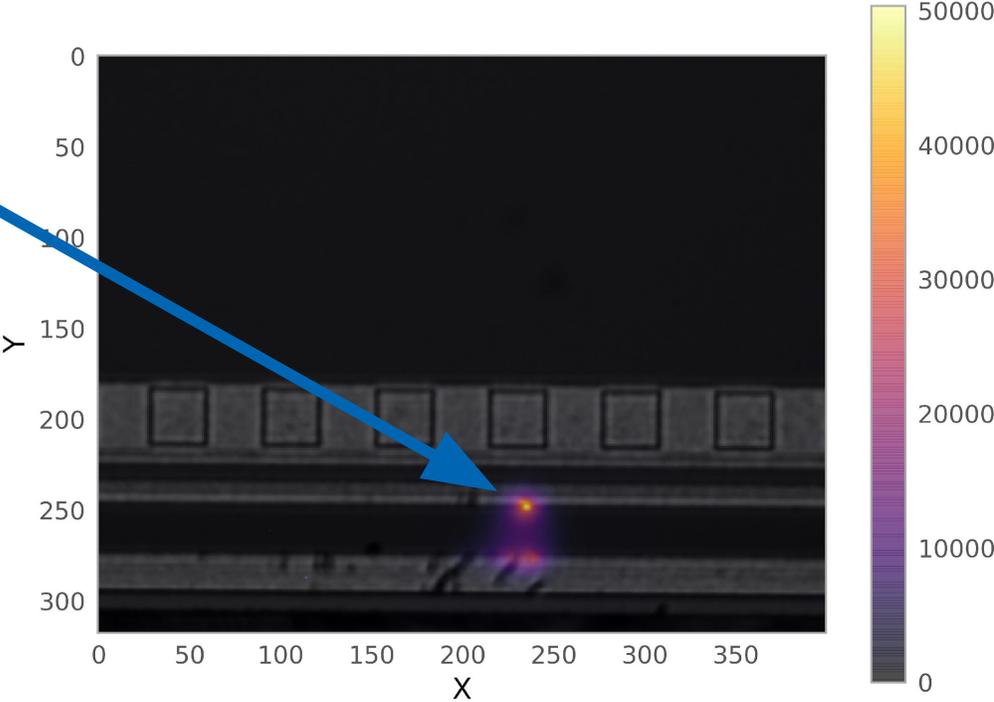
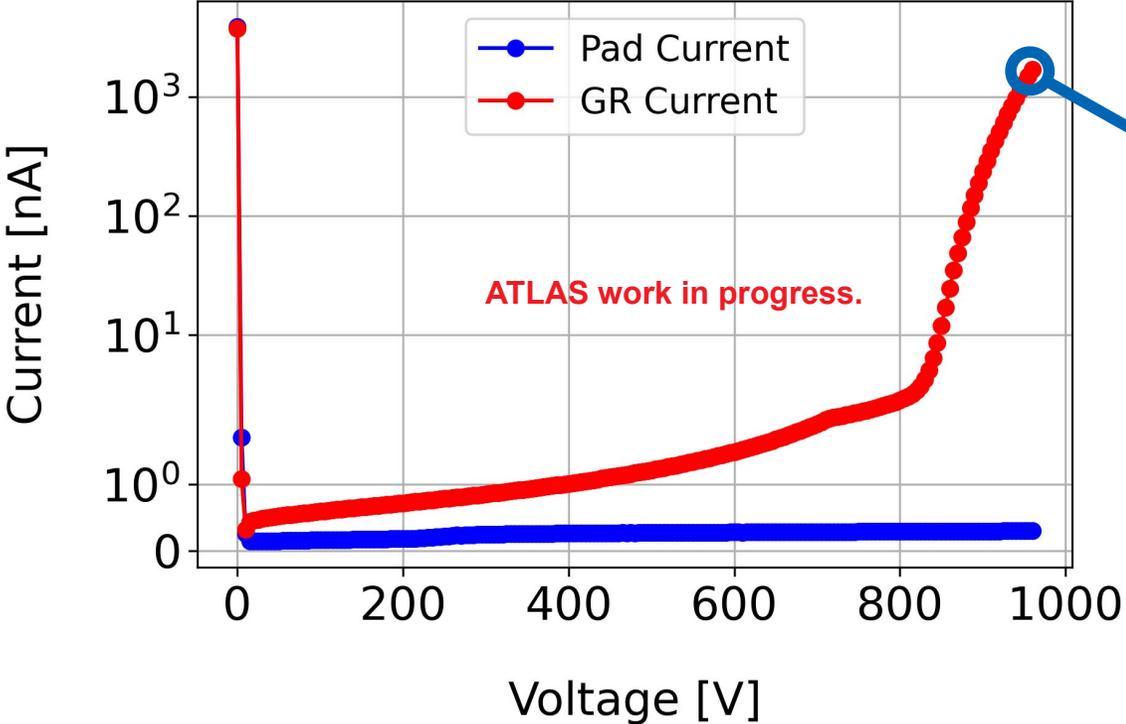


- Test structures monitor the characteristics of silicon bulk
- Similar properties as the strip sensor
- More cost-effective to stress test than a large area sensor
- Goals:
  - Identify cause of the sensor's high humidity sensitivity
  - Develop a more robust geometry

- 8 mm x 8 mm n<sup>+</sup>pp<sup>+</sup> (n-in-p) diodes (**MD8 diodes**)
- Active thickness ~ 296 μm
- Bulk doping: p-type concentration ~ 4.8 × 10<sup>12</sup> cm<sup>-3</sup>
- $V_{\text{Full Depletion}} = \sim - 330 \text{ V}$

# Current – Voltage Characterization

IV Curve

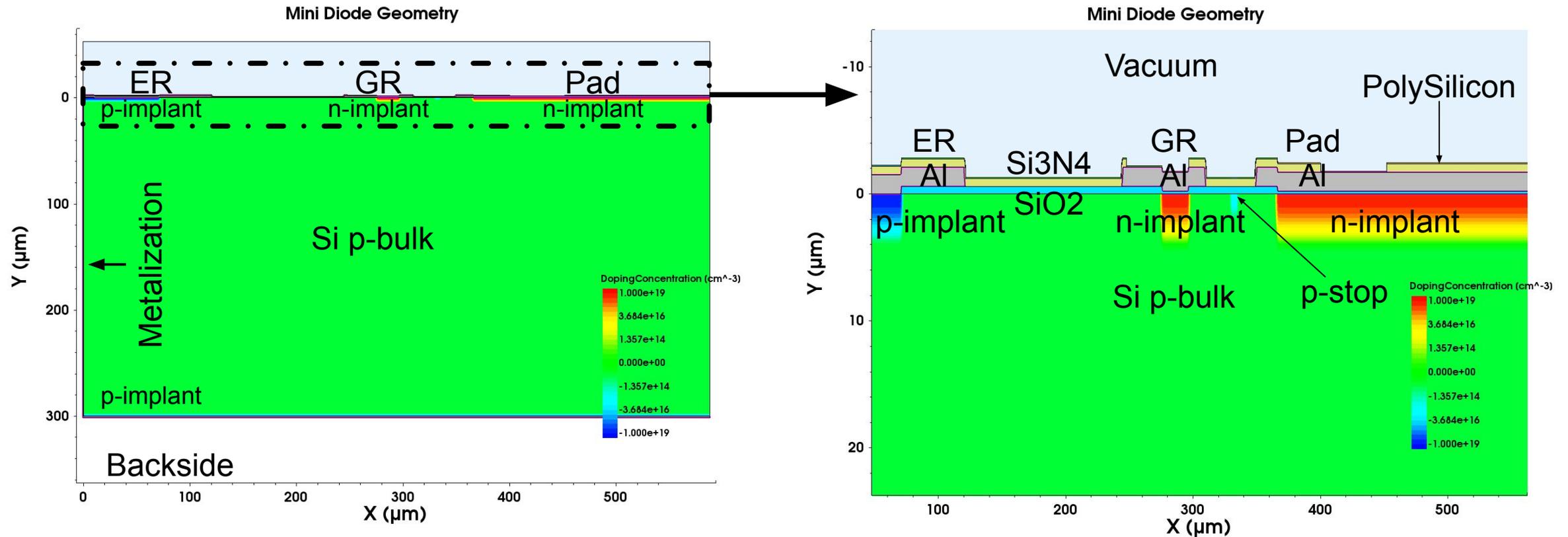


**Measured** GR and pad currents in respect to the applied bias voltage for RH = 36 % and T = 25° C. Breakdown at – 825 V.

**Imaged** hot electron emission at RH = 36 % and T = 25° C.  $V_{bias} = -990$  V.

# Technology Computer Aided Design (TCAD) Simulations

- Sentaurus TCAD is a suit of software tools developed by **Synopsis**



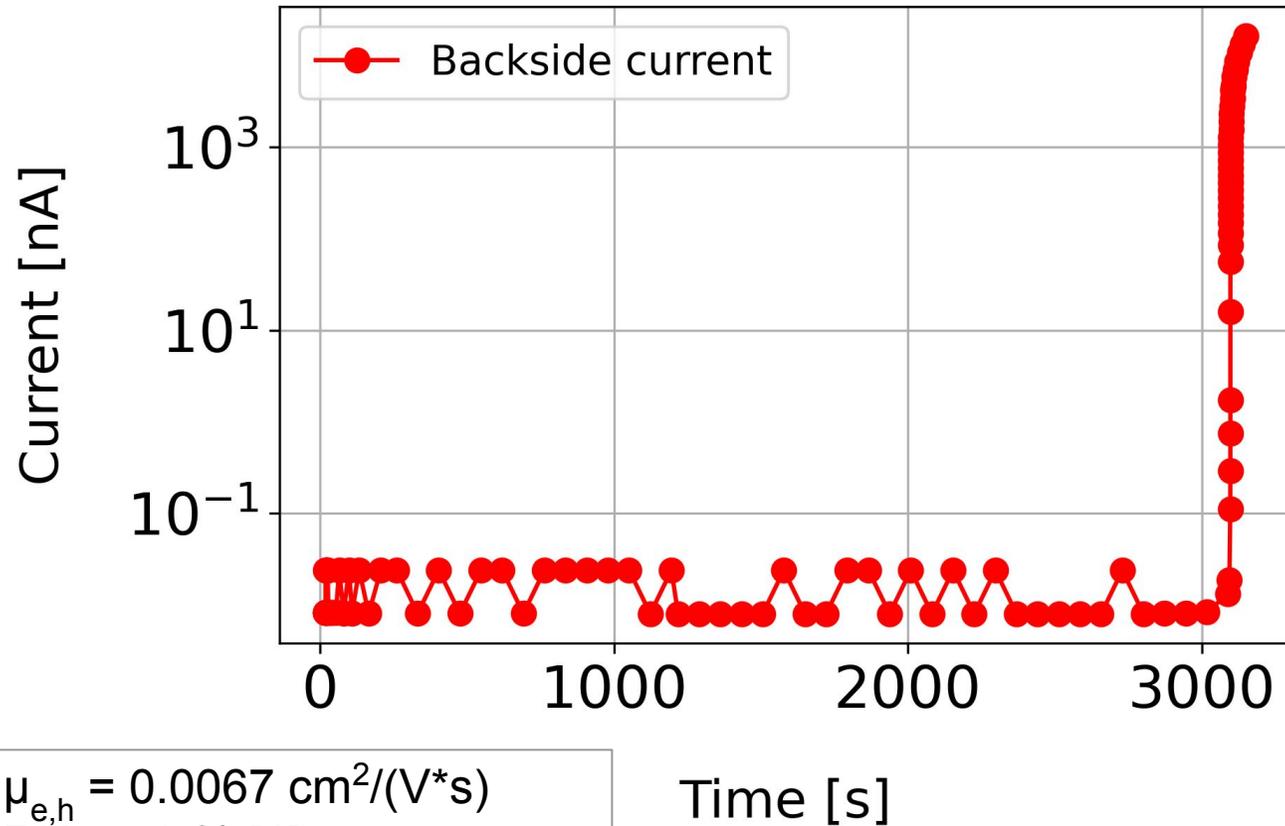
- 0.1 μm thick PolySilicon layer on top of the passivation
- GR and pad at ground
- - 900 V applied on the ER and backside

# Mobile Charge at the Surface and TCAD Modeling

- During wafer manufacturing, charges can become trapped
  - For RH > 10 %, trapped charges become mobile
    - ⇒ can affect device characteristics such as threshold voltage, leakage current, and capacitance
- Modeling Mobile Charge in TCAD
  - TCAD utilizes advanced numerical techniques to simulate and predict the behavior of semiconductor devices
  - Mobile charge at the surface is modeled through surface potential and charge equations
  - Surface states and their energy levels are considered to accurately capture the impact of mobile charge
- TCAD Simulation Steps
  - Surface Potential Calculation:
    - ⇒ Solve Poisson's equation to determine the electrostatic potential at the semiconductor surface
  - Surface Charge Calculation:
    - ⇒ Utilize charge equations to compute the density and distribution of mobile charges at the surface
  - Iterative Process:
    - ⇒ The simulation iterates until a self-consistent solution is reached, considering the interaction between charge and potential

# TCAD Results of MD8 Diode with p-stop

IV Curve



$\mu_{e,h} = 0.0067 \text{ cm}^2/(\text{V}\cdot\text{s})$   
RH = 40 % [1]  
 $V_{\text{bias}} = -900 \text{ V}$

- For RH = 40 % and  $V_{\text{const}} = -900 \text{ V}$ , breakdown at 3097 s
- For dry conditions  $V_{\text{const}} = -900 \text{ V}$ , no sign of breakdown after 3 hours

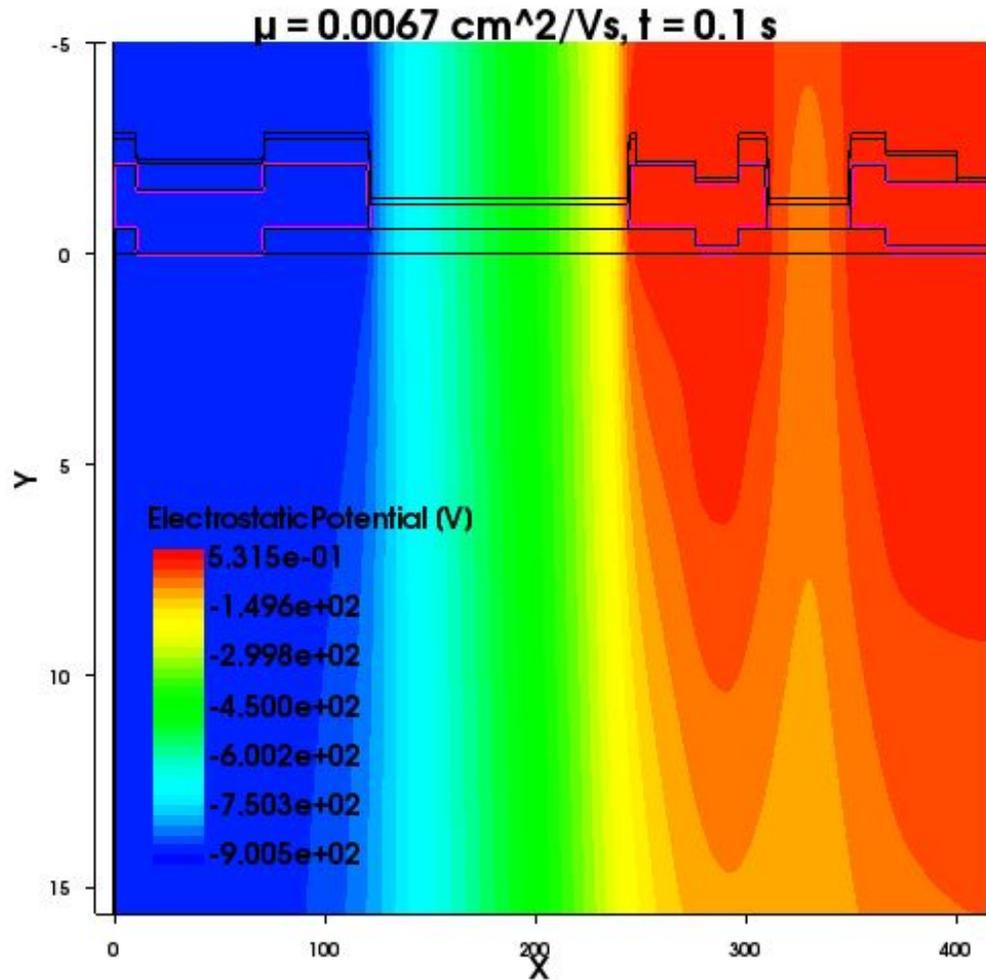
# TCAD Results of MD8 Diode with p-stop

$$\mu_{e,h} = 0.0067 \text{ cm}^2/(\text{V}\cdot\text{s})$$

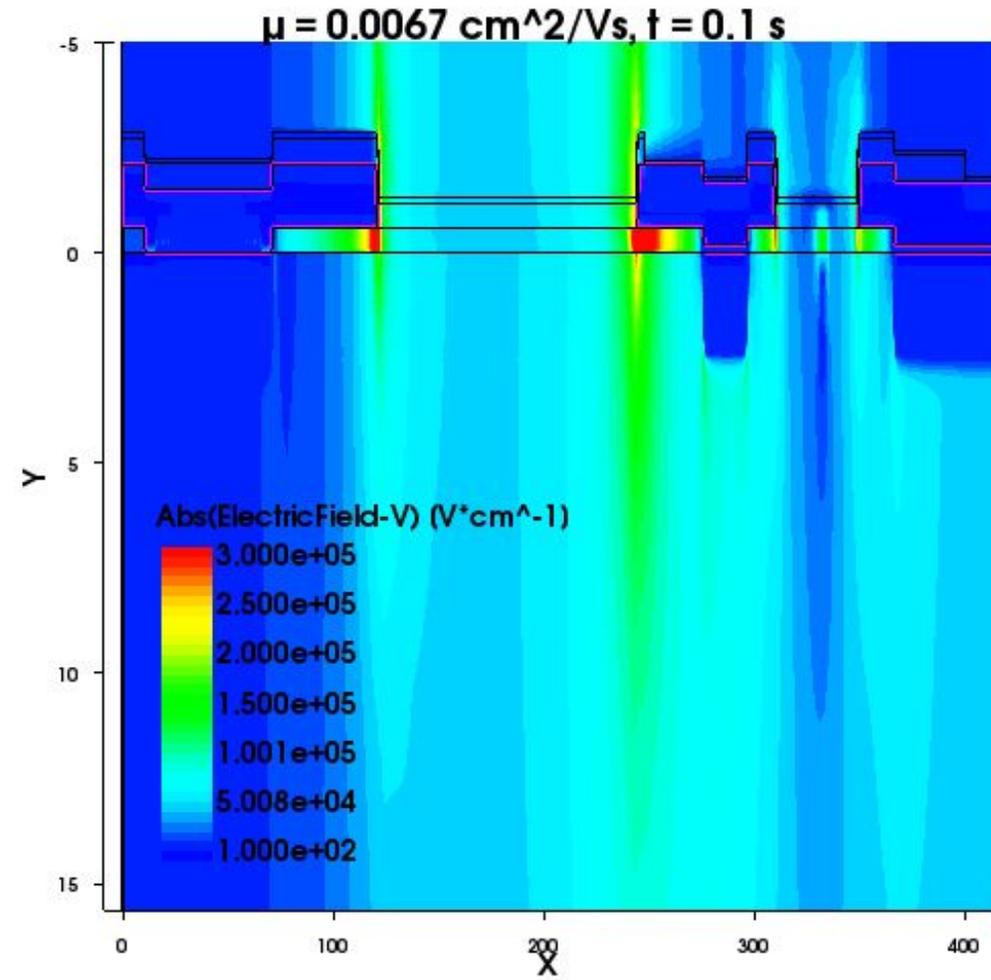
$$\text{RH} = 40 \% \text{ [1]}$$

$$V_{\text{bias}} = -900 \text{ V}$$

Electrostatic Potential (V)

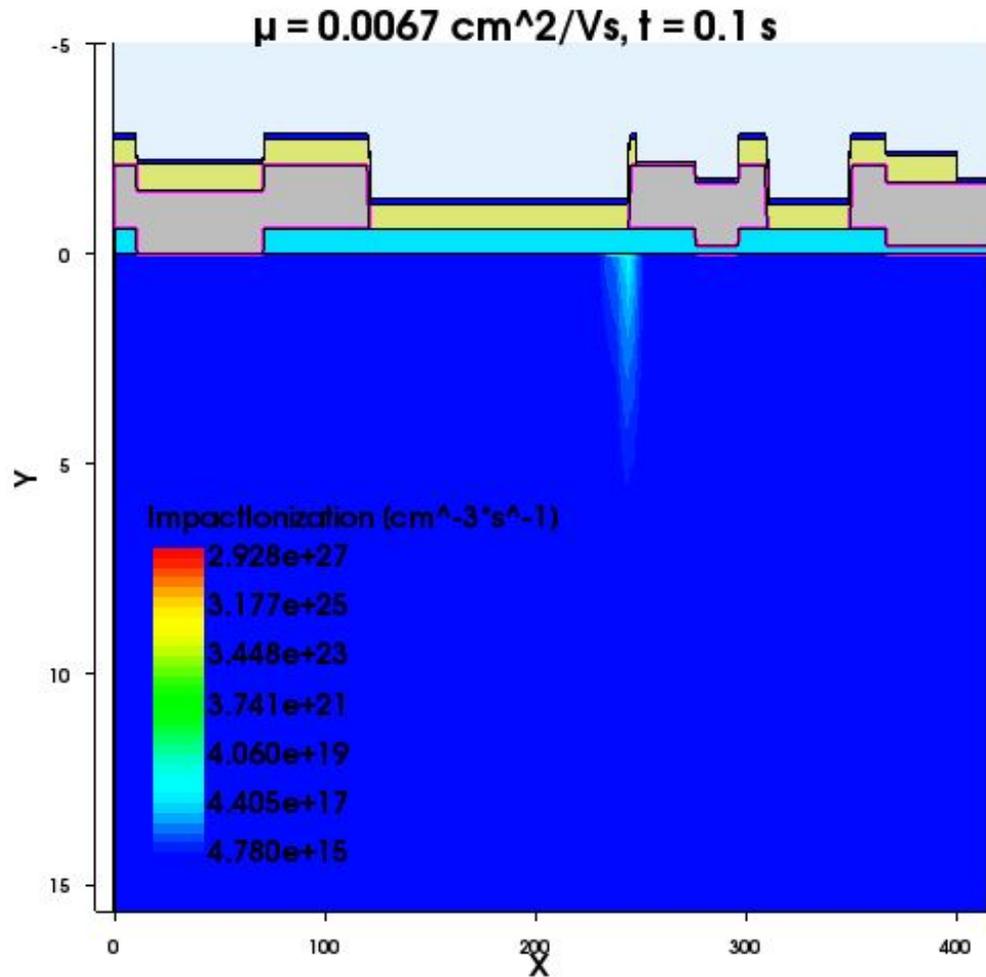


Electric Field ( $\text{V}\cdot\text{cm}^{-1}$ )



# TCAD Results of MD8 Diode with p-stop

Impact Ionization ( $\text{cm}^{-3}\text{s}^{-1}$ )

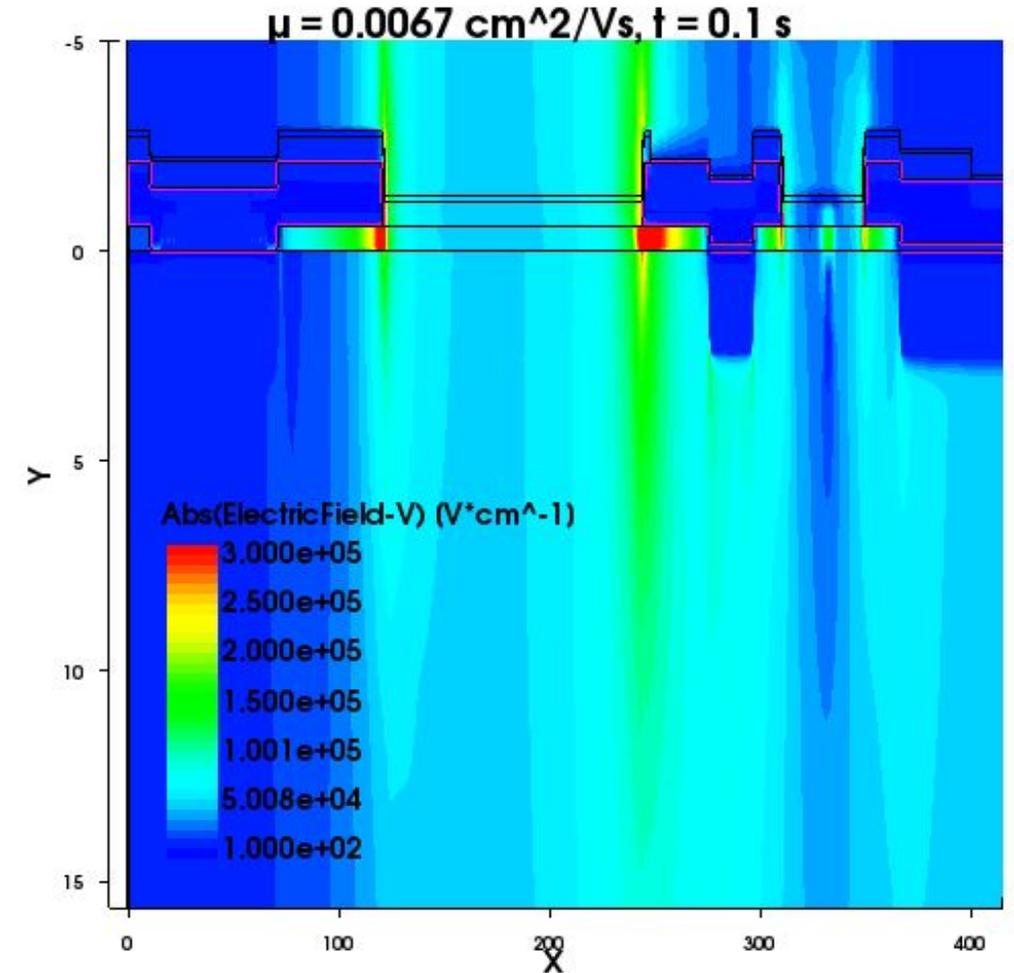
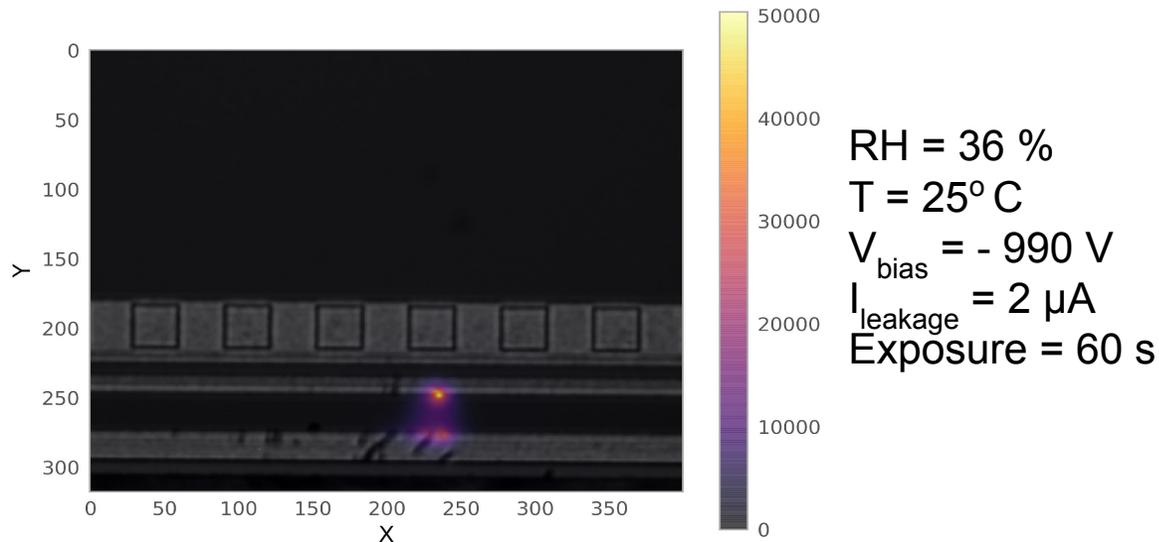


$$\begin{aligned}\mu_{e,h} &= 0.0067 \text{ cm}^2/(\text{V}\cdot\text{s}) \\ \text{RH} &= 40 \% \text{ [1]} \\ V_{\text{bias}} &= -900 \text{ V} \\ \tau &= 10^{-6} \text{ s} \\ U &= 4.78 \times 10^{15} \text{ cm}^{-3} \text{ s}^{-1}\end{aligned}$$

- Charge spread between GR and pad  $\Rightarrow$  possibility of a short
- High electric field near ER and GR  $\Rightarrow$  high chances of hot electrons to be emitted

# Hot Electron Emission

- Photo-emission mechanisms in semiconductors:
  - radiative recombination involving both carrier types (conduction band to valence band)
  - radiative transitions which involve one type of carrier (conduction/valence band to conduction/valence band)
- High electric field
  - accelerated free electrons -> hole electron pairs formation -> increased current -> avalanche breakdown



# Top Transient Current Technique (Top-TCT)

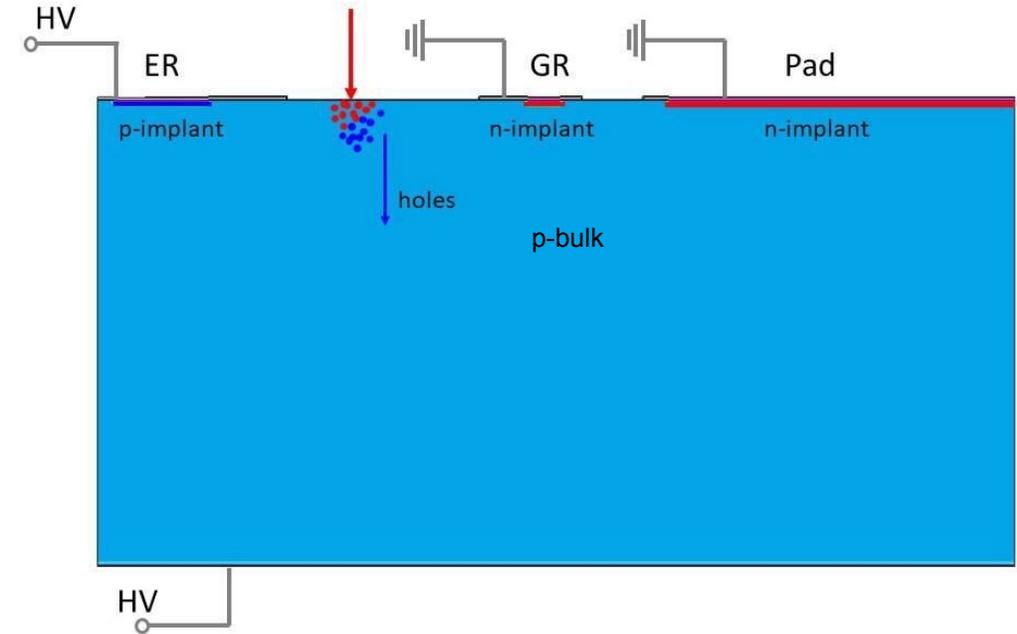
- Employ Top-TCT to investigate EF near GR region
- Method: red laser illumination ( $\lambda = 660 \text{ nm}$ ) from top
- Measure: transient current generated by the electron-hole pairs drift:

$$I_{e,h}(t) = A e_0 N_{e,h} \vec{v}_{e,h}(t) E_w(y) = \text{const } \vec{v}_{e,h} E_w(y)$$

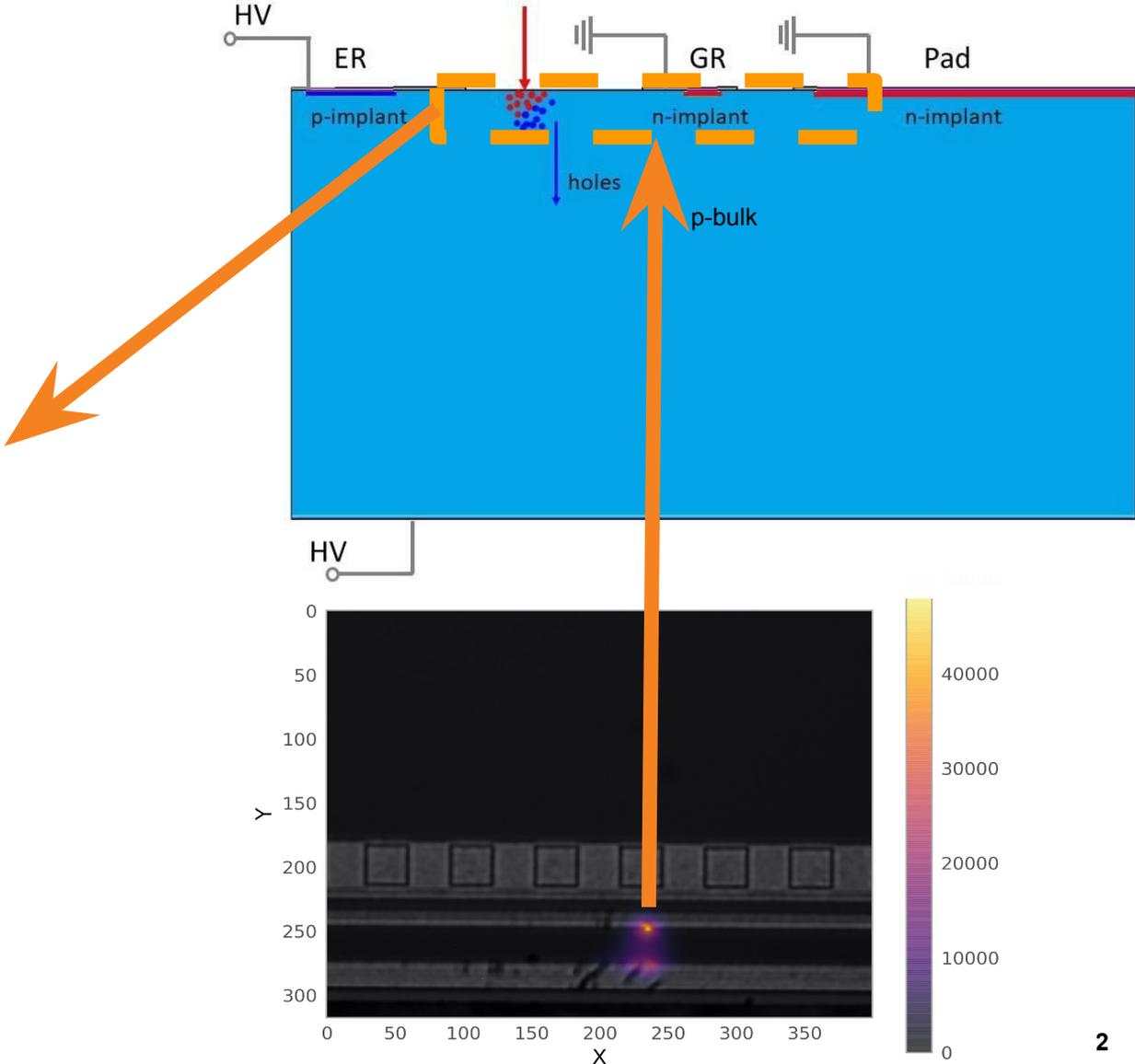
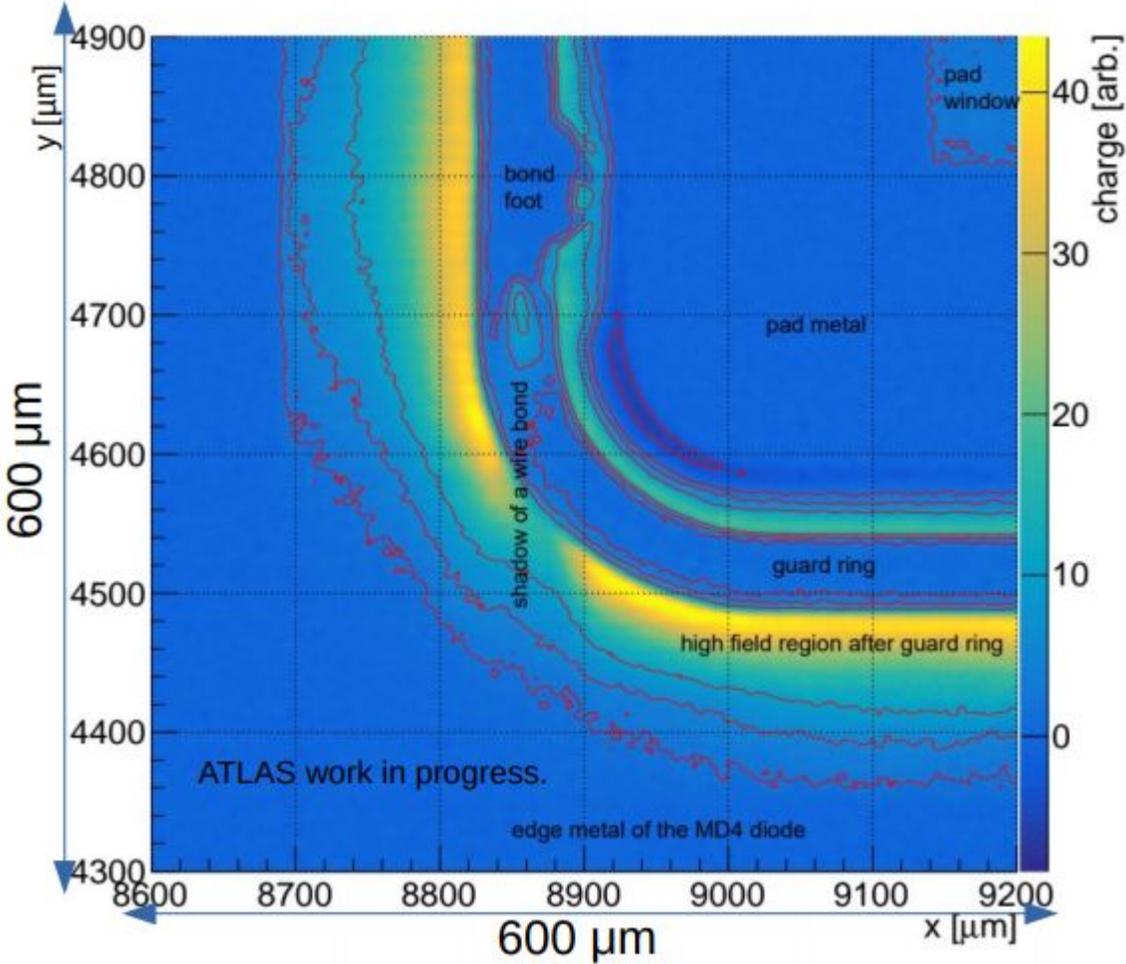
- The amplitude of the current after charge formation can be calculated with:

$$I(y, t \sim 0) = \text{const } \mu_{e,h} E_w(y) E(y)$$

- $e_0$  = elementary charge
- $N_{e,h}$  = number of created e-h pairs
- $v_{e,h}$  = drift velocity
- $\mu_{e,h}$  = mobility
- $E_w(y)$  = weighting field

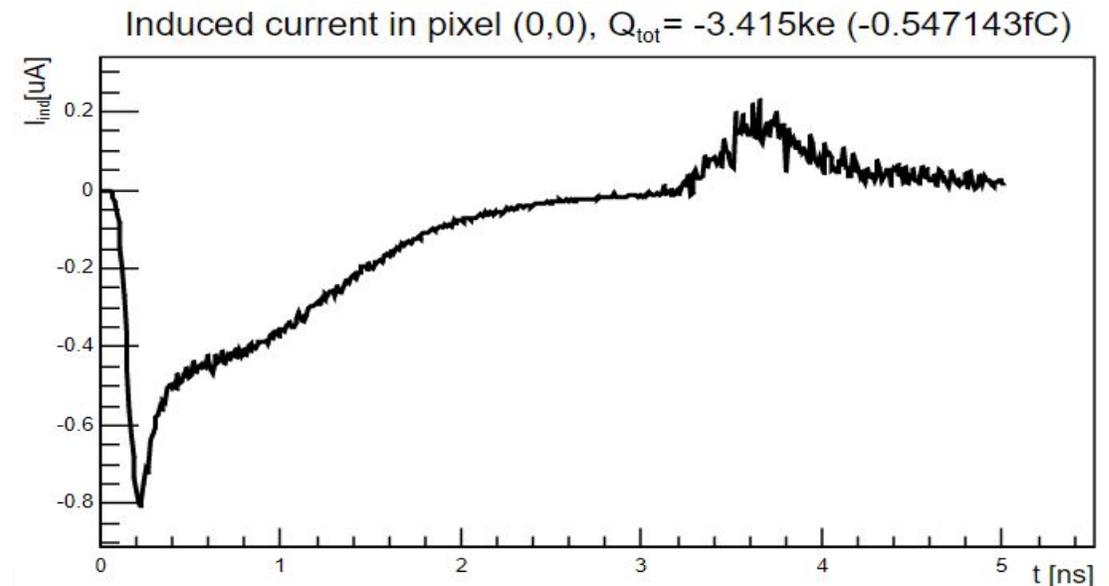
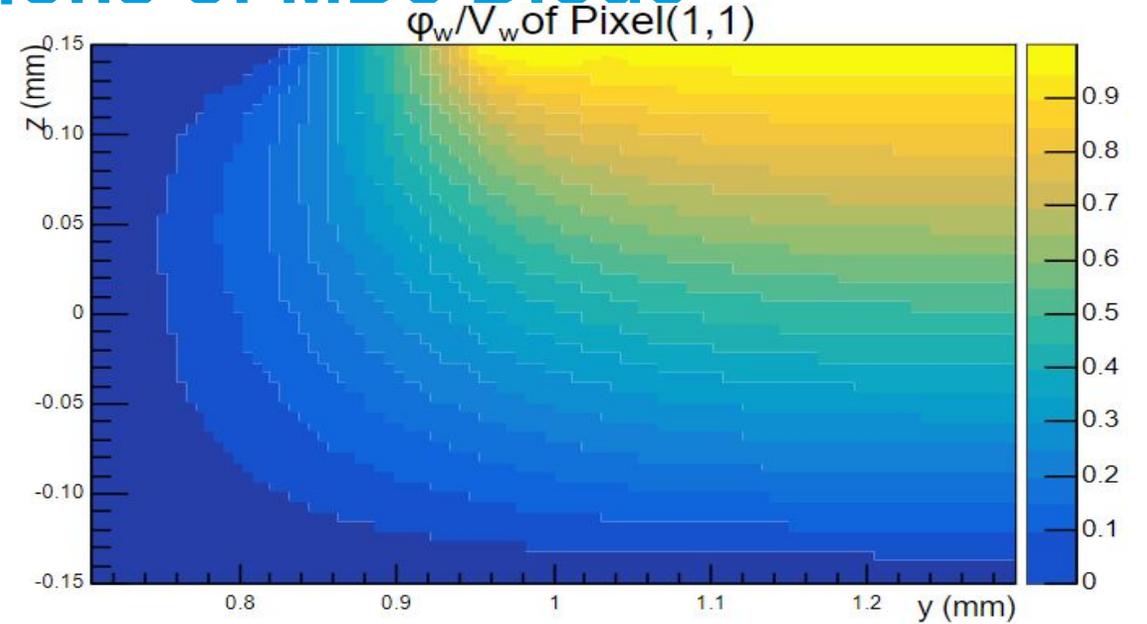
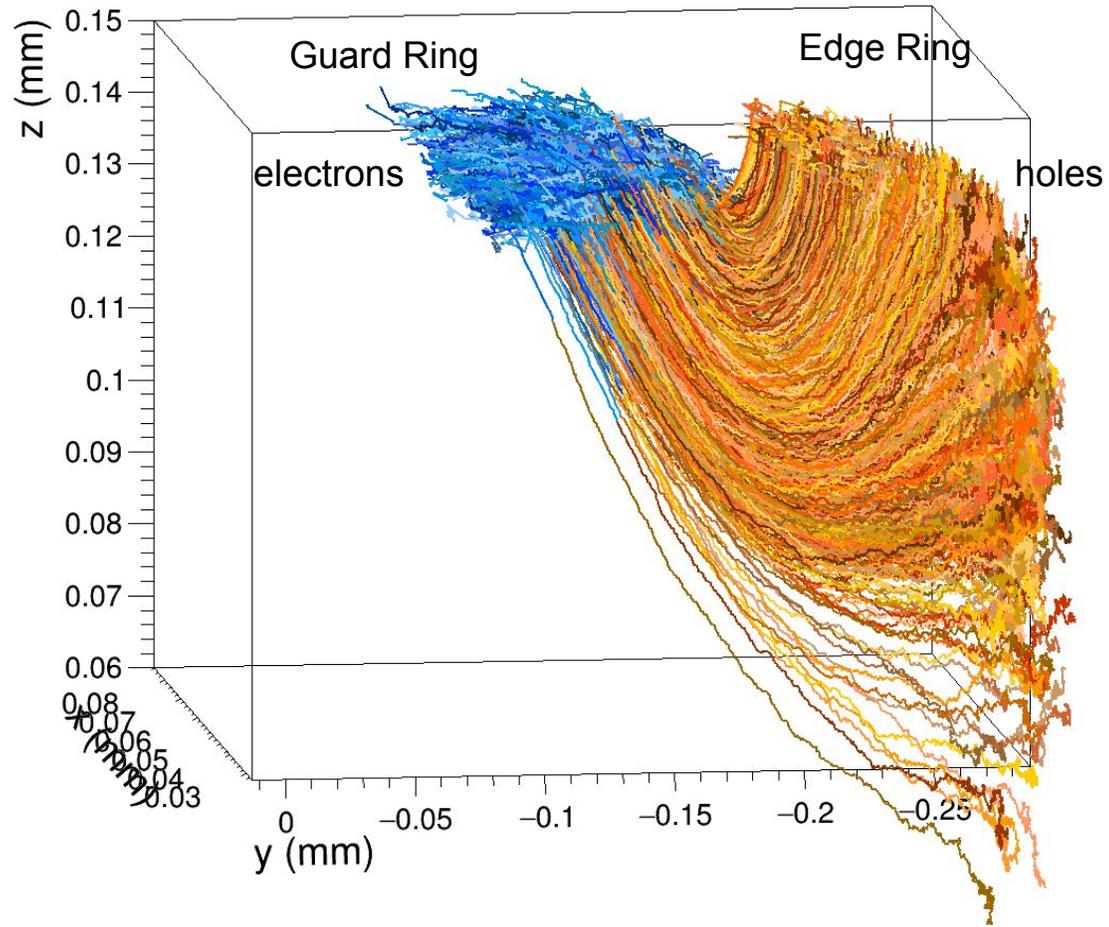


# Top Transient Current Technique (Top-TCT)



- - 500 V applied on the back
- Temperature and RH at room

# Allpix Squared Top-TCT Simulations of MD8 Diode



# Outlook

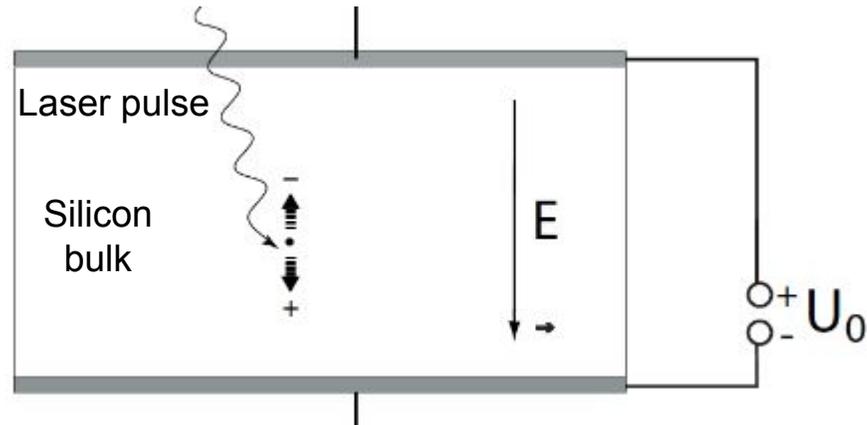
- Can image hot electron emission using a near-IR camera
- TCAD simulation seems to estimate hot carrier emission and TCT measurements in first comparison
- Allpix Squared simulations provide comparable results to experiment
- Next steps:
  - Take multiple pictures over a long period of time to study the evolution of the hot electron emission
  - Systematically compare TCAD results with lab measurements
  - Try to estimate Electric Field from Top-TCT measurements
  - Have a systematic study for several sensors at different humidity values

# Thank you!

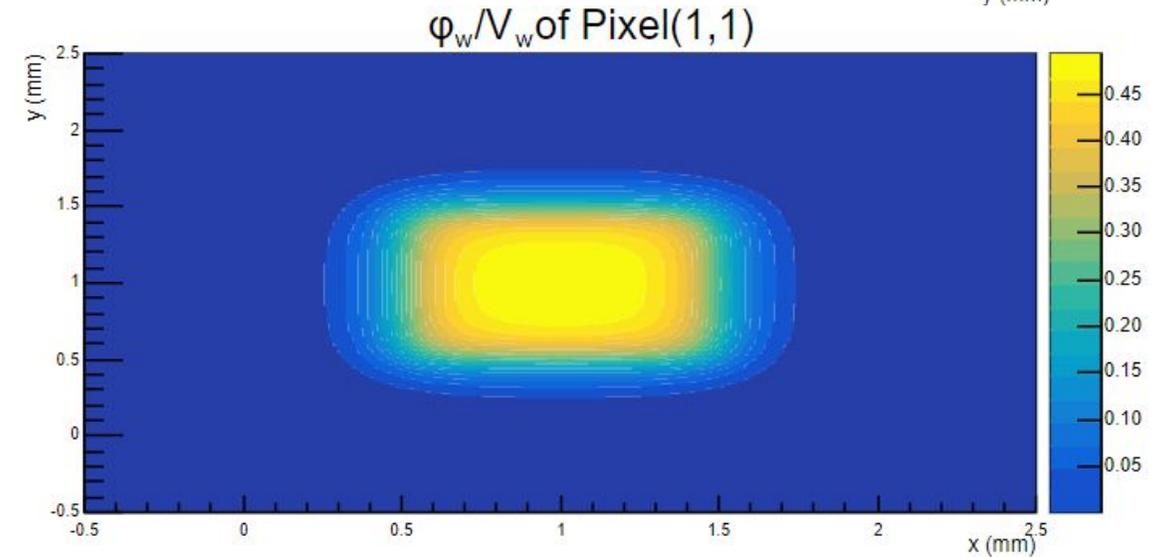
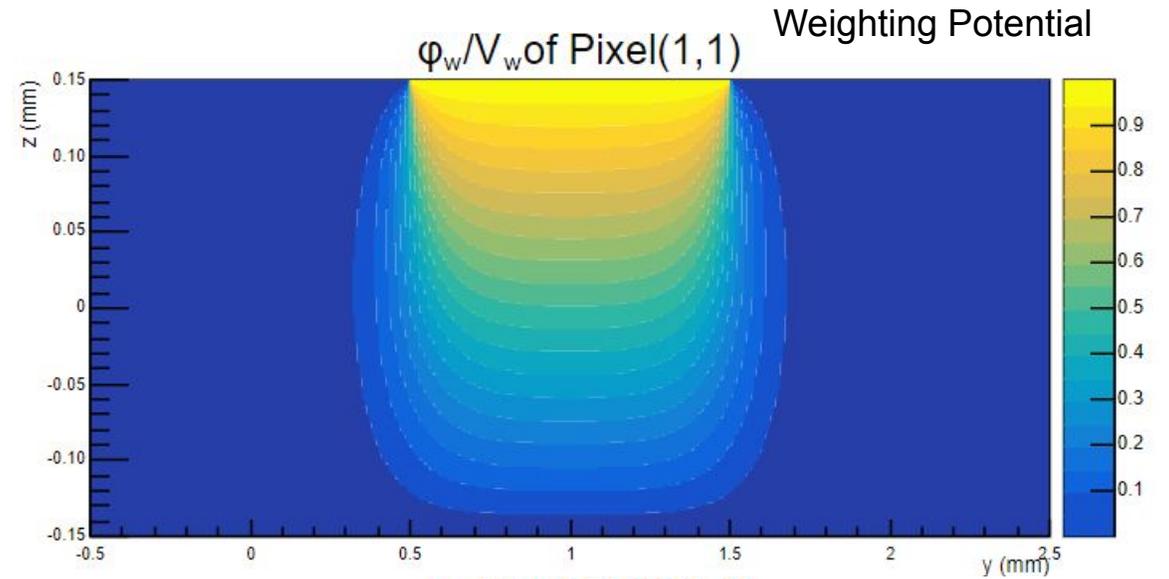
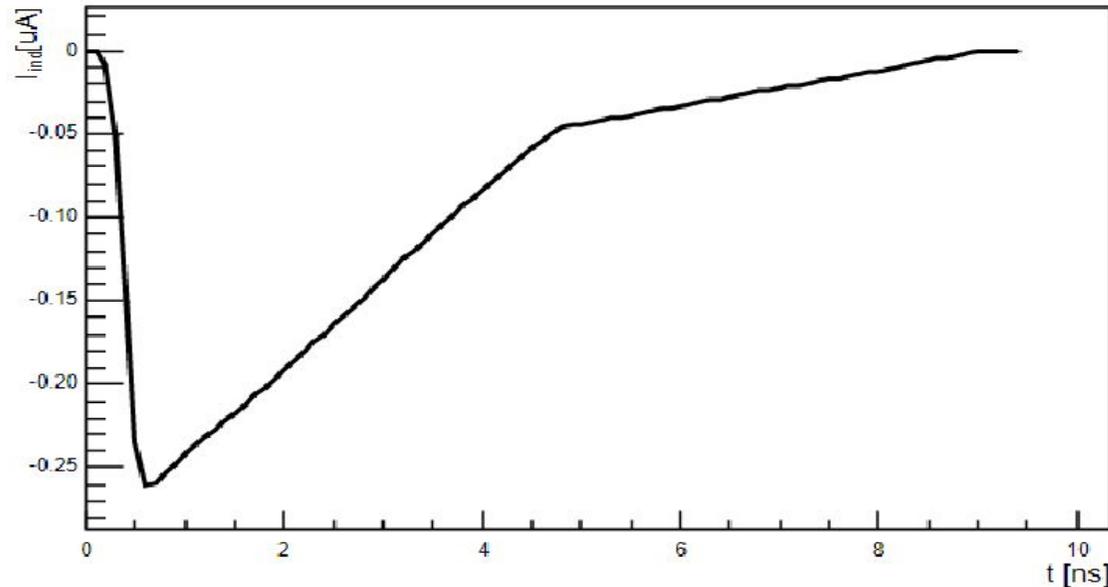
# Back up

# Allpix Squared Top-TCT Simulations of a Simple Diode

Simple diode geometry



Induced current in pixel (0,0),  $Q_{\text{tot}} = -4.99\text{ke}$  ( $-0.799486\text{fC}$ )



## Contact

**DESY.**

Deutsches Elektronen-Synchrotron

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