

The DAMC-UNIZUP processing board for the new MTCA.4 BPM system for PETRA IV

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I-Tech and DESY have developed a MTCA.4 BPM electronics prototype for Petra IV TDR, which consists of the BPM-optimized signal conditioning RTM module from I-Tech, and the DAMC-FMC2ZUP AMC processing board from DESY MSK.

While the DAMC-FMC2ZUP board proved to be invaluable for the prototype phase, it is an overkill in terms of the FPGA resources required for the BPM application. In light of the cost optimization of the final ~800 BPM series for Petra IV, a new DAMC-UNIZUP universal processing AMC board has been developed by DESY MSK, featuring a range of smaller Zynq Ultrascale+ SoC, D1.2 and/or D1.3 Zone3 standard, as well as full bandwidth availability for the LVDS lines to/from the Zone3 connector. The new board features are discussed in light of the current BPM, as well as future potential applications.

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