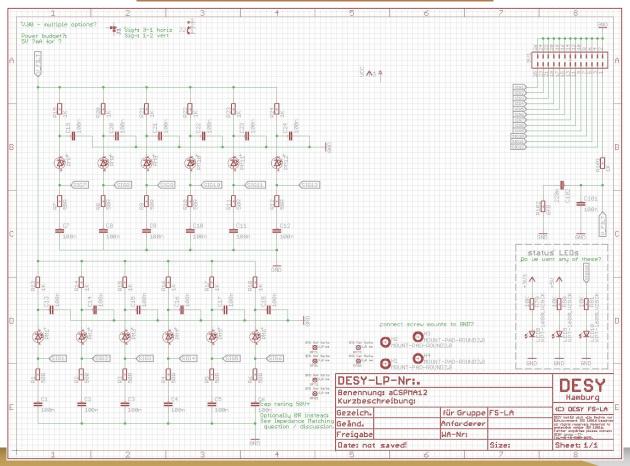
aCSPMA12 PCB design

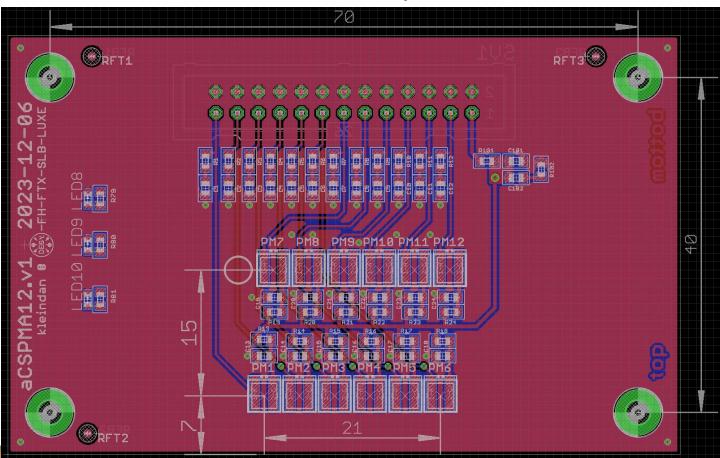
(assembly/)a Cherenkov Silicon PhotoMultiplier Array Printed Circuit Board design review

Daniel Klein - 07/12/2023

electrical Schematic



Board layout



Topics encountered

Noise filtering - in series or parallel? Measurements / Experimenting with filters

Impedance matching

Readout line biasing

Geometry choices

Why do we do this? Cherenkov detectors

The importance of scientific communication