

Real-time ML event classification with FPGAs at the LHC

Friday 8 December 2023 14:36 (12 minutes)

Particle colliders such as the LHC produce data at an unprecedented rate and volume. To overcome bandwidth constraints, event filtering systems are employed, with the first stage usually implemented in hardware using FPGAs. We present the first hardware demonstration of a real-time event filtering algorithm using machine learning for the Level-1 Trigger of the CMS experiment.

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Session Classification: Session III