



Performance of CMS Inner Tracker pixel assemblies for the Phase-2 Upgrade

B. Raciti | SiDet R&D Meeting – 26th Mar. 2024, DESY

The **High-Luminosity Large Hadron Collider** (HL-LHC) is an upgraded version of the LHC^[1], foreseen to begin in 2028 onwards.

In the HL-LHC phase:

Luminosity: $7.5 \times 10^{34} \text{ cm}^{-2} \text{ s}^{-1}$

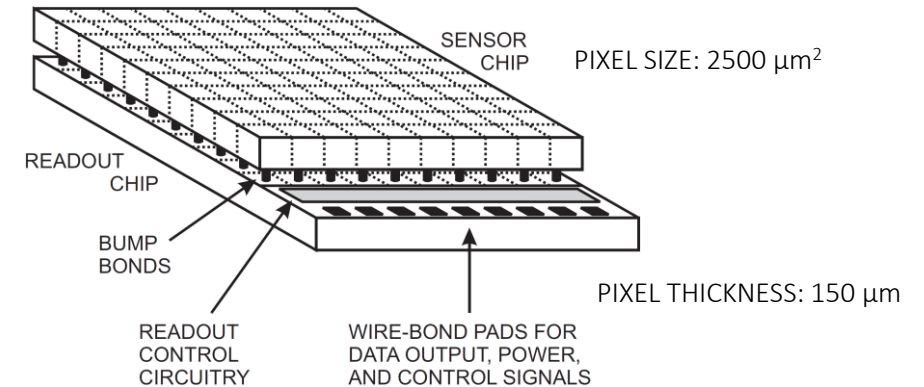
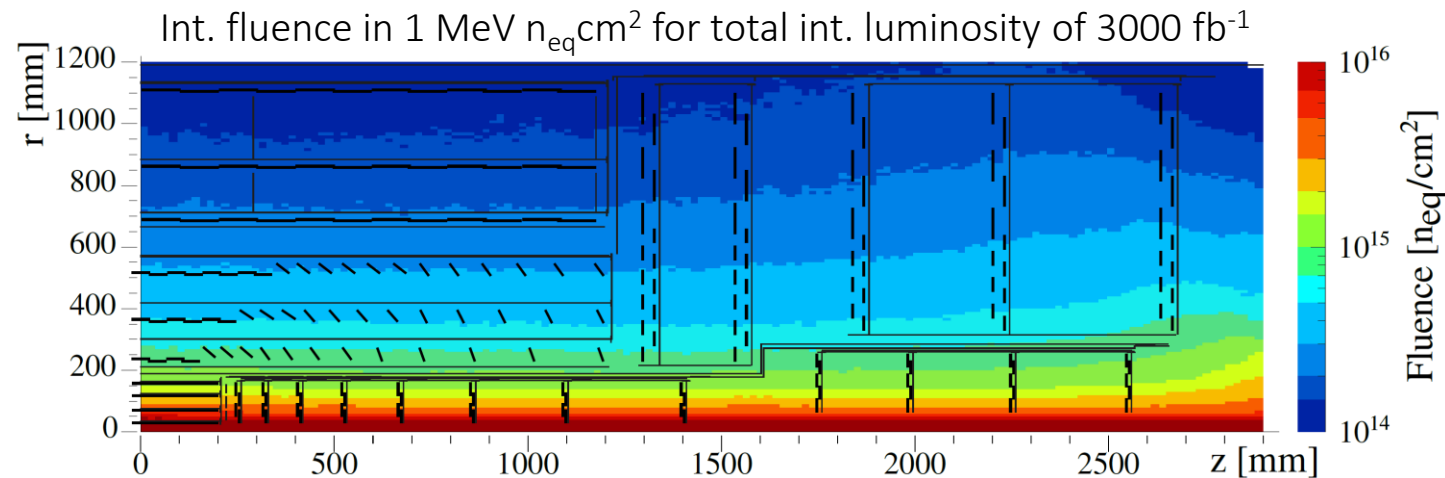
Integrated luminosity: 3000 – 4000 fb⁻¹

Pile-up: $\langle \mu \rangle = 200$

ϕ_{max} planar modules = $1 \times 10^{16} \text{ n}_{\text{eq}} \text{ cm}^{-2}$

During the **Long-Shutdown 3**:

- **replacement** of CMS Outer (OT) and Inner Tracker (IT)
- IT pixel sensors: new **readout chip** (RD53C_CMS) and **sensor** design



Stringent requirements for beginning → end of operation

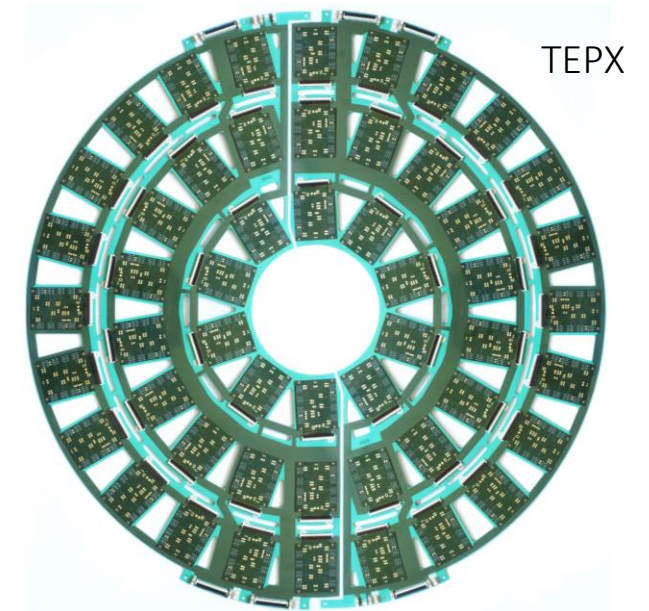
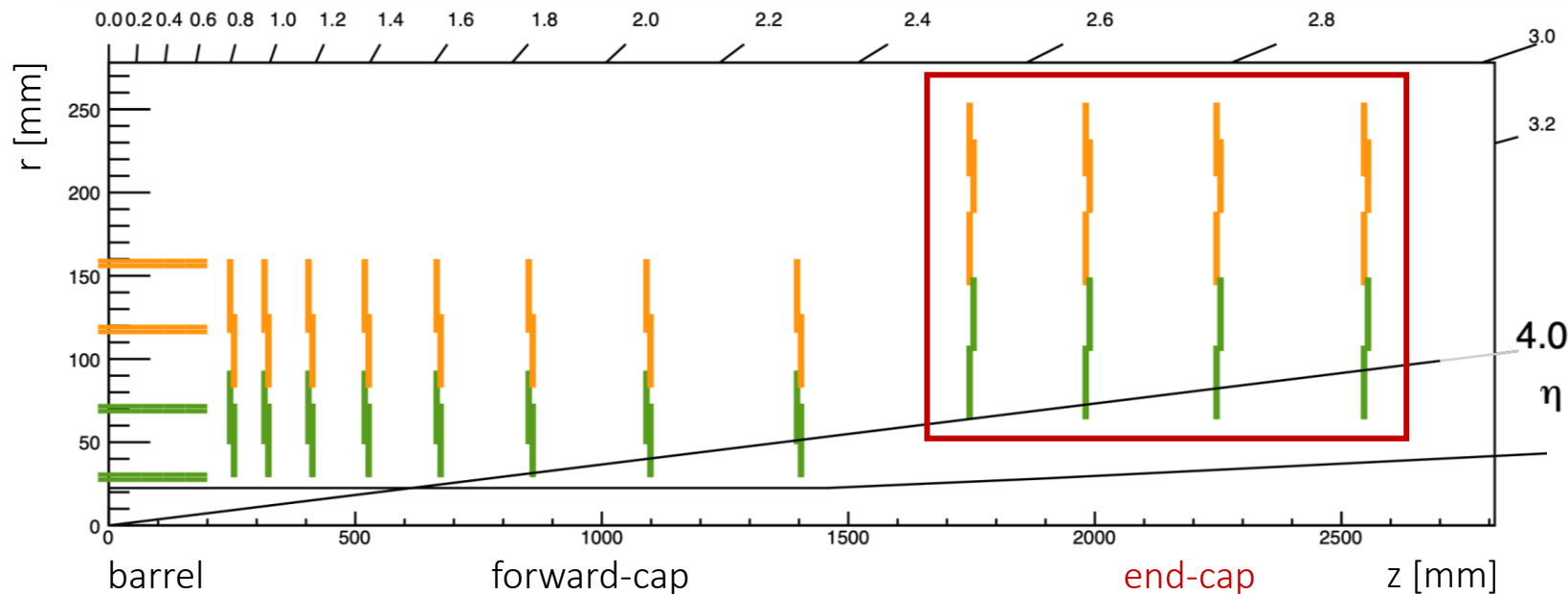
[1] <https://voisins.web.cern.ch/en/high-luminosity-lhc-hl-lhc>

The Inner Tracker is divided into three sections: **barrel**, **forward-cap** and **end-cap**.

In total, it will feature:

- 1156 **1x2** pixel modules → 2 readout-chips per module
- 2736 **2x2** pixel modules → 4 readout-chips per module

500 **TEPX 2x2** modules will be glued, wire-bonded and tested in Hamburg.

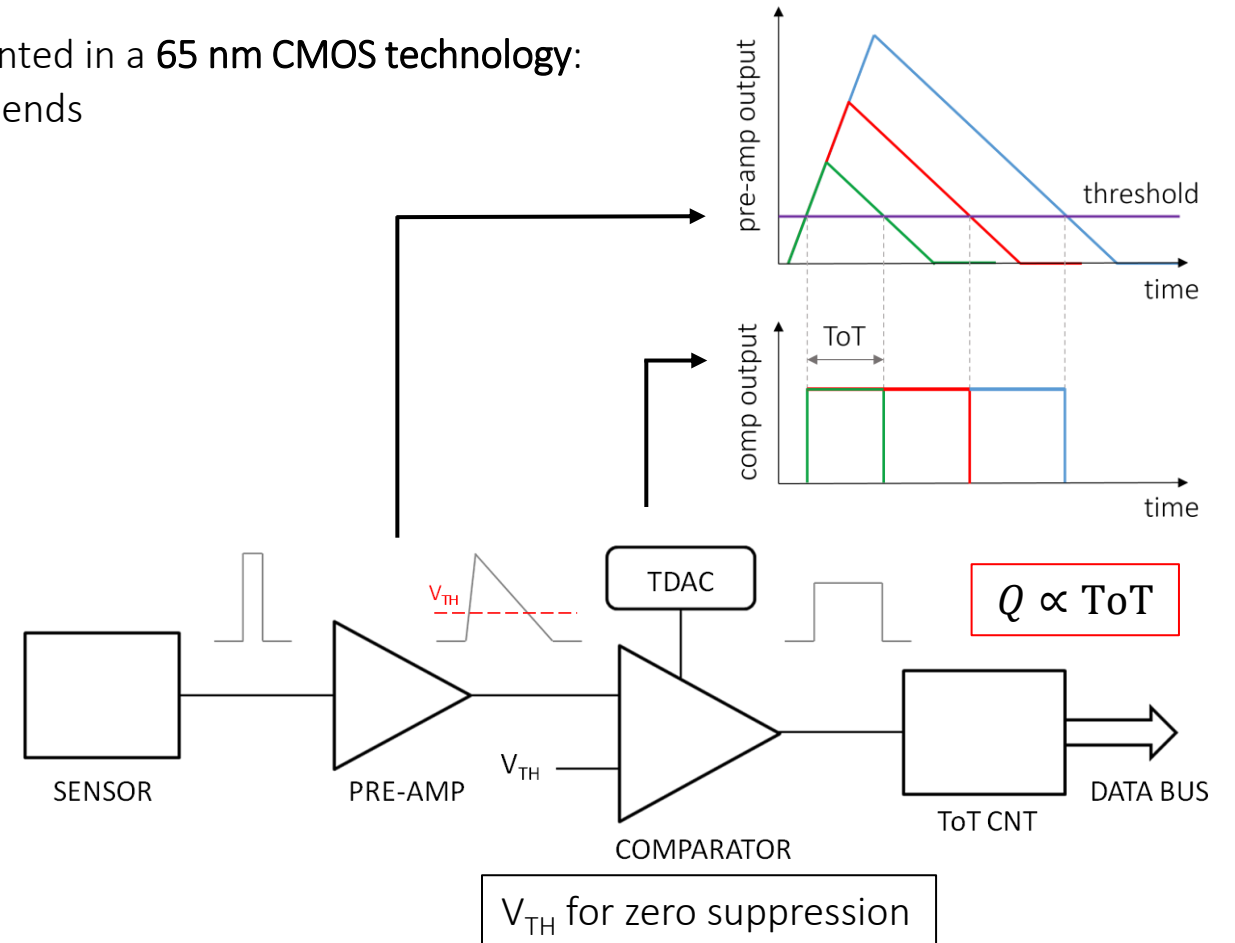
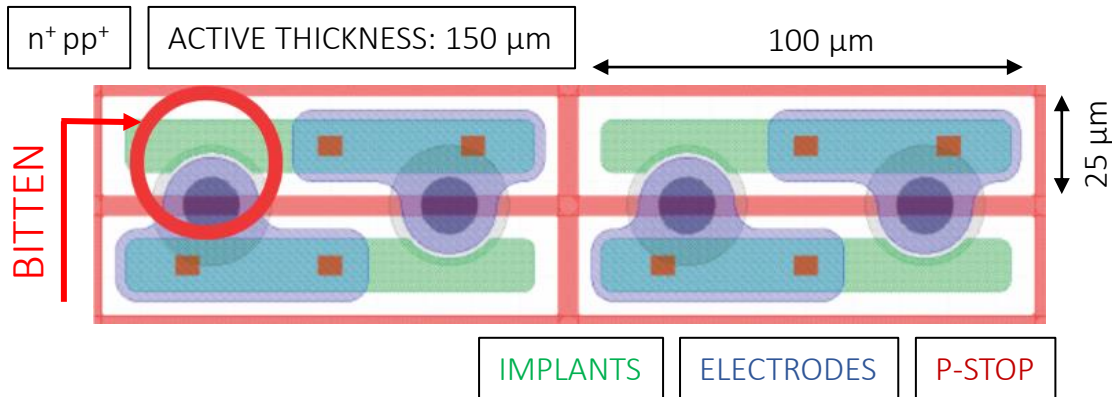


In hybrid pixel detectors the **sensor** and the **readout chip** are individually optimized and joined with bump-bonds.

Readout chip developed by the **RD53 Collaboration** and is implemented in a **65 nm CMOS technology**:

- RD53A → half-size pixel chip demonstrator with 3 different front-ends
- RD53B_CMS → full-size prototype chip
- RD53C_CMS → production chip

CMS chose the **linear front-end** design for the readout chip.



DESY test beam facility: e^-/e^+ beam with energies ranging from 1 to 6 GeV (data taken at 5.2 GeV)

Observables:

$$\varepsilon_{\text{hit}} = \frac{N_{\text{tracks}}^{\text{DUT}}}{N_{\text{tracks}}^{\text{total}}}$$

$$\alpha = 1 - \frac{N_{\text{pxl}}^{\text{masked}}}{N_{\text{pxl}}^{\text{total}}}$$

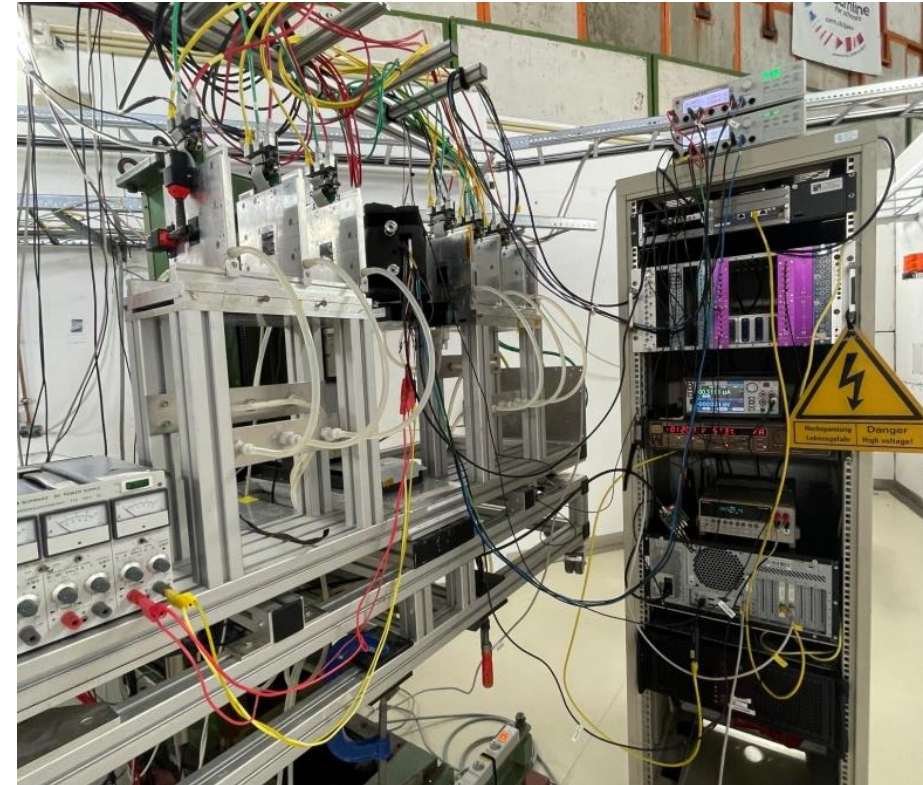
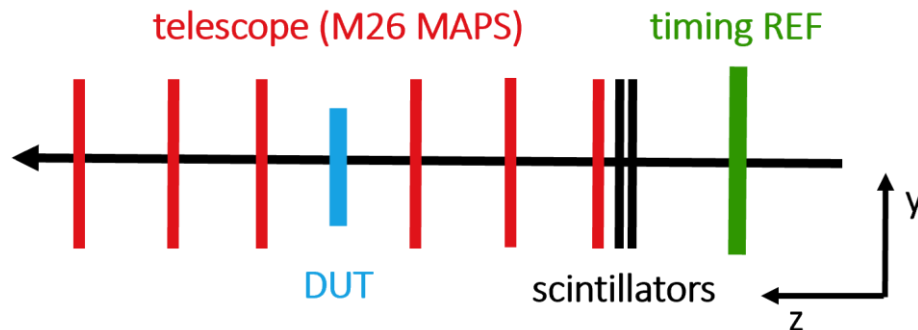
$$\varepsilon_{\text{hit assembly}} = \varepsilon_{\text{hit}} \times \alpha$$

$$\sigma_{\text{hit}} = \sqrt{\text{RMS}_{\text{trunc.}}^2 - \sigma_{\text{tel}}^2}$$

→ as function of angle, temperature, ϕ_{eq} , V_{bias}

Setup:

- **tracking:** six Mimosa26 MAPS
- **trigger:** two upstream scintillators
- **timing:** reference module RD53B_CMS ($50 \times 50 \mu\text{m}^2$)
- DUT located after the first three planes



Two modules irradiated at CERN PS → non-uniform fluence across columns

- Fluence range: $\phi_{\text{eq}} = 3 \times 10^{15} \text{ n}_{\text{eq}}\text{cm}^{-2} \rightarrow 1 \times 10^{16} \text{ n}_{\text{eq}}\text{cm}^{-2}$

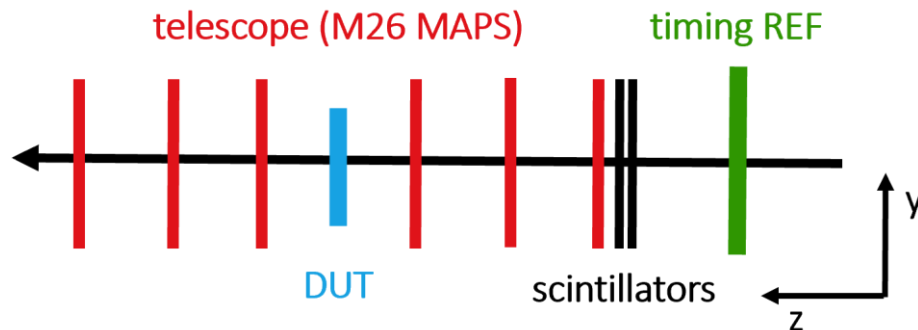
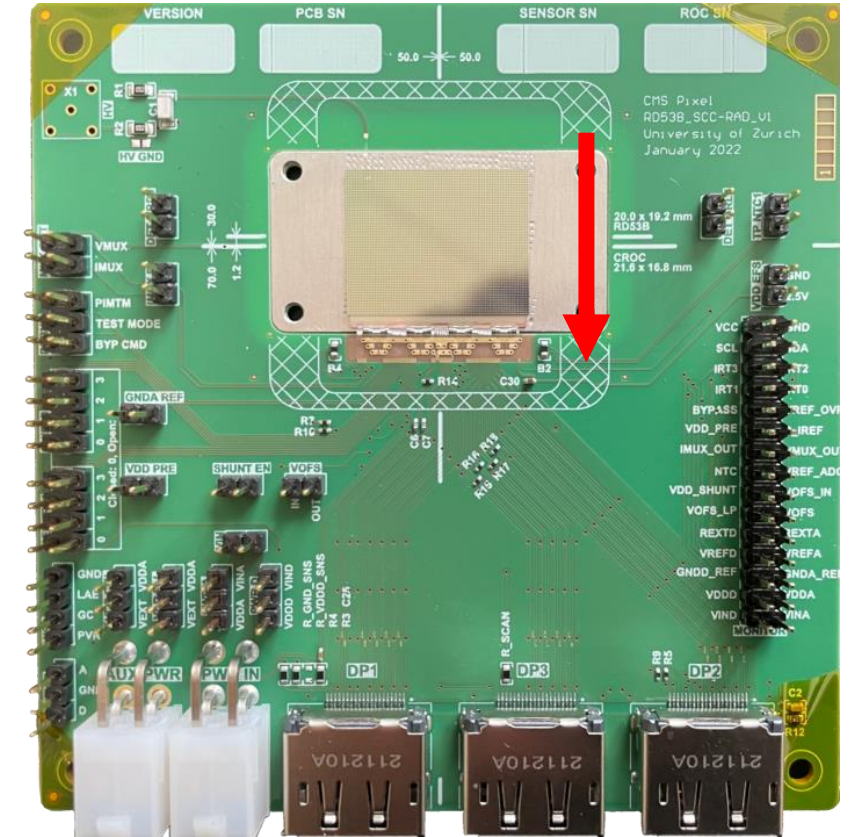
Aim: study ε_{hit} , α , $\varepsilon_{\text{hit}} \times \alpha$ and σ_{hit} as a function of the equivalent fluence ϕ_{eq}

Types of measurements:

- **bias scan**: fixed angle wrt. beam, threshold | change **bias voltage**
- **angle scan**: fixed bias voltage, threshold | change **angle** wrt. beam
- **threshold scan**: fixed angle wrt. beam, bias voltage | change **threshold**

The software (*Corryvreckan*^[1]) used for the analysis of test beam data:

- aligns** all planes and fits particle **tracks** from recorded hits

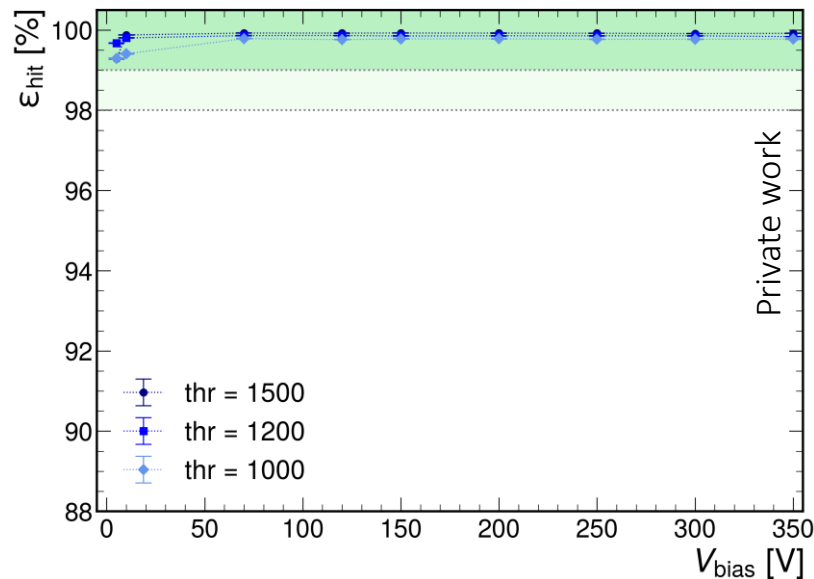


Requirements for the **efficiency of the assembly** at vertical incidence at -20°C for **fresh** modules:

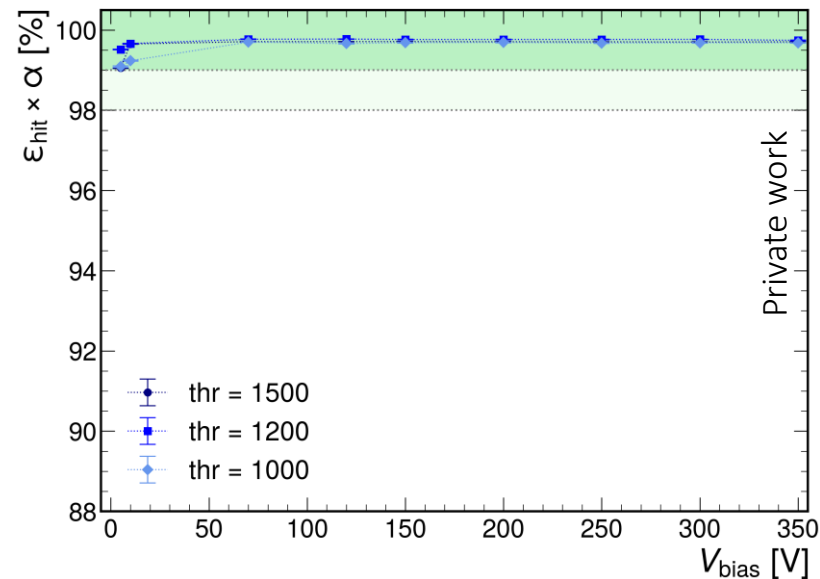
$$\epsilon_{\text{hit}} \times \alpha > 99\% \text{ and } V_{\text{bias}} = V_{\text{depl}} + 50\text{ V} = 120\text{ V}$$

$$< 1\% \text{ masked pixels and avg. noise occupancy } < 10^{-6}$$

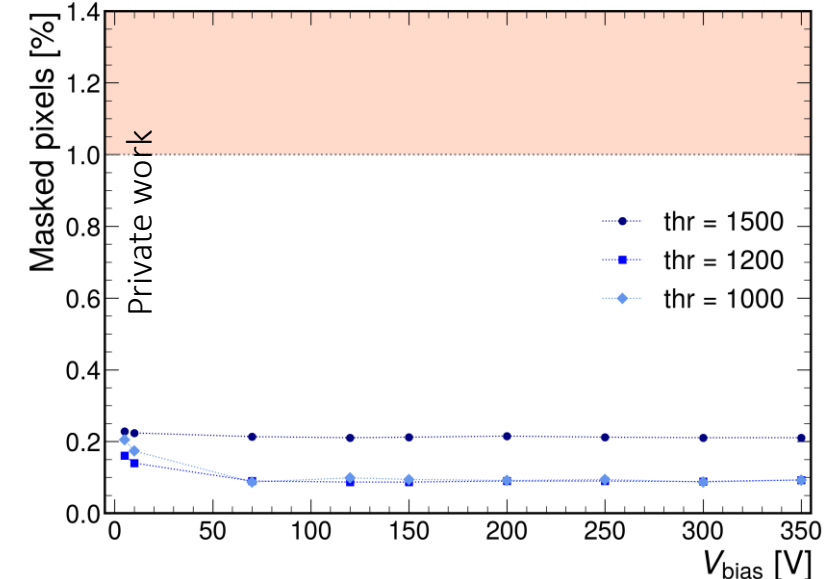
un-irradiated module



un-irradiated module



un-irradiated module



Before irradiation:

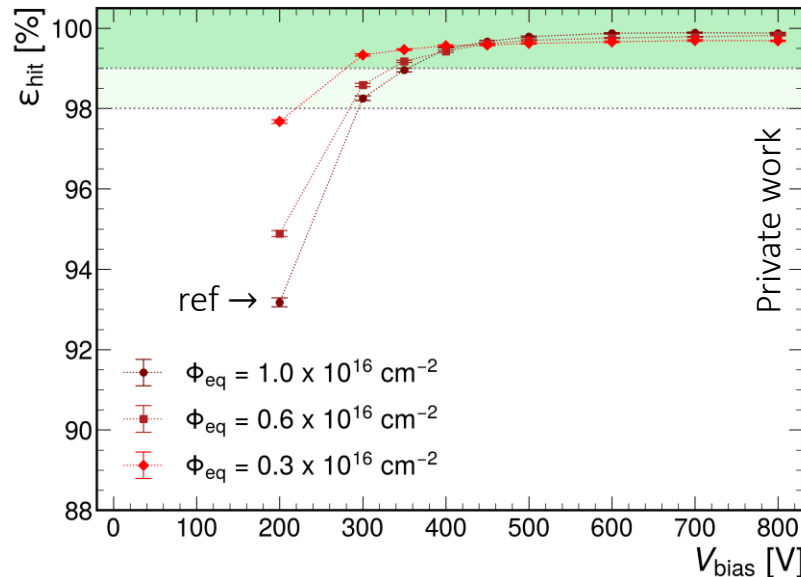
- $\epsilon_{\text{hit}} \times \alpha > 99\%$ for $V_{\text{bias}} \geq 0\text{ V}$ for all thresholds and met requirement for masked pixels

Requirements for the **efficiency of the assembly** at vertical incidence at -20°C for **irradiated** modules:

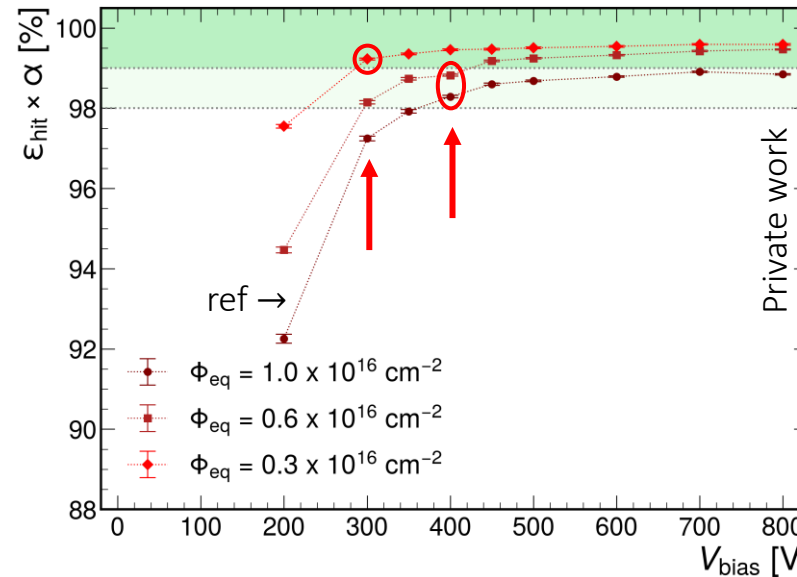
$$\phi_{\text{ref}} = 5 \times 10^{15} \text{ cm}^{-2} \rightarrow \begin{cases} \Phi_{\text{eq}} < \Phi_{\text{ref}} & \epsilon \times \alpha > 99\% \text{ and } V_{\text{bias}} \leq 600 \text{ V} \\ \Phi_{\text{eq}} > \Phi_{\text{ref}} & \epsilon \times \alpha > 98\% \text{ and } V_{\text{bias}} \leq 800 \text{ V} \end{cases}$$

$< 1\%$ masked pixels and avg. noise occupancy $< 10^{-6}$

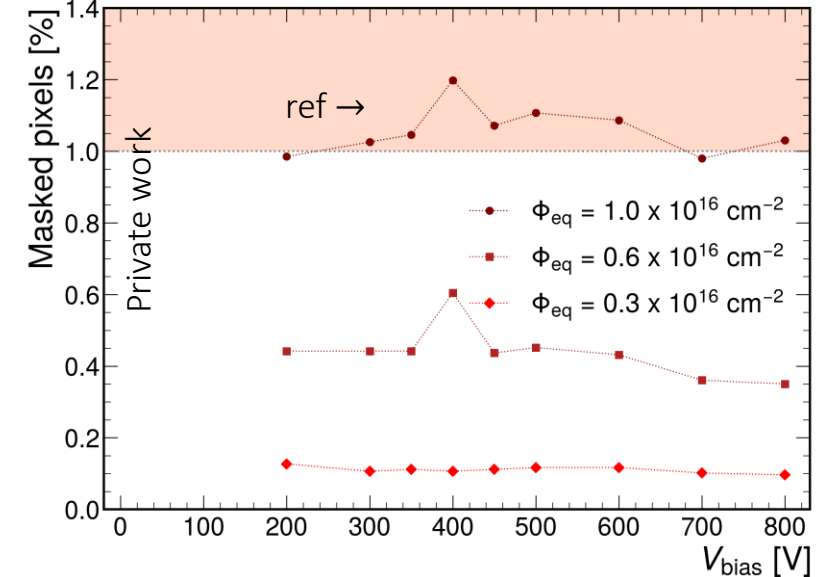
irradiated module



irradiated module



irradiated module



After irradiation:

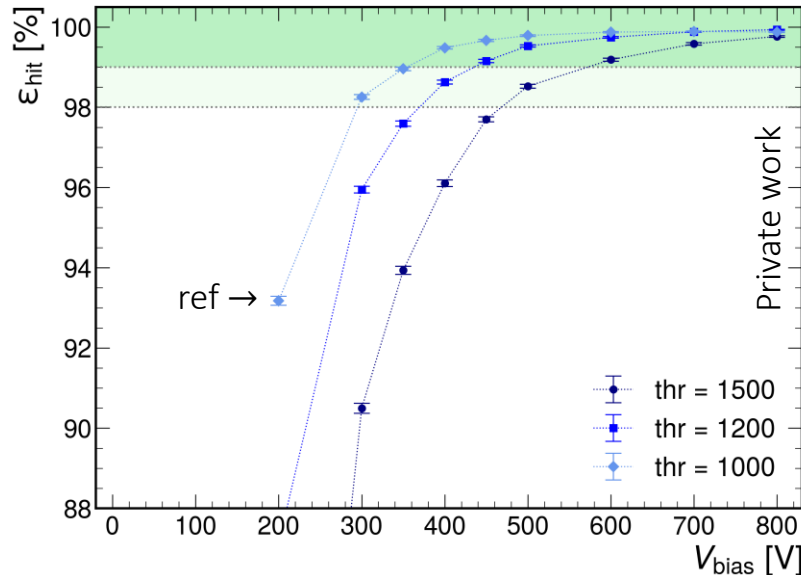
- plots show a section of the module tuned to a threshold = 1000 e-
- $\epsilon_{\text{hit}} \times \alpha > 99\%$ for $V_{\text{bias}} \geq 300 \text{ V}$ and $\epsilon_{\text{hit}} \times \alpha > 98\%$ for $V_{\text{bias}} \geq 400 \text{ V}$ and met requirement for masked pixels for $\phi_{\text{eq}} < 1 \times 10^{16} \text{ n}_{\text{eq}} \text{ cm}^{-2}$

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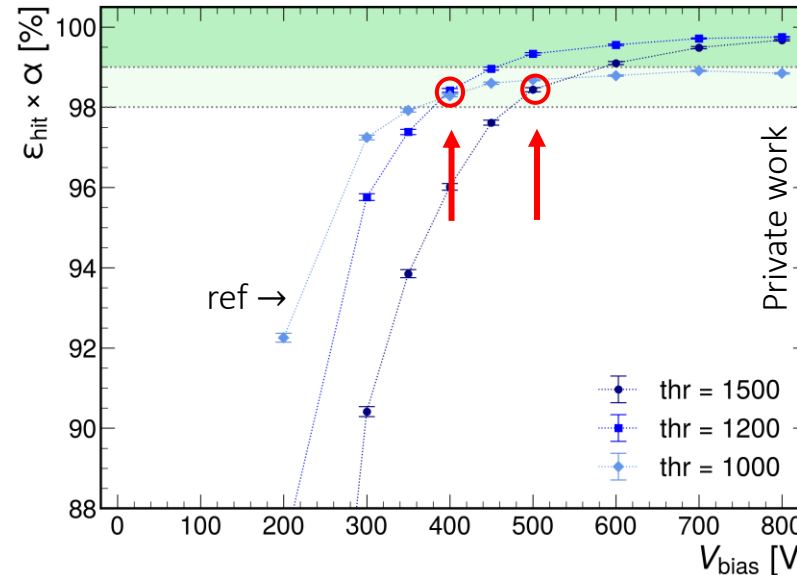
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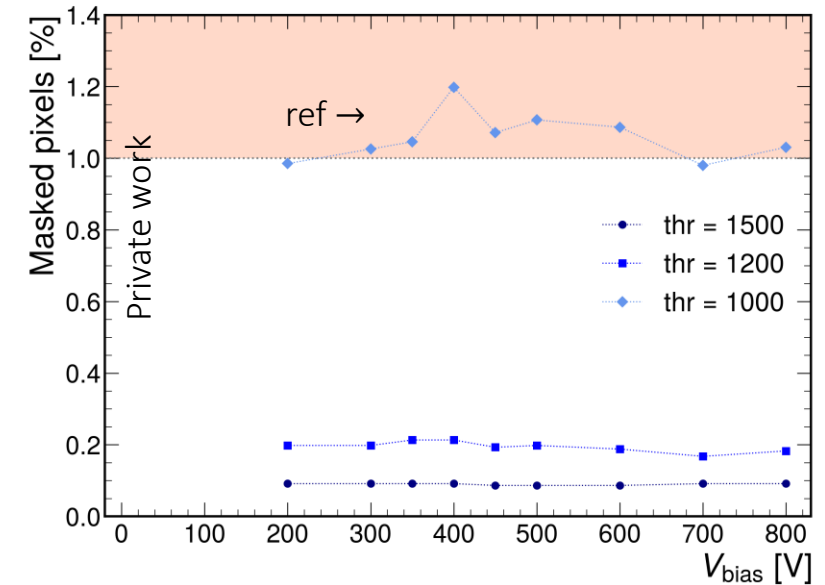
irradiated module



irradiated module



irradiated module



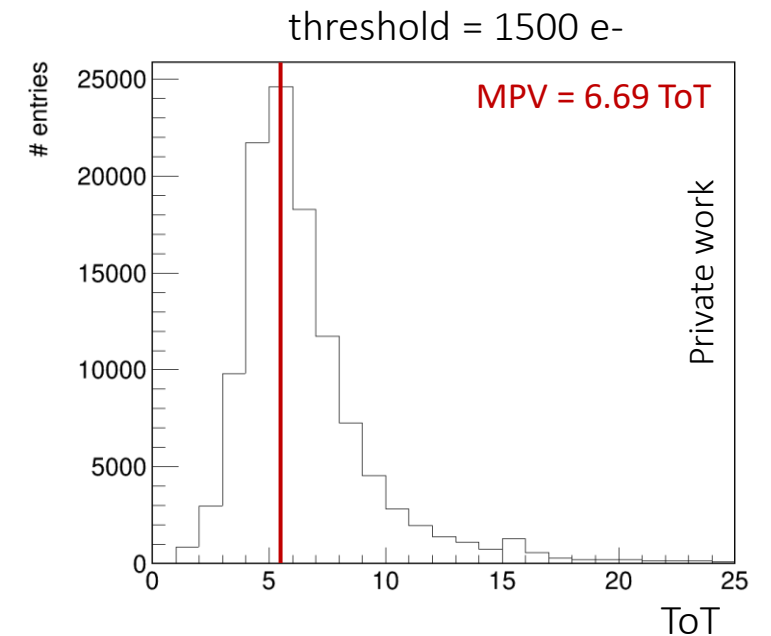
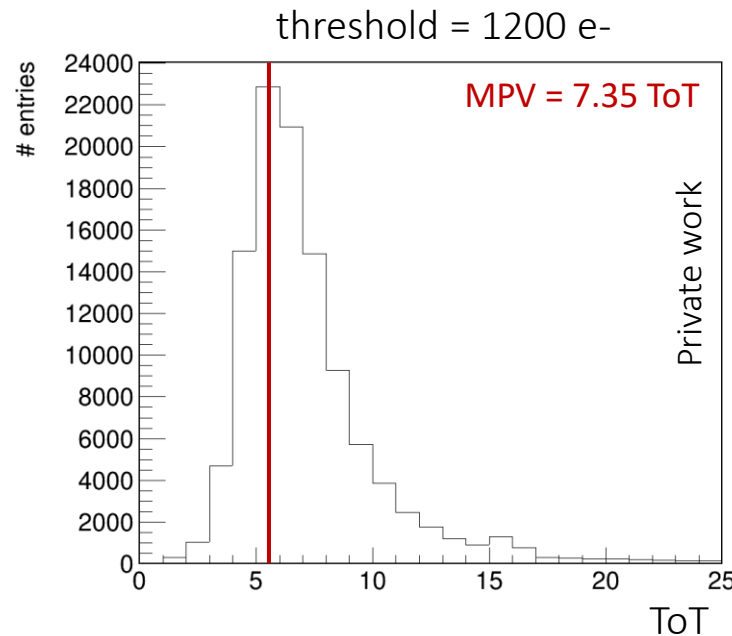
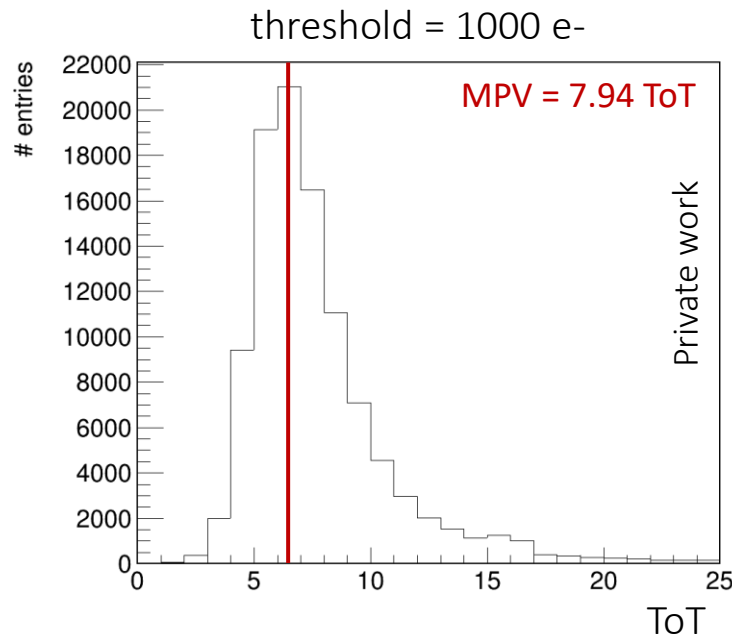
After irradiation:

- plots show a section of the module irradiated to $\phi_{\text{eq}} = 1 \times 10^{16} \text{ n}_{\text{eq}} \text{ cm}^{-2}$
- $\varepsilon_{\text{hit}} \times \alpha > 98\%$ for $V_{\text{bias}} \geq 400 \text{ V}$ (thresholds = 1000 e-, 1200 e-) and $V_{\text{bias}} \geq 500 \text{ V}$ (threshold = 1500 e-)

Requirements for the **efficiency of the assembly** at vertical incidence at -20°C for **irradiated** modules:

$$\phi_{\text{ref}} = 5 \times 10^{15} \text{ cm}^{-2} \rightarrow \begin{cases} \Phi_{\text{eq}} < \Phi_{\text{ref}} & \varepsilon \times \alpha > 99\% \text{ and } V_{\text{bias}} \leq 600 \text{ V} \\ \Phi_{\text{eq}} > \Phi_{\text{ref}} & \varepsilon \times \alpha > 98\% \text{ and } V_{\text{bias}} \leq 800 \text{ V} \end{cases}$$

$< 1\%$ masked pixels and avg. noise occupancy $< 10^{-6}$



After irradiation:

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Single hit resolution σ_{hit}

Binary limits for σ_{hit} :

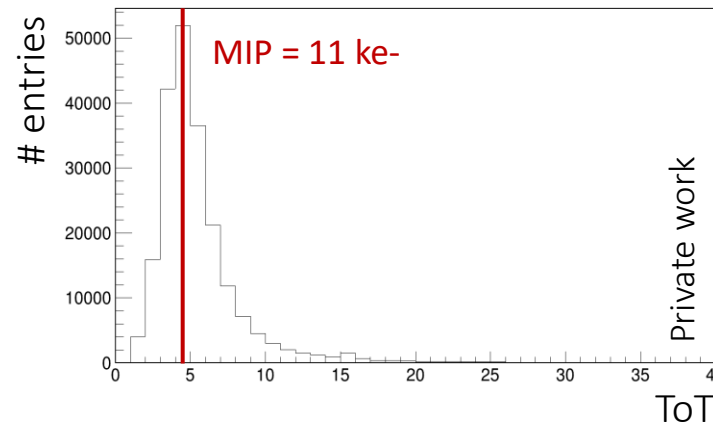
$$\begin{cases} \sigma_{\text{hit}} < 25/\sqrt{12} \sim 7.2 \mu\text{m} \text{ in } 25 \mu\text{m} \\ \sigma_{\text{hit}} < 100/\sqrt{12} \sim 28.9 \mu\text{m} \text{ in } 100 \mu\text{m} \end{cases}$$

$$\sigma_{\text{hit}} = \sqrt{\text{RMS}_{\text{trunc.}}^2 - \sigma_{\text{tel}}^2}$$

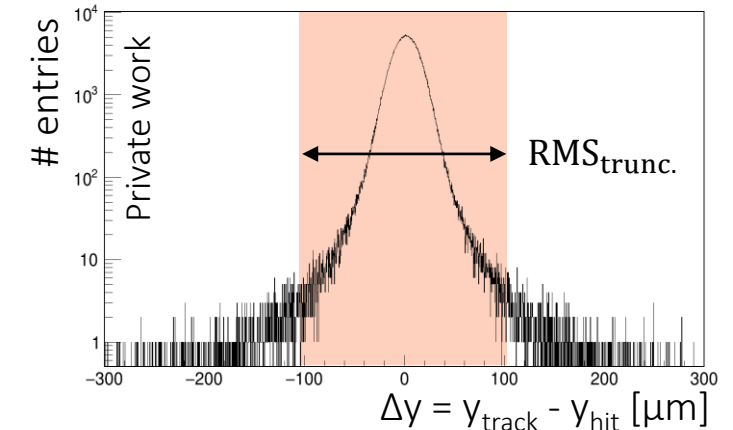
$$X_{\text{hit}} = \sum_i \frac{\text{ToT}_i X_i}{\text{ToT}_{\text{cluster}}}$$

crosstalk < 10%

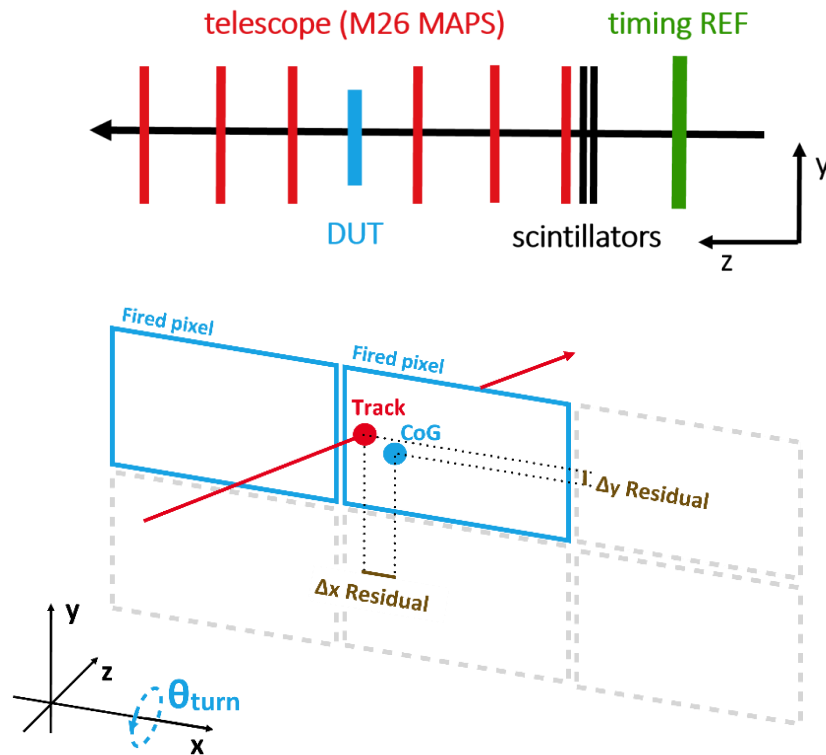
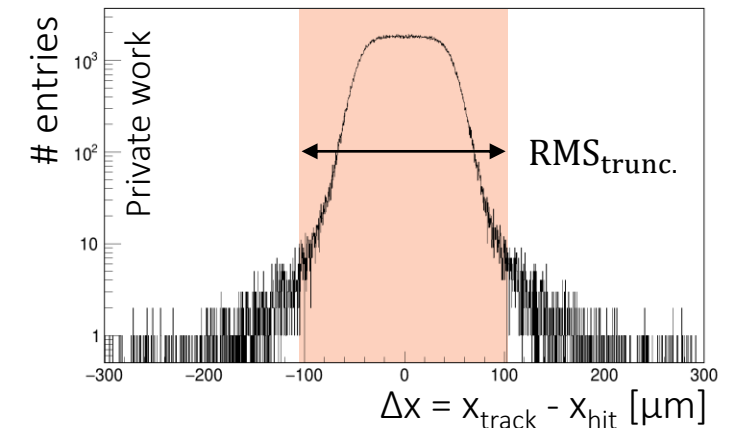
ToT distribution



residual in local y



residual in local x



1 entry in the residuals distribution

Every hit is associated to a $\text{ToT}_{\text{cluster}}$

- sum of individual ToT_i of the cluster
- weighted-mean algorithm $\rightarrow x_{\text{hit}}, y_{\text{hit}}$

Single hit resolution σ_{hit}

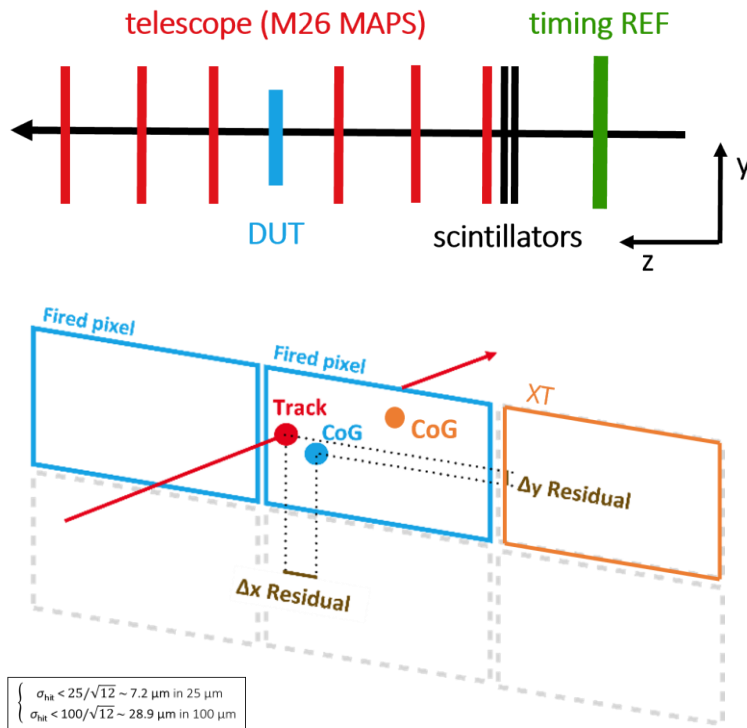
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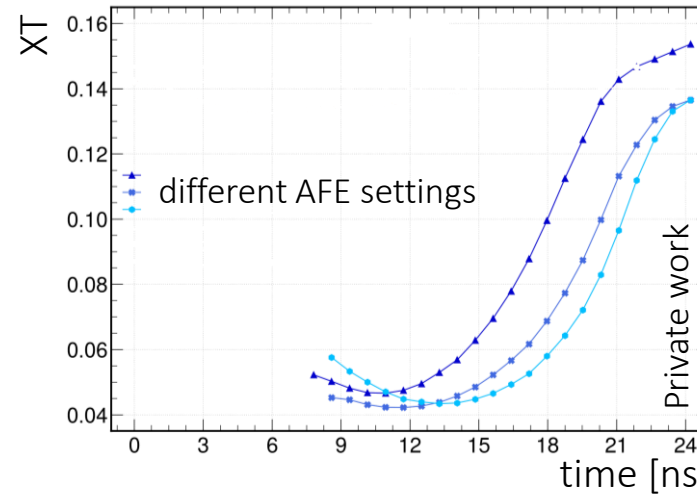
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crosstalk < 10%



1 entry in the residuals distribution

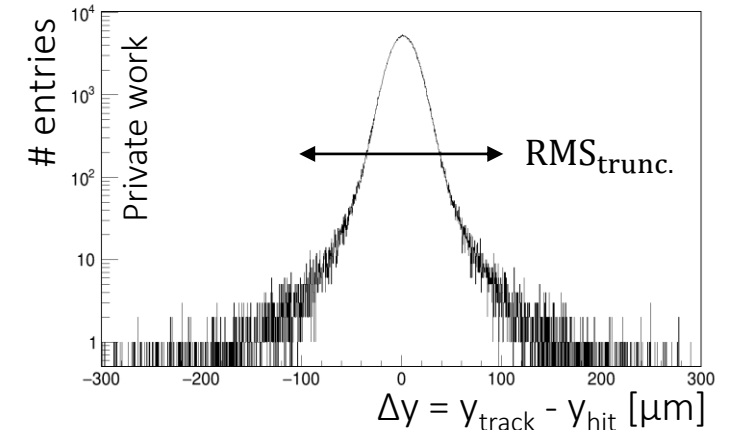
XT for RD53B_CMS



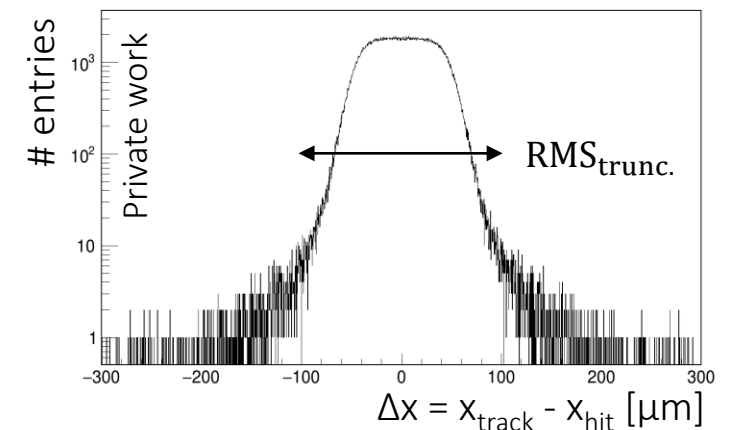
XT measured with **internal injection circuit**:

- broadens residuals distributions
- precise timing of the readout reduces XT levels to ~4%
- synchronous readout at LHC
- ... more in the backup

residual in local y



residual in local x



Single hit resolution σ_{hit}

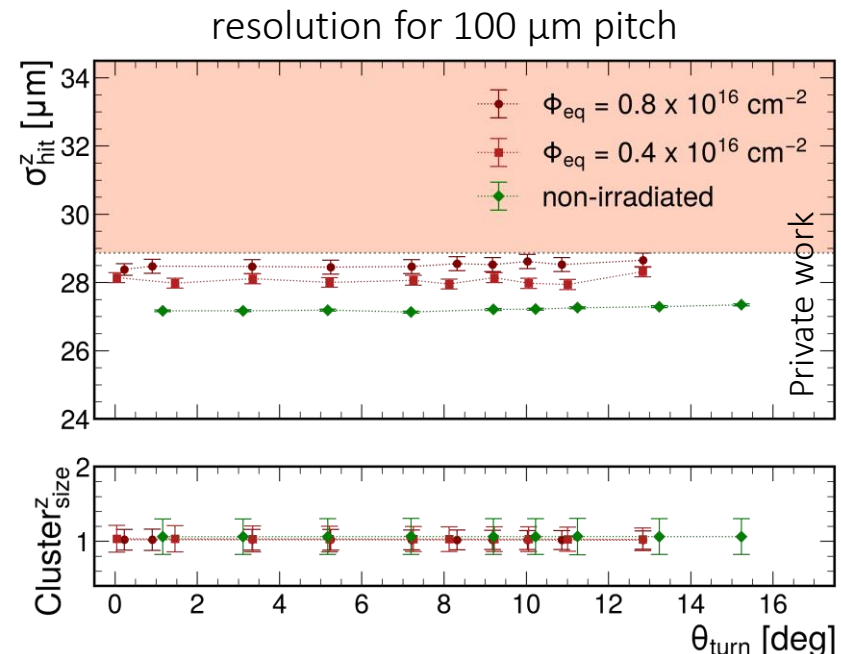
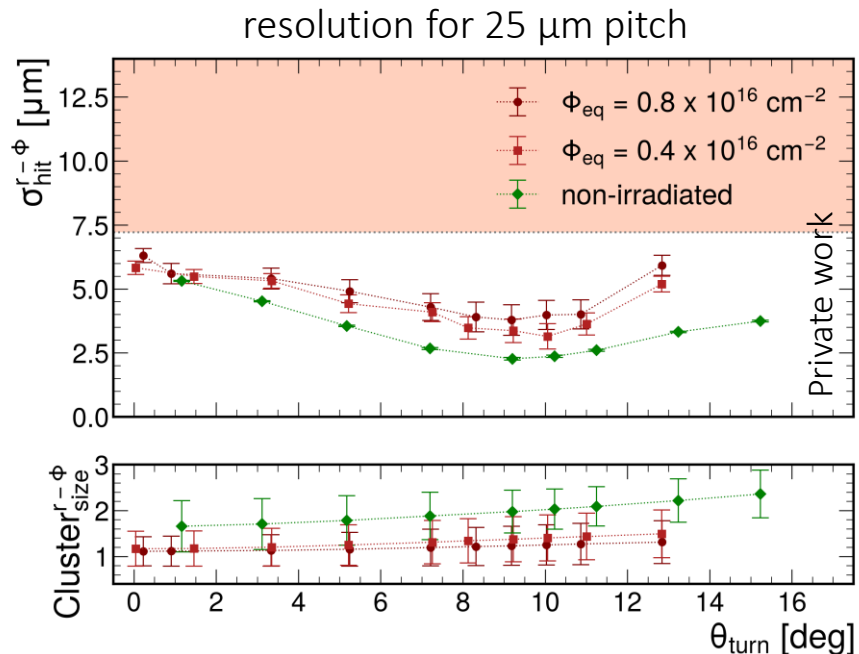
Binary limits for σ_{hit} :

- expected minimum at optimal angle $\rightarrow$ cluster size = 2 and best charge sharing
- 25 μm pitch corresponds to r - ϕ direction and 100 μm pitch to z

$$\begin{cases} \sigma_{\text{hit}} < 25/\sqrt{12} \sim 7.2 \mu\text{m} \text{ in } 25 \mu\text{m} \\ \sigma_{\text{hit}} < 100/\sqrt{12} \sim 28.9 \mu\text{m} \text{ in } 100 \mu\text{m} \end{cases}$$

After irradiation:

- plots show a module tuned to $\text{thr} = 1200 \text{ e-}$
- resolution below the binary limit and met requirement for masked pixels



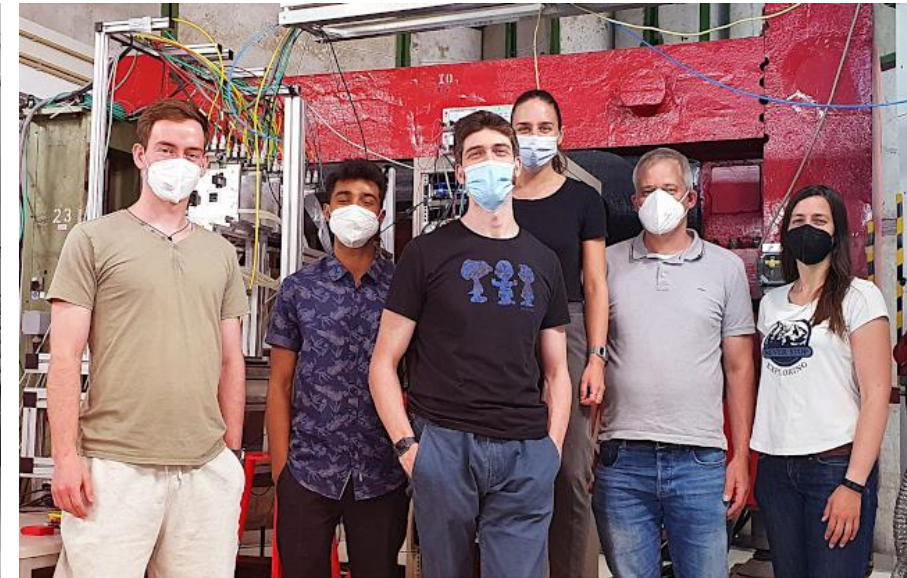
Conclusions

The preliminary analysis of test beam data acquired with modules irradiated to a variety of fluences has been presented:

- all requirements concerning the $\varepsilon_{\text{hit}} \times \alpha$, the σ_{hit} and the number of noisy pixels for RD53B_CMS assemblies have been met

Outlook:

- find the optimal configuration to operate the pixel assemblies throughout their lifetime at the LHC
- refine the analysis and simulate the obtained results as a function of fluence, threshold, bias voltage and incidence angle



Thank you for your attention!

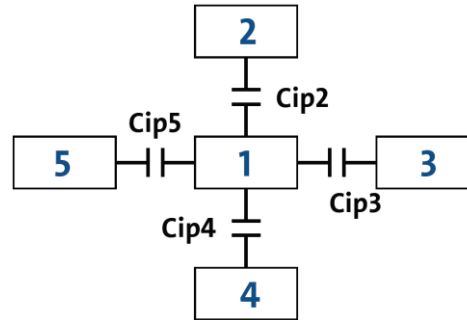
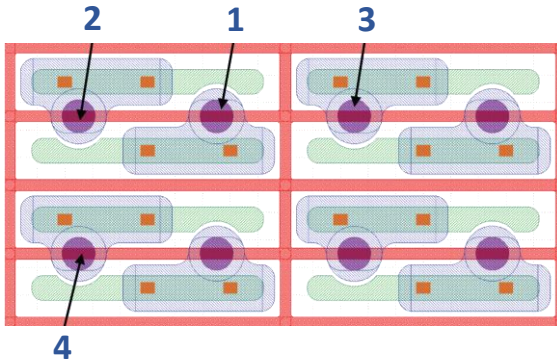


BACKUP

What is crosstalk?

When do we have crosstalk (XT)?

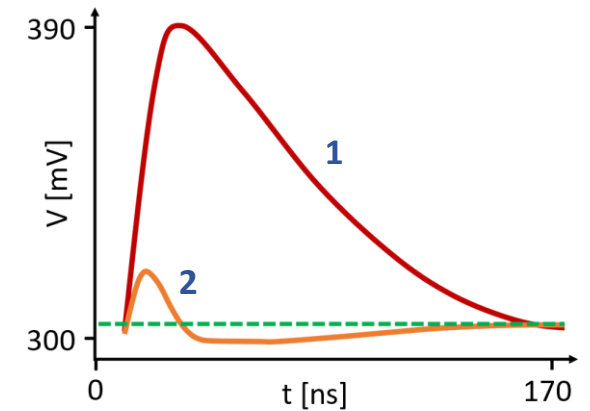
- When charge on one pixel induces a signal on a neighboring pixel → due to capacitive coupling.



From TCAD simulations:

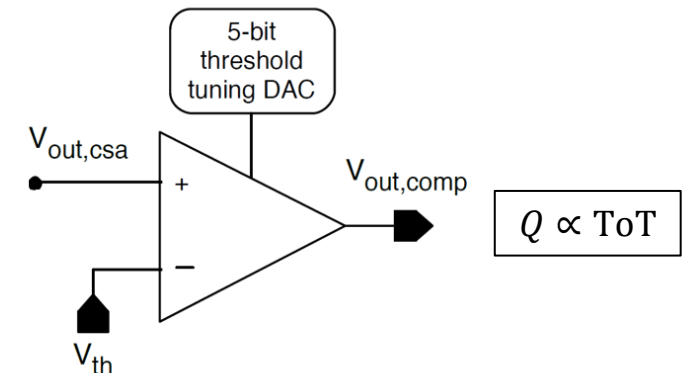
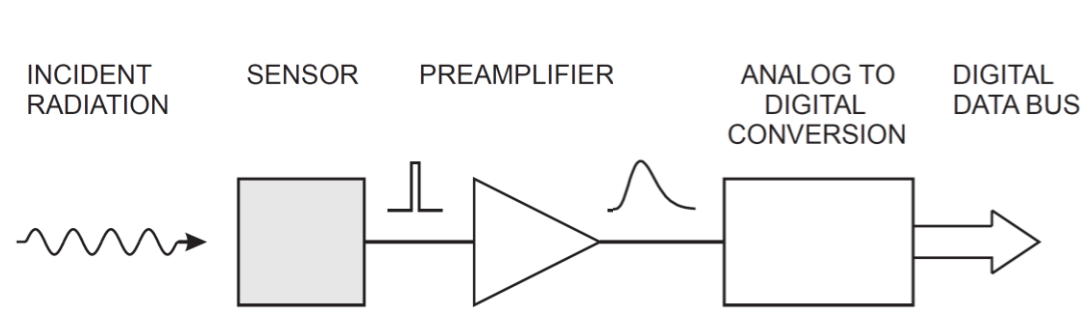
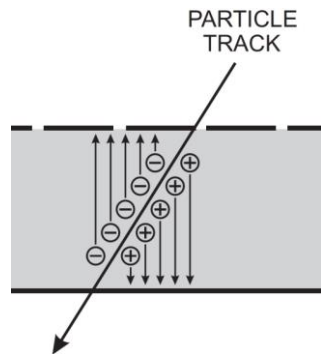
• **Values at -100 V sensor bias:**

- Cip2 = 14 fF
- Cip4 = 6.5 fF
- Cip3 = Chi5 = 2 fF



Why do we want to get rid of it?

- XT degrades the spatial resolution biasing the position of the reconstructed hit.



Hit detection probability

The probability of detecting a hit is a function of:

- Injected charge Q
- Threshold of the comparator
- Delay between injection and sampling

Detectable charge:

- $f(\Delta t)$
- 25 ns periodicity

The **highlighted red curve**:

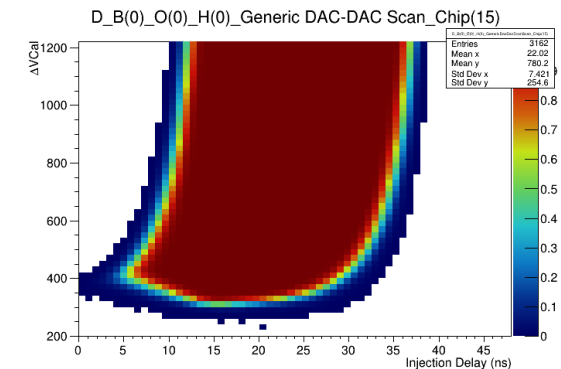
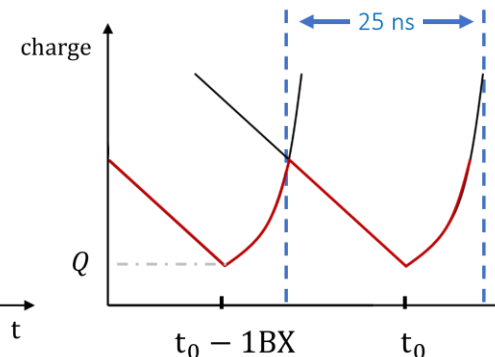
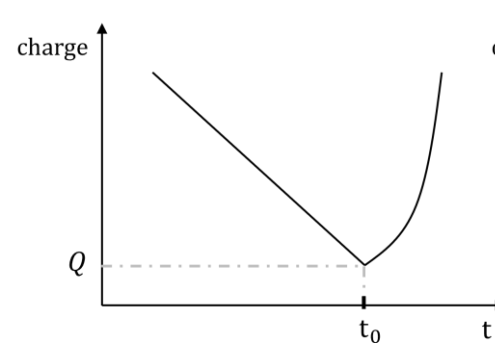
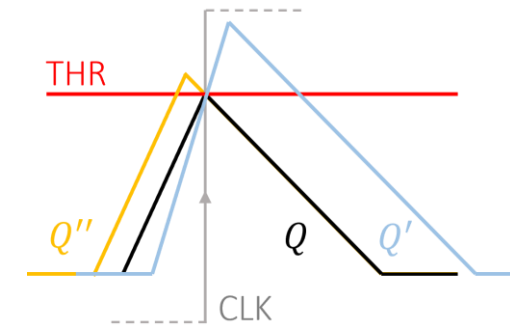
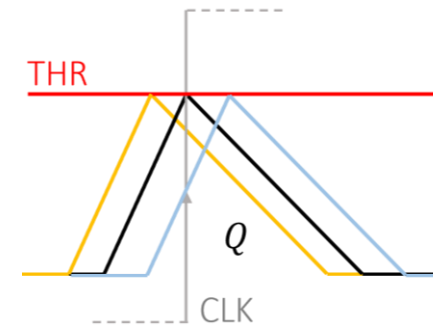
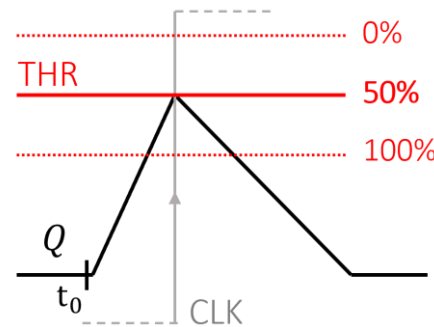
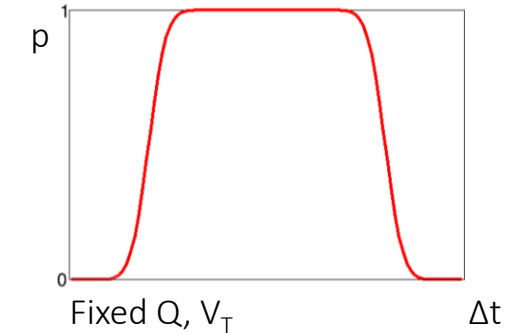
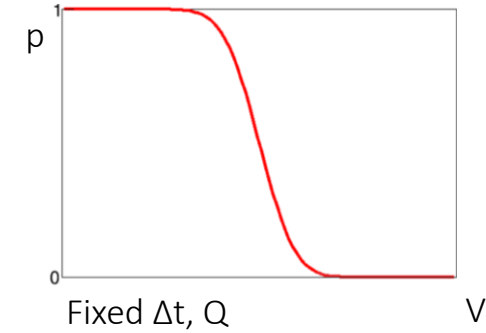
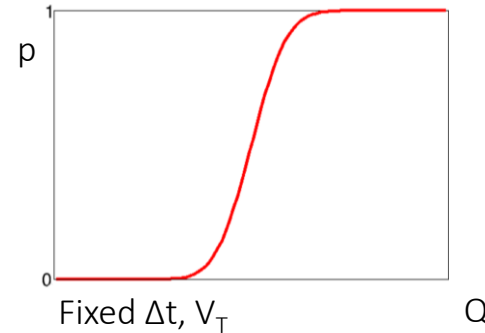
→ **effective threshold** of the detector

At the LHC the best timing will be chosen:

- Identifying the correct BX^0 to readout

Within BX^0 and for the correct t_0 the module:

- Exhibits the highest efficiency
- associated the lowest **threshold**
- found for the **best delay** t_0
- as for the **tuning procedure**



Crosstalk measurement procedure

The shape of the XT-induced signal differs from the one of the injected signal → respective thresholds can have different phase and minima

Procedure consists in changing the timing of the readout and measuring:

- Effective threshold **injected** pixel | threshold of the **coupled** pixel | threshold of the **uncoupled** pixel

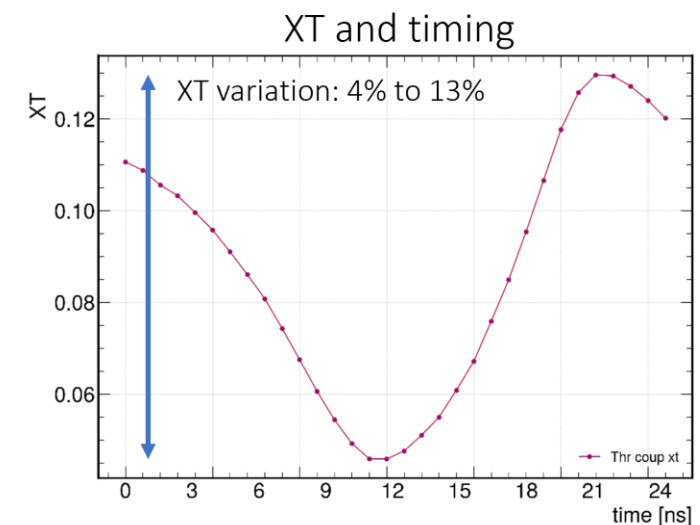
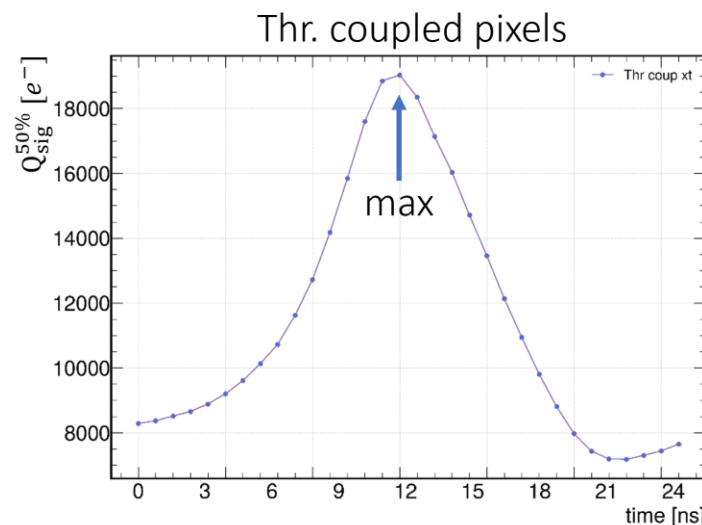
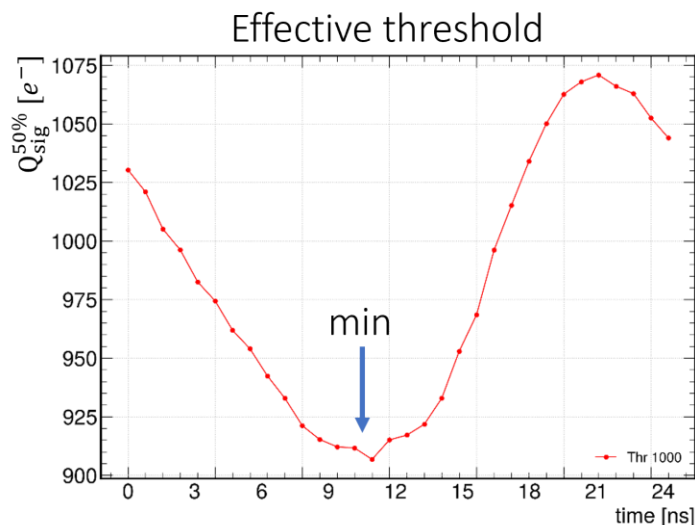
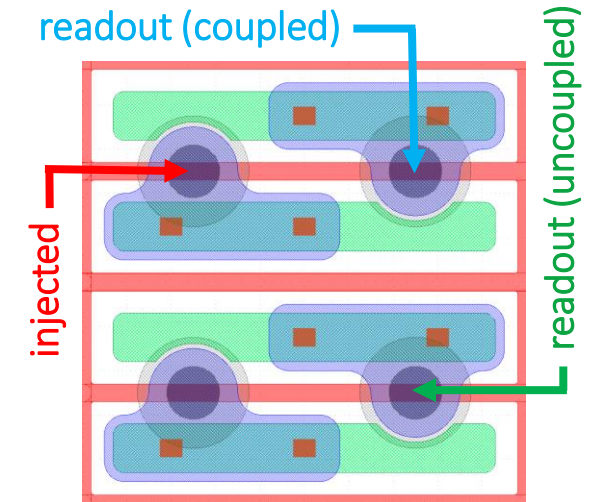
XT calculated for every fine delay:

$$XT_{un/coup}(t) = \frac{r_{un/coup}(t)}{1 + r_{un/coup}(t)}$$

$$r_{un/coup}(t) = \frac{Q_{sig,inj}^{50\%}(t)}{Q_{sig,un/coup}^{50\%}(t)}$$

oscillations in **anti-phase** → XT has a minimum
oscillations in **phase** → XT is stable

Anti-phase (RD53B_CMS):



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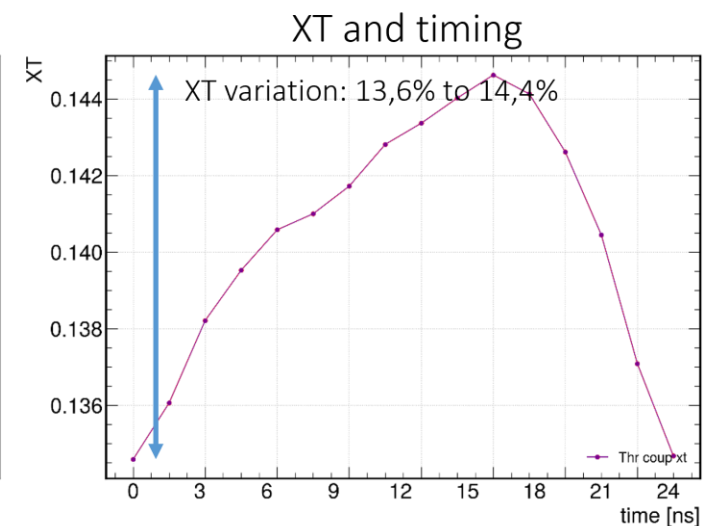
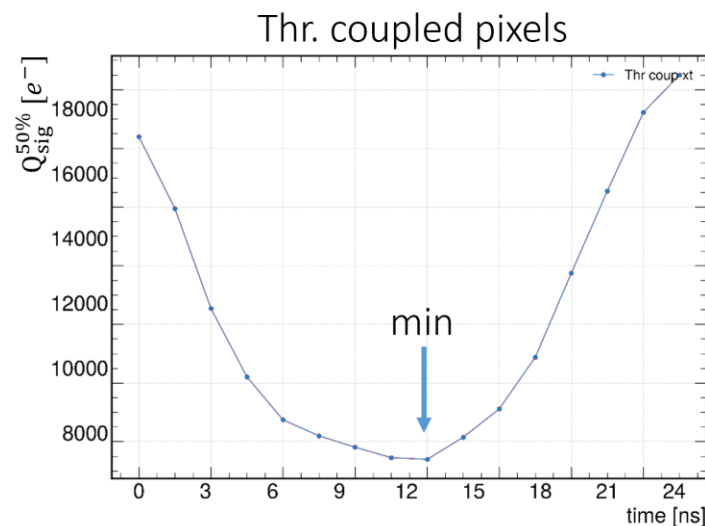
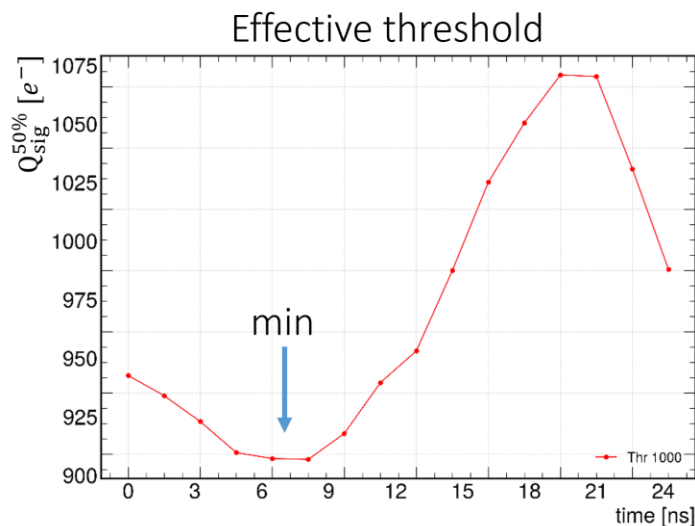
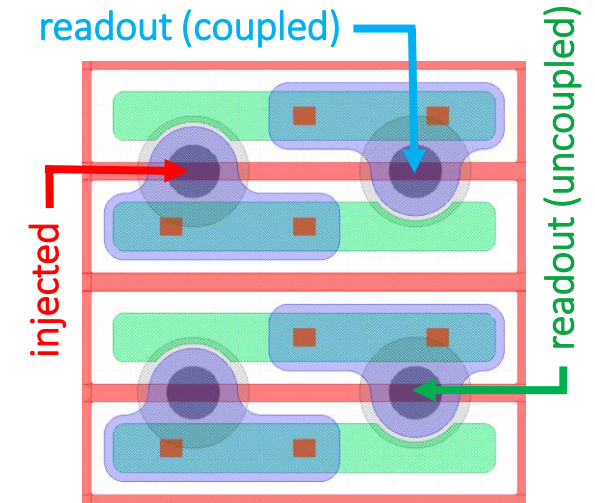
XT calculated for every fine delay:

$$XT_{un/coup}(t) = \frac{r_{un/coup}(t)}{1 + r_{un/coup}(t)}$$

$$r_{un/coup}(t) = \frac{Q_{sig,inj}^{50\%}(t)}{Q_{sig,un/coup}^{50\%}(t)}$$

oscillations in **anti-phase** → XT has a minimum
 oscillations in **phase** → XT is stable

Phase (RD53A):



XT was studied for CROC and RD53A assemblies with **bitten** HPK 25x100 planar sensors for different:

- pre-amp biasing | discharge modes: fast, slow | readout chips (RD53A and CROC) → always in **synchronous** mode | reading out **1 BX**

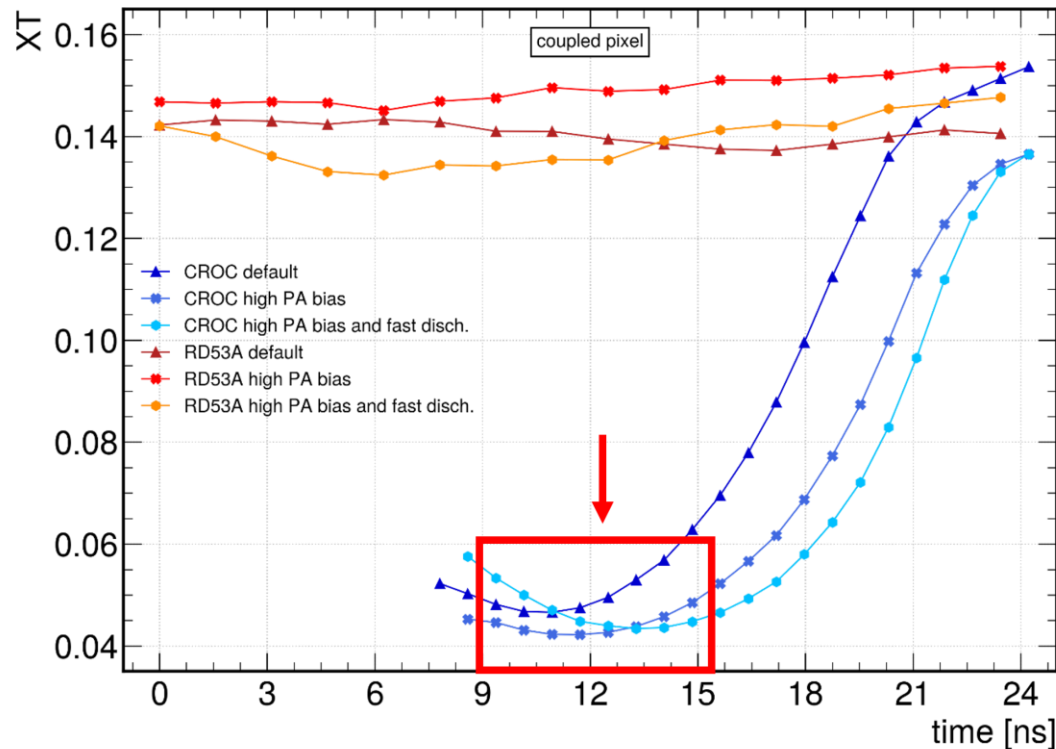
In the **region where the effective threshold is minimized**:

CROC
 $XT_{\text{coup}} = 4\% - 6\%$
 $XT_{\text{uncoup}} = 2.4\% - 2.8\%$

RD53A
 $XT_{\text{coup}} = 14\%$
 $XT_{\text{uncoup}} = 4.5\%$

crosstalk < 10%

XT and fine delay for coupled pixels



XT and fine delay for uncoupled pixels

