CMOS Sensors for Lepton Collider Experiments

WP1: Simulations

- Task 1: Sensor Optimization / TCAD
- Task 2: Detector Performance / Allpix Squared
- Task 3: Physics Performance / Geometry Optimization

WP2: ASIC Design

- Task 1: Pixel Front-End
- Task 2: Matrix Architecture & Readout
- Task 3: Periphery, DACs & Slow Control
- Task 4: Transceivers & Readout

WP3: Data Acquisition

- Task 1: Caribou 2 Development
- Task 2: Demonstrator Preparation (SW, FW, PCB)

WP4: Testing & Characterization

- Task 1: Summary of current 65nm Demonstrator Results
- Task 2: Lab Characterization, FE Optimization, Calibration
- Task 3: Testbeam Characterization, Performance

WP5: System Demonstrator

- Task 1: Concept, Design of Mechanics & Cooling
- Task 2: Prototype Construction