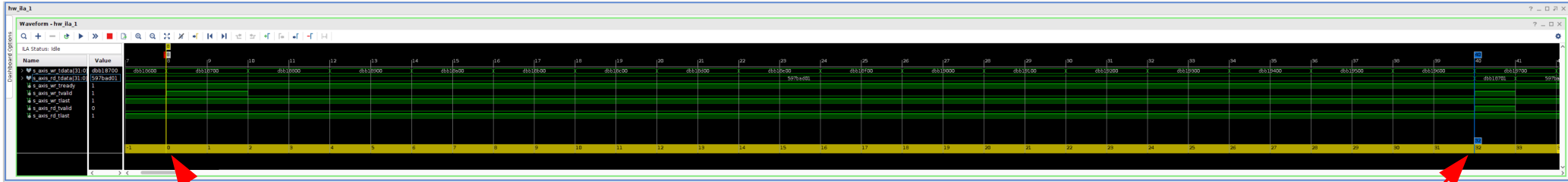


Using Versal AI-Engines for neural networks on FPGAs

Latency measurement

- Feeding AI engine with 32bit words @312.5 MHz from PL
- Increment data words by one in AI engine
- Reading back 32bit words @625 MHz



T=8
Writing into AIE

T=40
Reading incremented data from AIE

- Latency: $(40 - 8) \text{ clock cycles} = 32 / 625 \text{ MHz} = 51.2 \text{ ns}$