

2nd PUNCH4NFDI TA5 Workshop on Machine Learning and Data Processing on FPGAs

Wednesday 18 September 2024

Session 1 - REC B214 (14:00 - 15:30)

-Conveners: Arno Straessner; Johann Christoph Voigt; Andreas Redelbach

time	[id] title	presenter
14:00	[18] Welcome to the Workshop	
14:10	[21] Experience with Xilinx Versal AI and status and news from Mainz group	KAHRA, Christian
14:35	[20] Deep Learning for Real-time Classification of Astronomical Radio Signals: Current Status	KAZANTSEV, Andrei
15:00	[22] Track reconstruction for the ATLAS Phase-II Event Filter using Graph Neural Networks on FPGAs	DITTMEIER, Sebastian