

2nd PUNCH4NFDI TA5 Workshop on Machine Learning and Data Processing on FPGAs

Wednesday 18 September 2024

Session 2 - REC B214 (16:00 - 18:00)

-Conveners: Arno Straessner; Johann Christoph Voigt; Andreas Redelbach

time	[id] title	presenter
16:00	[23] Comparison of massively parallel and fully serial sorting on FPGAs	MEUSER, Emanuel
16:20	[24] CNNs for real time signal reconstruction in ATLAS LAr: Status and news from TU Dresden group	VOIGT, Johann Christoph
16:40	[19] Hands-on Tutorial: Running Machine Learning models on an Alveo card using hls4ml	MEN, Yunpeng